

Vishay

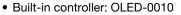
COMPLIANT

# 200 x 16 Graphic OLED

### **FEATURES**

• Type: Graphic

• Display format: 200 x 16 dots



• Duty cycle: 1/16

• +5 V power supply, +3 V optional

• Interface: 6800, option 8080 and SPI

• Sunlight readable and polarizer optional

 Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

| 0          | _ | 0     |
|------------|---|-------|
| 0000000000 |   |       |
| Ö          |   | <br>0 |

| MECHANICAL DATA  |                           |      |  |  |  |
|------------------|---------------------------|------|--|--|--|
| ITEM             | STANDARD VALUE            | UNIT |  |  |  |
| Module dimension | 182.0 x 38.5 x 9.3 (max.) |      |  |  |  |
| Viewing area     | 154.4 x 16.50             |      |  |  |  |
| Active area      | 123.95 x 11.15            | mm   |  |  |  |
| Dot size         | 0.57 x 0.65               | mm   |  |  |  |
| Dot pitch        | 0.62 x 0.70               |      |  |  |  |
| Mounting hole    | 175.0 x 26.5              |      |  |  |  |

| ABSOLUTE MAXIMUM RATINGS |                                    |         |          |      |  |
|--------------------------|------------------------------------|---------|----------|------|--|
| ITEM                     | SYMBOL                             | STANDAF | UNIT     |      |  |
| I I EIVI                 | STIVIBUL                           | MIN.    | MAX.     | UNIT |  |
| Supply voltage for logic | V <sub>DD</sub> to V <sub>SS</sub> | -0.3    | 5.3      | V    |  |
| Input voltage            | VI                                 | -0.3    | $V_{DD}$ |      |  |

#### Note

•  $V_{SS} = 0 \text{ V}, V_{DD} = 3.0 \text{ V}/5.0 \text{ V}$ 

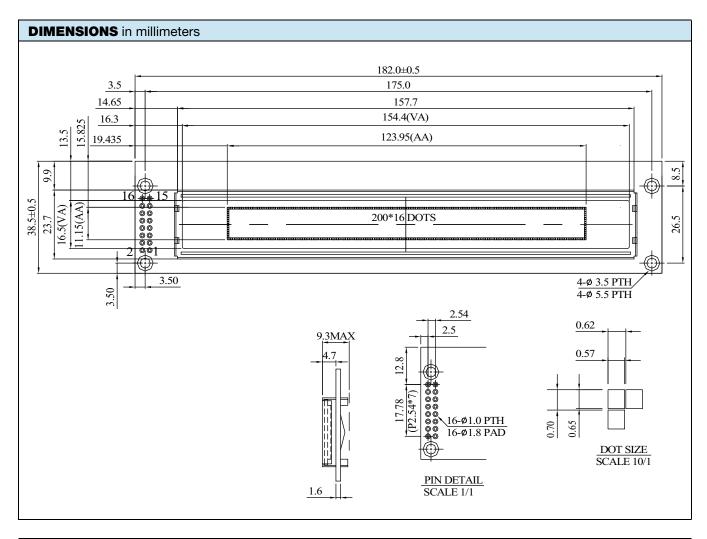
| ELECTRICAL CHARACTERISTICS |                                    |                           |                     |       |                     |      |  |
|----------------------------|------------------------------------|---------------------------|---------------------|-------|---------------------|------|--|
| ITEM                       | SYMBOL                             | CONDITION                 | ST                  | LINUT |                     |      |  |
| IIEW                       | STNIBOL                            | CONDITION                 | MIN.                | TYP.  | MAX.                | UNIT |  |
| Supply voltage for logic   | V <sub>DD</sub> to V <sub>SS</sub> | -                         | 3.0                 | 5.0   | 5.3                 | V    |  |
| Input high voltage         | V <sub>IH</sub>                    | -                         | 0.9 V <sub>DD</sub> | -     | $V_{DD}$            | V    |  |
| Input low voltage          | V <sub>IL</sub>                    | -                         | GND                 | -     | 0.1 V <sub>DD</sub> | V    |  |
| Output high voltage        | V <sub>OH</sub>                    | I <sub>OH</sub> = 0.5 mA  | 0.8 V <sub>DD</sub> | -     | $V_{DD}$            | V    |  |
| Output low voltage         | V <sub>OL</sub>                    | $I_{OL} = 0.5 \text{ mA}$ | GND                 | -     | 0.2 V <sub>DD</sub> | V    |  |
| Supply current             | I <sub>DD</sub>                    | V <sub>DD</sub> = 5 V     | -                   | 60    | -                   | mA   |  |

| OPTIONS | S              |     |      |       |        |       |     |      |       |
|---------|----------------|-----|------|-------|--------|-------|-----|------|-------|
|         | EMITTING COLOR |     |      |       |        |       | MOQ |      |       |
| YELLOW  | GREEN          | RED | BLUE | WHITE | YELLOW | GREEN | RED | BLUE | WHITE |
| Υ       | Υ              | Y   | Υ    | Υ     | N      | Y     | Υ   | Y    | Υ     |



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| INTERFACE PIN FUNCTION |                 |  |  |  |  |
|------------------------|-----------------|--|--|--|--|
| PIN NO.                | SYMBOL          | FUNCTION   |  |  |  |
| 1                      | V <sub>SS</sub> | Ground   |  |  |  |
| 2                      | $V_{DD}$        | Supply voltage for logic   |  |  |  |
| 3                      | NC              | No connection  |  |  |  |
| 4                      | RS              | H: Data; L: Instruction code   |  |  |  |
| 5                      | R/W             | H: Read (MPU $\leftarrow$ Module); L: Write (MPU $\rightarrow$ Module) |  |  |  |
| 6                      | E               | $H \rightarrow L$ enable signal  |  |  |  |
| 7                      | DB0             | Data bit 0   |  |  |  |
| 8                      | DB1             | Data bit 1   |  |  |  |
| 9                      | DB2             | Data bit 2   |  |  |  |
| 10                     | DB3             | Data bit 3   |  |  |  |
| 11                     | DB4             | Data bit 4   |  |  |  |
| 12                     | DB5             | Data bit 5   |  |  |  |
| 13                     | DB6             | Data bit 6   |  |  |  |
| 14                     | DB7             | Data bit 7   |  |  |  |
| 15                     | CS1             | Chip1 select input pin   |  |  |  |
| 16                     | CS2             | Chip2 select input pin   |  |  |  |





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