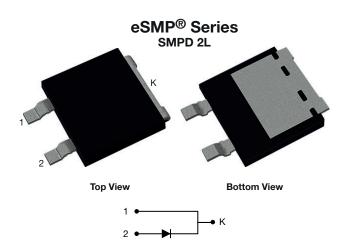


# **Surface-Mount ESD Capability Rectifiers**



## **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	10 A				
V <sub>RRM</sub>	400 V, 600 V				
I <sub>FSM</sub>	110 A				
$V_F$ at $I_F = 10 \text{ A } (T_J = 125 ^{\circ}\text{C})$	0.96 V				
I <sub>R</sub>	15 μA				
T <sub>J</sub> max.	175 °C				
Package	SMPD 2L				
Circuit configuration	Single				

### **FEATURES**

Creepage and clearance distance 3.7 mm typical



ROHS

HALOGEN

FREE

- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- · Oxide planar chip junction
- Low forward voltage drop
- ESD capability
- AEC-Q101 qualified available
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

## TYPICAL APPLICATIONS

General purpose, power line polarity protection, in both consumer and automotive on board charger (OBC) applications.

## **MECHANICAL DATA**

Case: SMPD 2L

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and industry grade

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified **Terminals:** matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SE10DTG	SE10DTJ	UNIT		
Device marking code		SE10DTG	SE10DTJ	V		
Maximum repetitive peak reverse voltage	$V_{RRM}$	400	600	V		
Market on BO for each a south	I <sub>F</sub> <sup>(1)</sup>	10		^		
Maximum DC forward current	I <sub>F</sub> <sup>(2)</sup>	2.9		A		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	11	А			
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to	°C			

### **Notes**

(1) With heatsink

<sup>(2)</sup> Free air, mounted on recommended copper pad area



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5 A	- T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.95	-	V
	I <sub>F</sub> = 10 A			1.04	1.15	
	I <sub>F</sub> = 5 A	- T <sub>J</sub> = 125 °C		0.85	=	
	I <sub>F</sub> = 10 A			0.96	1.10	
Reverse current	Rated V <sub>R</sub>	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	=	15	μА
	naieu v <sub>R</sub>	T <sub>J</sub> = 125 °C		22	150	
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>	3000	-	ns
Typical junction capacitance	4.0 V, 1 MHz		CJ	67	=	pF

## Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °c unless otherwise noted)					
PARAMETER SYMBOL TYP. MAX. UN					
Thermal resistance	R <sub>0JA</sub> (1)(2)	60	75	°C/W	
THEITIGI TESISLATICE	R <sub>0JM</sub> (3)	1.8	2.2		

- $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint (3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

IMMUNITY TO ELECTRICAL STATIC DISCHARGE TO THE FOLLOWING STANDARDS ( $T_A = 25~^{\circ}\text{C}$ unless otherwise noted)						
STANDARD TEST TYPE TEST CONDITIONS SYMBOL CLASS VALUE						
AEC-Q101-001	AEC-Q101-001 Human body model (contact mode) $C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega$ $V_C$ H3B $> 8 \text{ kV}$					

ORDERING INFORMATION (Example)						
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY DELIVERY MODE						
SE10DTJ-M3/I	0.52	I	2000 / reel	13" diameter plastic tape and reel		
SE10DTJHM3/I (1)	0.52	I	2000 / reel	13" diameter plastic tape and reel		

(1) AEC-Q101 qualified



## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

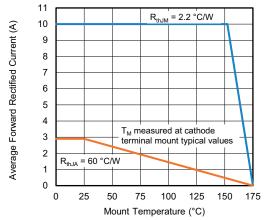
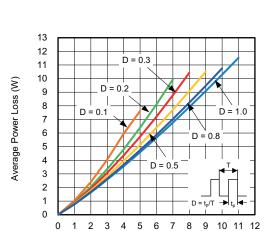


Fig. 1 - Forward Current Derating Curve



Average Forward Current (A)
Fig. 2 - Forward Power Loss Characteristics

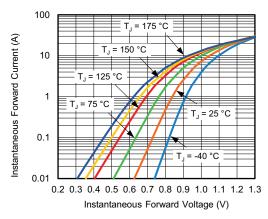


Fig. 3 - Typical Instantaneous Forward Characteristics

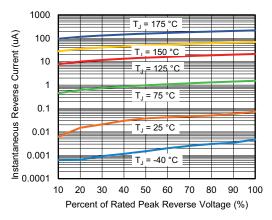


Fig. 4 - Typical Reverse Leakage Characteristics

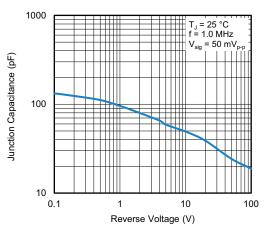


Fig. 5 - Typical Junction Capacitance

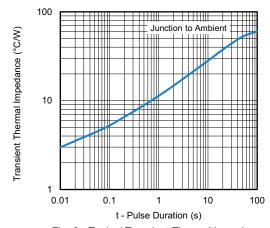
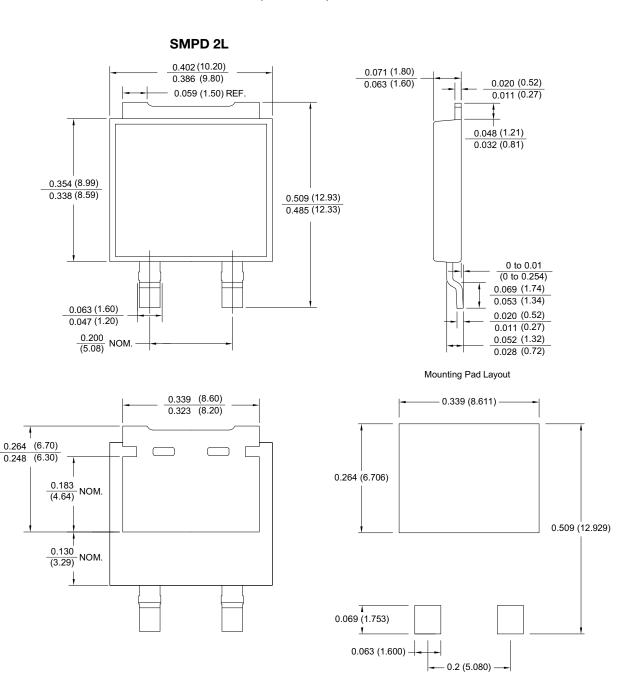


Fig. 6 - Typical Transient Thermal Impedance



## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



## Note

• The suggested mounting pad layout is provided for reference only, as actual pad layouts may vary depending on application



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