

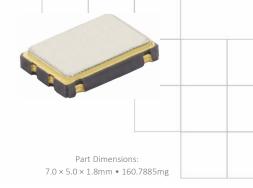
Model CA70C AUTOMOTIVE GRADE HCMOS CLOCK

Features

- AEC-Q200 Compliant
- Ceramic Surface Mount Package
- Operating Temperature Ranges to -55°C to +105°C
- Fundamental and 3rd Overtone Crystal Designs
- Frequency Range 1.25 156.25MHz *
- +1.8V, +2.5V, +3.3V Operation; +5.0V Limited Availability
- Output Enable Standard
- Tape and Reel Packaging, EIA-481

Applications

- Automotive Electronics
- Mobile Multimedia/Infotainment
- IoT and IIoT
- Wireless Communication



Standard Frequencies

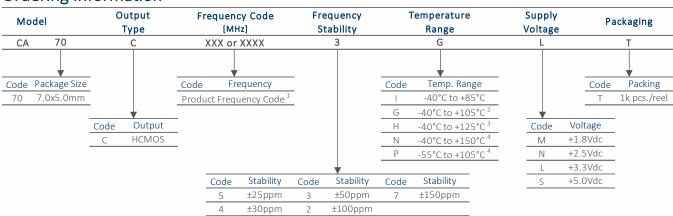
* See Page 6 for common frequencies. Check with factory for availability of frequencies not listed and for +5.0V operation.

- Medical Electronics
- Commercial Military & Aerospace

Description

CTS Model CA70C is a low cost, small size, Clock Oscillator [XO] developed for use in automotive electronics operating over extended temperature ranges. CA70C has an HCMOS/TTL compatible output, offers excellent stability and low jitter/phase noise performance.

Ordering Information



Notes:

- 1] Refer to document 016-1454-0, Frequency Code Tables. 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.
- 2] Available with stability codes 4, 3, 2 and 7.
- 3] Available with stability codes 3, 2 and 7.
- 4] Stability codes 2 and 7. Contact factory for availability. Not available with voltage code "S".

Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.



Electrical Specifications

Operating Conditions

PARAMETER	SYMBOL	YMBOL CONDITIONS		TYP	MAX	UNIT
Maximum Supply Voltage	V _{cc}	V _{cc} +1.8V to +3.3V	-0.5	-	4.0	V
			1.710	1.8	1.890	
Cumply Valtage	\ /	150/	2.375	2.5	2.665	\/
Supply Voltage	V _{CC}	±5%	3.135	3.3	3.465	V
			4.750	5.0	5.250	
	Гуріса	al @ Nominal Vcc, C _L = 15 pF, T _A = -	+25°C			
		@ +1.8V	-	15	25	
Supply Current	I _{cc}	@ +2.5V	-	20	30	mA
		@ +3.3V	-	35	40	
		@ +5.0V	-	35	55	
Output Load	C _L	-	-	-	15	pF
			-40		+85	
			-40		+105	
Operating Temperature	T_A	-	-40	+25	+125	°C
			-40		+150	
			-55		+105	
Storage Temperature	T _{STG}	-	-55	-	+125	°C

Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Frequency Range	f _O	- 1.25 - 156.2		.5	MHz	
Frequency Stability [Note 1]	$\Delta f/f_{O}$	-	25, 30, 50, 100 or 150			±ppm
Aging	$\Delta f/f_{25}$	First Year @ +25°C, nominal V_{CC} -5 - 5		ppm		
1.] Inclusive of initial tolerance at time of shipment, changes in supply voltage, load, temperature and 1st year aging.						

Output Parameters

PARAMETER	SYMBOL	SYMBOL CONDITIONS		TYP	MAX	UNIT
Output Type	put Type			HCMOS	5	-
Output Voltage Levels	V _{OH}	Logic '1' Level, CMOS Load	0.9V _{CC}	-	-	V
	V_{OL}	Logic '0' Level, CMOS Load	-	-	$0.1V_{CC}$	V
Outract Comment Levels	I _{OH}	$V_{OH} = 90\%V_{CC} [1.8V, 2.5V, 3.3V, 5.0V]$	-	-	-4, -4, -8, -16	mA
Output Current Levels	I _{OL}	$V_{OL} = 10\%V_{CC} [1.8V, 2.5V, 3.3V, 5.0V]$	-	-	+4, +4, +8, +16	IIIA
Output Duty Cycle	SYM	@ 50% Level	45	-	55	%
		@ 10%/90% Levels, Nominal V_{CC} , $C_L = 15pF$				
		@ +1.8V	-	4	5	
Rise and Fall Time [Note 2]	T_R , T_F	@ +2.5V	-	4	5	ns
[1000 2]		@ +3.3V	-	7	10	
		@ +5.0V	-	7	10	
Start Up Time	Ts	Application of V _{CC}	-	2	5	ms

2.] Parameters are worst case and account for comprehensive range of product specification. Performance may vary by application and must be validated by end user.



Electrical Specifications

Output Parameters

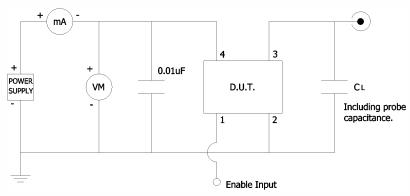
PARAMETER	SYMBOL	BOL CONDITIONS		TYP	MAX	UNIT
Enable Function	St	andby				
Enable Input Voltage	V_{IH}	Pin 1 Logic '1', Output Enabled	$0.7V_{CC}$	-	-	V
Disable Input Voltage	V_{IL}	Pin 1 Logic '0', Output Standby	-	-	$0.3V_{CC}$	V
Enable Current	I_{STB}	Pin 1 Logic '0', Output Standby	-	-	10	μΑ
Enable Time	T_{PLZ}	Pin 1 Logic '1'	-	-	5	ms
Phase Jitter, RMS	tjrms	Bandwidth 12 kHz - 20 MHz	-	0.5	< 1	ps

Enable Truth Table

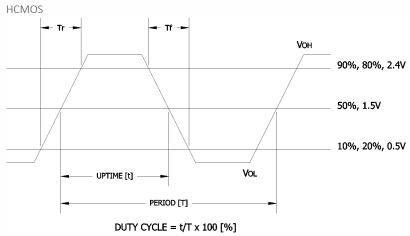
Pin 1	Pin 3			
Logic '1'	Output Enabled			
Open	Output Enabled			
1 (0)	Output Disabled,			
Logic '0'	High Impedance			

Test Circuit

HCMOS

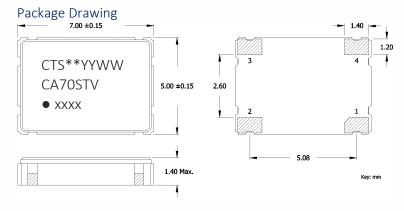


Output Waveform

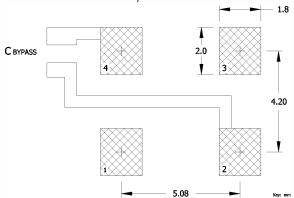


Model CA70C AUTOMOTIVE GRADE HCMOS CLOCK

Mechanical Specifications



Recommended Pad Layout



Pin Assignments

Pin	Symbol	Function
1	EOH	Enable
2	GND	Circuit & Package
3	Output	RF Output
4	V_{CC}	Supply Voltage

Marking Information

- 1. ** Manufacturing Site Code.
- 2. YYWW Date Code, YY = year, WW = week.
- 3. CA70 CTS model.
- 4. ST Frequency stability/temperature code. [Refer to Ordering Information]
- 5. V Voltage code. M = 1.8V, N = 2.5V, L = 3.3V, S = 5.0V.
- 3. – Pin 1 identifier.
- 4. xxxx Frequency Code; 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.

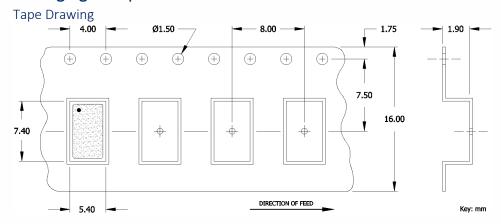
[See document 016-1454-0, Frequency Code Tables.]

Notes

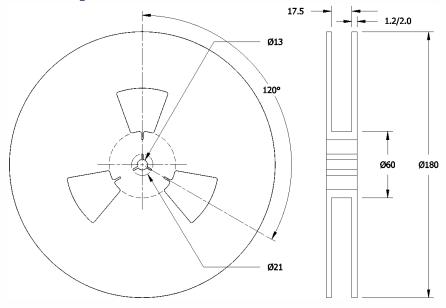
- 1. Termination pads (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- 2. Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.



Packaging - Tape and Reel



Reel Drawing



Notes

- 1. Device quantity is 1k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.



Model CA70C AUTOMOTIVE GRADE HCMOS CLOCK

Addendum

Common Frequencies Available – MHz

FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE	FREQUENCY	FREQUENCY CODE
4.000000	040	24.000000	240	40.000000	400		
8.000000	080	24.576000	24C	48.000000	480		
10.000000	100	25.000000	250	50.000000	500		
12.000000	120	26.000000	260	100.000000	1000		
12.288000	122	27.000000	270	125.000000	1250		
14.318180	143	30.000000	300	156.250000	1562		
14.745600	147	32.000000	320				
16.000000	160	33.333000	33E				
20.000000	200	37.400000	374				
22.118400	221	38.400000	384				