



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Reverse voltage			V _R	6	V
Forward current			I _F	60	mA
Surge current			I _{FSM}	2.5	A
Power dissipation			P _{diss}	100	mW
Derate linearly from 25 °C				1.33	mW/°C
Thermal resistance			R _{th}	750	°C/W
OUTPUT					
Peak off-state voltage		IL4216	V _{DRM}	600	V
		IL4217	V _{DRM}	700	V
		IL4218	V _{DRM}	800	V
RMS on-state current			I _{DRM}	300	mA
Single cycle surge			I _{TSM}	3	A
Power dissipation			P _{diss}	300	mW
Derate linearly from 25 °C				6.6	mW/°C
Thermal resistance			R _{th}	150	°C/W
COUPLER					
Storage temperature			T _{stg}	-55 to +150	°C
Ambient temperature			T _{amb}	-55 to +100	°C
Lead soldering temperature ⁽¹⁾	5 s		T _{slid}	260	°C

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability
- ⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP)

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 20 mA		V _F	-	1.3	1.5	V
Breakdown voltage	I _R = 10 μA		V _{BR}	6	30	-	V
Reverse current	V _R = 6 V		I _R	-	0.1	10	μA
Input capacitance	V _F = 0 V, f = 1 MHz		C _{IN}	-	40	-	pF
Thermal resistance, junction to lead			R _{thjl}	-	750	-	°C/W
OUTPUT							
Repetitive peak off-state voltage	I _{DRM} = 100 μA	IL4216	V _{DRM}	600	650	-	V
		IL4217	V _{DRM}	700	750	-	V
		IL4218	V _{DRM}	800	850	-	V
Off-state voltage	I _{D(RMS)} = 70 μA	IL4216	V _{D(RMS)}	424	460	-	V
		IL4217	V _{D(RMS)}	484	536	-	V
		IL4218	V _{D(RMS)}	565	613	-	V
Off-state current	V _D = 600 V, T _{amb} = 100 °C		I _{D(RMS)}	-	10	100	μA
Reverse current	V _R = 600 V, T _{amb} = 25 °C		I _{RMS}	-	10	100	μA
On-state voltage	I _T = 300 mA		V _{TM}	-	1.7	3	V
On-state current	PF = 1, V _{T(RMS)} = 1.7 V		I _{TM}	-	-	300	mA
Surge (non-repetitive, on-state current)	f = 50 Hz		I _{TSM}	-	-	3	A
Holding current	V _T = 3 V		I _H	-	65	200	μA
Latching current	V _T = 2.2 V		I _L	-	-	500	μA
LED trigger current	V _{AK} = 5 V		I _{FT}	-	0.7	-	mA



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
OUTPUT							
Critical rate of rise of off-state voltage	V _D = 0.67 V _{DRM} , T _{amb} = 25 °C		dV/dt _{cr}	10 000	-	-	V/μs
	V _D = 0.67 V _{DRM} , T _{amb} = 80 °C		dV/dt _{cr}	5000	-	-	V/μs
Critical rate of rise of voltage at current commutation	V _D = 230 V _{RMS} , I _D = 300 mA _{RMS} , T _J = 25 °C		dV/dt _{crq}	-	8	-	V/μs
	V _D = 230 V _{RMS} , I _D = 300 mA _{RMS} , T _J = 85 °C		dV/dt _{crq}	-	7	-	V/μs
Critical rate of rise of on-state current commutation	V _D = 230 V _{RMS} , I _D = 300 mA _{RMS} , T _J = 25 °C		dI/dt _{crq}	-	12	-	A/ms
Thermal resistance, junction to lead			R _{thjl}	-	150	-	°C/W
COUPLER							
Capacitance (input to output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}	-	0.8	-	pF
Critical rate of rise of coupled input to output voltage	I _T = 0, V _{RM} = V _{DM} = 300 V _{AC}		dV _(IO) /dt	5000	1	-	mA

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

POWER FACTOR CONSIDERATIONS

A snubber is not needed to eliminate false operation of the TRIAC driver because of the IL4216, IL4217, IL4218 high static and commutating dV/dt with loads between 1 and 0.8 power factors. When inductive loads with power factors less than 0.8 are being driven, include a RC snubber or a single capacitor directly across the device to damp the peak commutating dV/dt spike. Normally a commutating dV/dt causes a turning-off device to stay on due to the stored energy remaining in the turning-off device.

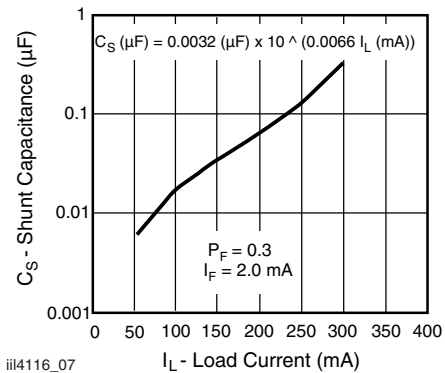


Fig. 1 - Shunt Capacitance vs. Load Current vs. Power Factor

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	8000	V _{peak}
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	500	mW
Input safety current		I _{SI}	250	mA
Safety temperature		T _S	175	°C
Creepage distance	DIP-6; SMD-6, option 7; SMD-6, option 9		≥ 7	mm
	DIP-6, 400 mil, option 6		≥ 8	mm
Clearance distance	DIP-6; SMD-6, option 7; SMD-6, option 9		≥ 7	mm
	DIP-6, 400 mil, option 6		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm

Note

- As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

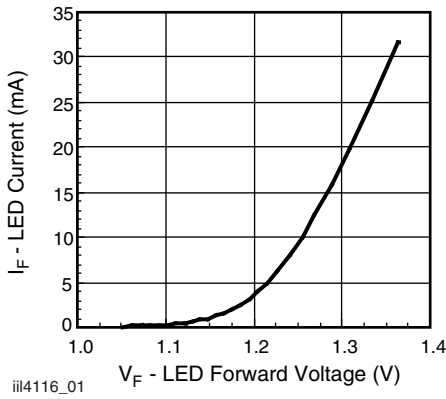


Fig. 2 - LED Forward Current vs. Forward Voltage

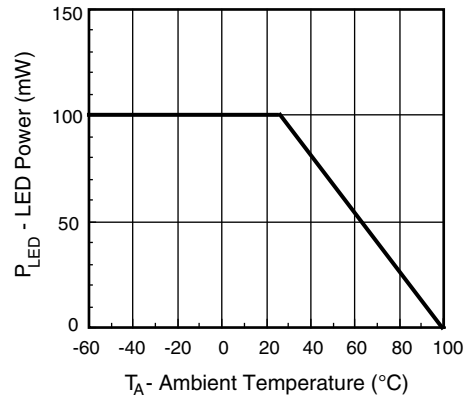


Fig. 5 - Maximum LED Power Dissipation

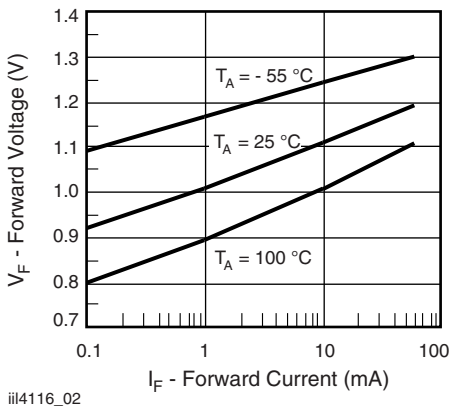


Fig. 3 - Forward Voltage vs. Forward Current

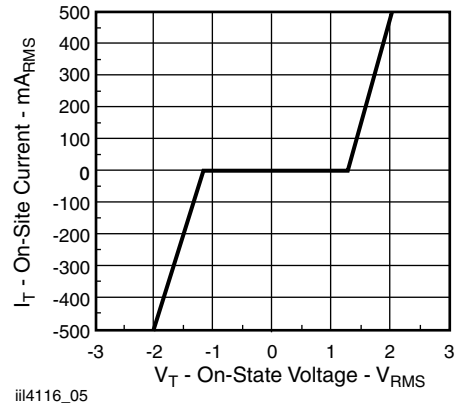


Fig. 6 - On-State Terminal Voltage vs. Terminal Current

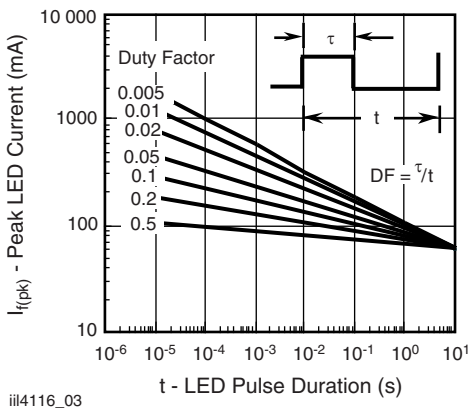


Fig. 4 - Peak LED Current vs. Duty Factor, τ

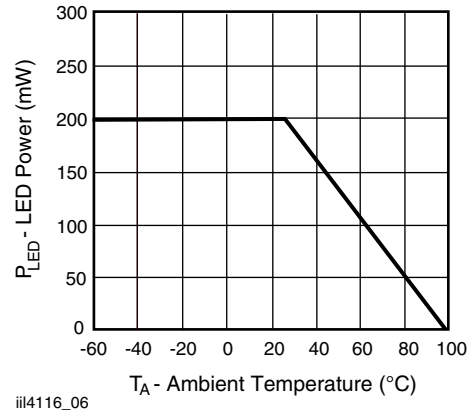
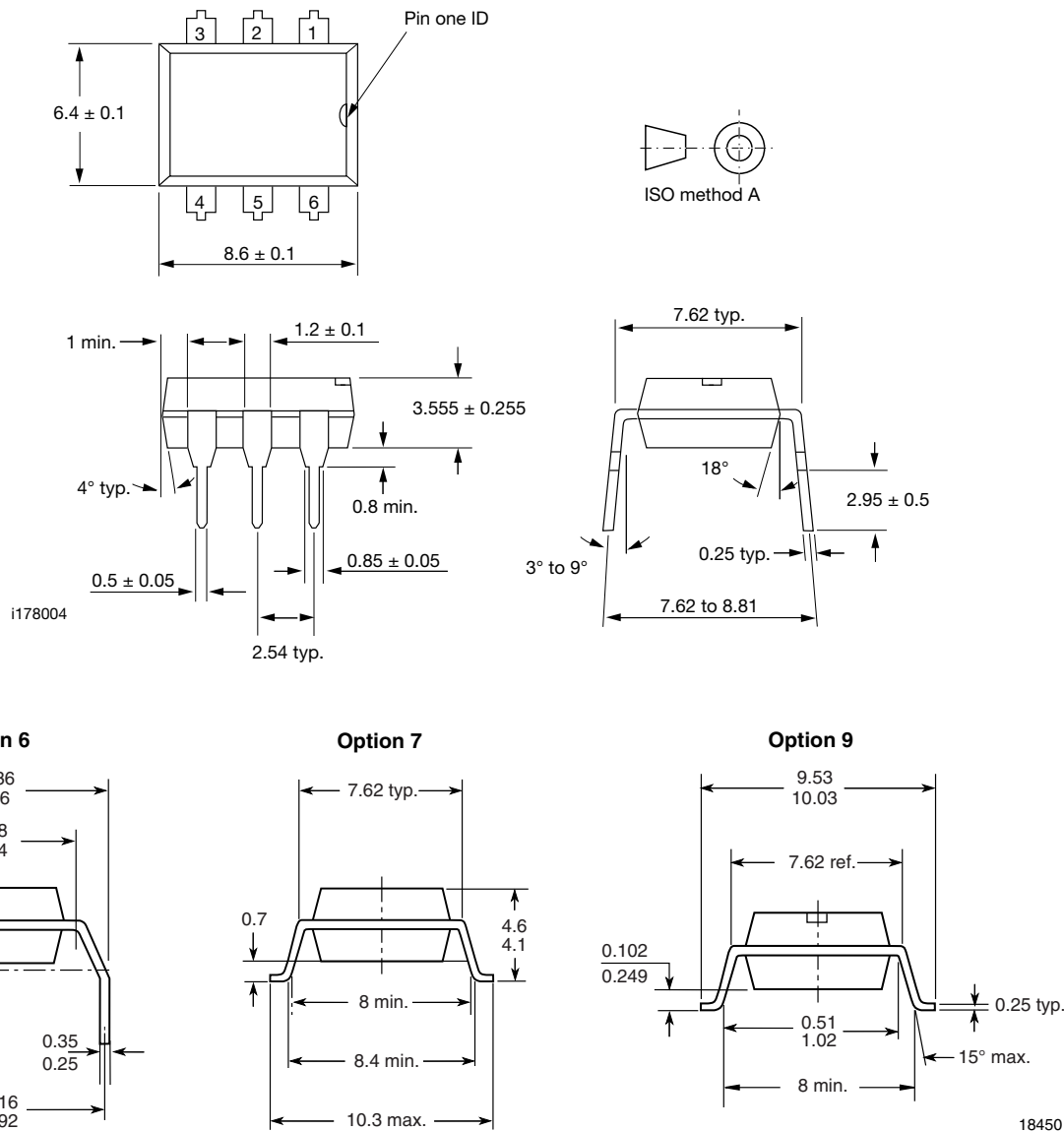


Fig. 7 - Maximum Output Power Dissipation

PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)

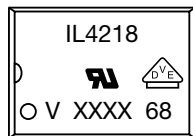
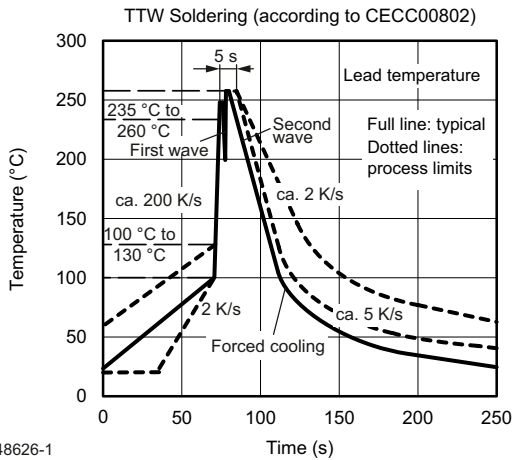


Fig. 8 - Example of IL4218-X017T

Notes

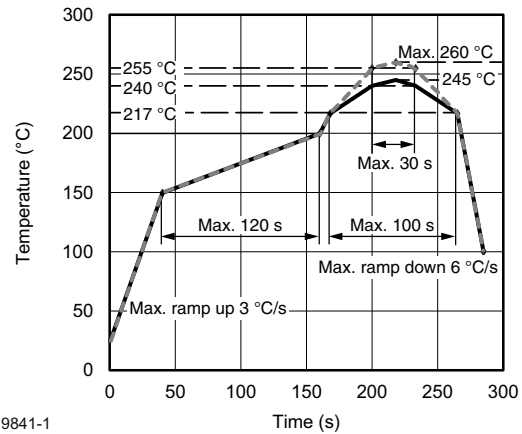
- XXXX = LMC (lot marking code)
- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

SOLDER PROFILES



948626-1

Fig. 9 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices



19841-1

Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: $T_{amb} < 30\text{ °C}$, $RH < 85\%$

Moisture sensitivity level 1, according to J-STD-020



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