

APPROVAL SHEET

	MODEL NO.: SMD0603-020-24V	
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CUSTOMER:

CUSTOMER'S APPROVAL:

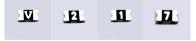
AUTHORIZED SIGNATURE/STAMP:

DATE

MANUFACTURER:	
HEAD OFFICE:	
	13F.,No.120-10,Sec.3,Zhongshan Rd.,Zhonghe Dist.,New Taipei City 23544,Taiwan
	Tel: 886-2-8221-2567
	Fax:882-2-2225-7268
	E-mail:service@chipfast.com.tw
China Branch:	
	Factory Building B)Shuangpeng,Weibu Village, Qiuchang Town,
	Huiyang District, Huizhou City, Guangdong Province, P.R.C.)
	Tel: 86-752-3562001
	Fax:86-752-3558696
	E-mail:service@atpptc.com

Submitted by: Approved by: DATE: Chen YC Lin 3-Mar-23

SEA & LAND ELECTRONIC CORP.



Features

Surface Mount Devices

- Lead free device
- Size 1.5*0.8 mm / 0.06*0.03 inch
- Surface Mount packaging for automated assembly

Applications

Almost anywhere there is a low voltage power supply, up to 15V and a load to be protected, including: Computer mother board, Modem. USB hub PDAs & Charger, Analog & digital line card Digital cameras, Disk drivers, CD-ROMs,

Alpha-Top (Sea & Land Alliance)

SMD0603-020-24V

Performance Specification

							Maxi		Resis	stance						
Model	Marking	V _{max}	max	hold		P _d		To Trip	D;	R1max	Agency A	Approval				
		(Vdc)	(A)	@25°C (A)	@25°C (A)	Тур. (W)	Current (A)	Time (Sec)	Ri _{min} (Ω)	K IIIIax (Ω)	UL	TUV				
SMD0603-020-24V	2	24	40	0.20	0.50	0.5	1.0	0.60	0.550	3.500						
Ihold = Hold Current. Maximum current device will not trip in 25°C still air.																
Itrip = Trip Current. Minimum current at which the device will always trip in 25°C still air.																
Vmax = Maximum operating voltage device can withstand without damage at rated current (Imax).																
Imax = Maximum fault current device can withstand without damage at rated voltage (Vmax).																
Pd = Power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.																
Rimin/max = Minimum/Maximum device resistance prior to tripping at 25°C.																
R1 _{max} = Maximum dev	vice resistant	ce is measur	ed one hour	post reflow.												
CAUTION : Operation I	beyond the s	pecified ratir	igs may resu	ult in damage	e and possibl	le arcing and	flame.	CAUTION : Operation beyond the specified ratings may result in damage and possible arcing and flame.								

Environmental Specifications

Test	Conditions					
Passive aging	+85°C, 1000 hrs.					
Humidity aging	+85°C, 85% R.H. , 168 hours					
Thermal shock	+85°C to -40°C, 20 times					
Resistance to solvent	MIL-STD-202, Method 215					
Vibration	MIL-STD-202, Method 201					
Ambient operating conditions : - 40 °C to +85 °C						
Maximum surface temperature of the device in the tripped state is 125 °C						
In case of special use, please contact our engineer						

Agency Approvals :

PBRoHS	2015/863/EU
HF	EN14582

Ihold Versus Temperature

Model		Max	imum ambie	ent operating	temperature	e (T _{mao}) vs. h	old current (I _{hold})	
Model	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
SMD0603-020-24V	0.27	0.25	0.23	0.20	0.17	0.14	0.12	0.10	0.07

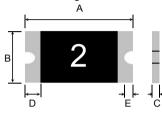
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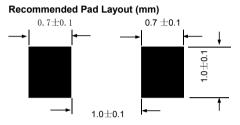
Construction And Dimension (Unit:mm)

Model	ļ	4		3	(•	D	E
Woder	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Min.
SMD0603-020-24V	1.45	1.85	0.65	1.05	0.50	1.20	0.15	0.10

Dimensions & Marking







Termination Pad Characteristics

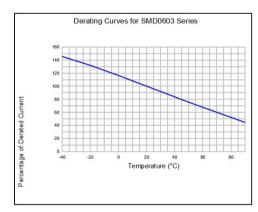
Terminal pad materials : Terminal pad solderability : Tin-plated Nickel-Copper

Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3.

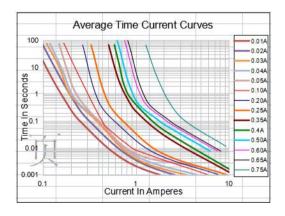
Rework

Use standard industry practices, the removal device must be replaced with a fresh one.

Thermal Derating Curve



Typical Time-To-Trip At 25°C



\Lambda WARNING:

· Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.

PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated. Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components. Use PPTC with a large inductance in circuit will generate a circuit voltage (L di/dt) above the rated voltage of the PPTC.

Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.

Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.
Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.

SMD0603-020-24V

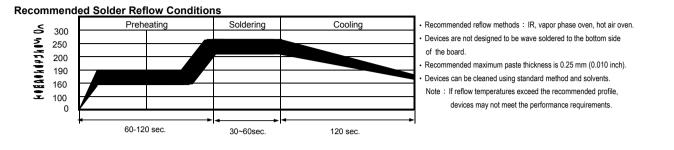
E₁

F w

W2(measured at hub)

W1(measured at hub)

N(hub dia.)



I← T

 T_1

Cover tapé

Carrier tape Embossed cavity

EIA Reel Dimensions

Tape And Reel Specifications (mm)

Paper Tape Component Dimensions

A

Δ

F

- P1 i-

Do

Governing Specifications	
W	8.0 ± 0.2
P ₀	4.0 ± 0.10
P ₁	4.0 ± 0.10
P ₂	2.0 ± 0.05
A ₀	1.05 ± 0.10
B ₀	1.85 ± 0.10
D ₀	1.55 + 0.05
F E ₁	3.5 ± 0.05
E ₁	1.75 ± 0.10
E ₂ min.	6.25
Т	0.75
T₁max.	0.1
K ₀	0.75/0.95 ± 0.1
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	60
W ₁	9.0 ± 0.5
W ₂	12.0 ± 0.05

Storage And Handling

• Storage conditions : 40°C max, 70% R.H.

· Devices may not meet specified performance

if storage conditions are exceeded.

Order Information

P	Packaging
020-24V	Tape & Reel Quantity
Hold	
Current	5,000 pcs/reel
0.20A	
	020-24V Hold Current

Tape & reel packaging per EIA481-1 Labeling Information

