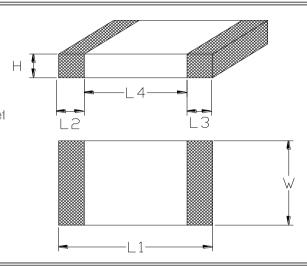


Multilayer Ceramic Chip Capacitor

1210J6300682JGTAF2

Knowles offers Multilayer Ceramic Chip Capacitors of the highest quality and reliability for a range of demanding applications. Manufactured using our "Wet Process", the range encompasses 10V to 12kV capability, with a variety of termination options including FlexiCap™, the worlds first commercially available flexible termination



Mechanical Specification

Size Code1210Length (L1) mm 3.2 ± 0.2 Width (W) mm 2.5 ± 0.2 Thickness (H) mm 2.0 ± 0.2 Termination Bands (L2,L3) mm0.25 - 0.75Minimum Band Gap (L4) mm1.4Termination Material100% Matte Sn over NiSolderabilityIEC 60068-2-58

General Electrical Specification

Rated Voltage 630V Nominal Capacitance Value 6.8nF Capacitance Tolerance ± 5% Tangent of Loss Angle (Tan δ) ≤0.001 Capacitance and Tan δ Test Conditions 1Vrms @ 1kHz Voltage Proof 1.5 x Rated Voltage (Voltage applied for 5 secs max. @ 50mA max. charge current) Min Insulation Resistance (IR) 100.00GΩ (IR Test Voltage applied @ 100V) Dielectric Classification COG (NPO) Rated Temperature Range -55°C to +125°C Maximum Capacitance Change over No DC Voltage 0±30ppm/°C Temperature Range Rated DC Voltage Climatic Category (IEC) 55/125/56 Ageing Characteristic Zero

Suffix AF2 indicates special dimension, packaging and test requirements for Huawei. This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at www.knowlescapacitors.com/syfer or by contacting us at syfersales@knowles.com

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eb: www.knowlescapacitors.com/syfer



RoHS Compliant

Packaging

Description:-

1210 630V 6.8nF ± 5% C0G (NP0)

Name: Js

2011/65/EU as amended by 2015/863/EU

Taped and Reeled, 7 inch Reel

The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consent of Knowles. and any customer mentioned within this specification Date: 25 April 2018

Filename: 1210J6300682JGTAF2-008.pdf