# Local/Remote Temperature Switches in a HMAX Package 

## General Description

The MAX6687/MAX6688 comprise a remote-junction temperature switch and a local temperature switch in a single package. The remote-junction switch uses an external P-N junction (typically a diode-connected transistor on the die of an external CPU, ASIC, or FPGA) as a sensing element to measure the remote temperature. The remote-junction temperature switch has a factoryprogrammed trip temperature threshold of either $+120^{\circ} \mathrm{C}$ or $+125^{\circ} \mathrm{C}$. The local temperature switch has a pin-programmable temperature threshold that is set by connecting pins S1 and S2 to ground, to VDD, or leaving them floating. Hysteresis for both local and remote thresholds is $5^{\circ} \mathrm{C}$. The MAX6687/MAX6688 do not assert on transient (single-sample) faults or when power is first applied.
The MAX6687 has two open-drain active-low outputs while the MAX6688 has two push-pull active-high outputs. Tremote asserts a logic signal when the remote temperature exceeds the factory-programmed $+120^{\circ} \mathrm{C}$ or $+125^{\circ} \mathrm{C}$ trip threshold. TLOCAL is asserted when the die temperature exceeds the pin-programmed threshold, which is controlled by pins S1 and S2. The local thresholds are available in two ranges in $5^{\circ} \mathrm{C}$ increments. The two ranges are $+40^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$ and $+75^{\circ} \mathrm{C}$ to $+115^{\circ} \mathrm{C}$.
The MAX6687/MAX6688 operate from a 3.0 V to 5.5 V power supply and are available in a space-saving 8-pin $\mu \mathrm{MAX}$ package.

Applications
CPU Temperature Protection
FPGA Temperature Protection
Fan Control
Temperature Alarms

Features

- Pin-Programmable Local Temperature Threshold in $5^{\circ} \mathrm{C}$ Increments in Two Distinct Ranges: $+40^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$ and $+75^{\circ} \mathrm{C}$ to $+115^{\circ} \mathrm{C}$
- Factory-Programmed Remote Threshold: $+120^{\circ} \mathrm{C}$ (L Suffix) or $+125^{\circ} \mathrm{C}$ (H Suffix)
- Open-Drain Active-Low Outputs (MAX6687)
- CMOS Push-Pull, Active-High Outputs (MAX6688)
- $\pm 1.5^{\circ} \mathrm{C}$ Accuracy
- 2Hz Temperature Sampling Rate
- 215 $\mu \mathrm{A}$ Average Supply Current
- 3.0V to 5.5V Power-Supply Voltage
- 8-Pin $\mu$ MAX Package

Ordering Information

| PART | TEMP RANGE | PIN-PACKAGE |
| :--- | :--- | :--- |
| MAX6687AU4OL | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6687AU40H | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6687AU75L | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6687AU75H | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6688AU4OL | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6688AU40H | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6688AU75L | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |
| MAX6688AU75H | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $8 \mu \mathrm{MAX}$ |

Pin Configurations/Functional Diagrams/Selector Guide appear at end of data sheet.

Typical Operating Circuit


## Local/Remote Temperature Switches in a $\mu$ MAX Package

ABSOLUTE MAXIMUM RATINGS<br>Voltages Referenced to GND<br>VDD, Tlocal, Tremote ............................................-0.3V to +6 V<br>DXN . -0.3 V to +0.8 V<br>All Other Pins<br>$\qquad$ -0.3 V to $\left(\mathrm{V}_{\mathrm{DD}}+0.3 \mathrm{~V}\right)$<br>Input Current....................................................................... 5 mA<br>Output Current<br>. 20 mA

Continuous Power Dissipation ( $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ )
8-Pin $\mu$ MAX (derate $4.1 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ............. 330 mW
Operating Temperature Range ......................... $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
Junction Temperature ..................................................... $150^{\circ} \mathrm{C}$
Storage Temperature Range ............................. $-65^{\circ} \mathrm{C}$ to $+165^{\circ} \mathrm{C}$
Lead Temperature (soldering, 10s) ................................. $300^{\circ} \mathrm{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{V}_{\mathrm{DD}}=3 \mathrm{~V}\right.$ to $5.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note 1)

| PARAMETER | SYMBOL | CONDITION | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Power-Supply Range | VDD |  | 3 |  | 5.5 | V |
| Average Supply Current | IDD |  |  | 215 | 500 | $\mu \mathrm{A}$ |
| Supply Current During Conversion |  |  |  | 400 | 800 | $\mu \mathrm{A}$ |
| Power-On Reset (POR) Threshold | POR | VDD falling edge | 1 | 1.5 | 2.0 | V |
| POR Threshold Hysteresis |  |  |  | 50 |  | mV |
| Remote-Diode Temperature Threshold Accuracy, Rising Temperature | $\Delta$ T ${ }_{\text {TH }}$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ | -1.5 |  | +1.5 | ${ }^{\circ} \mathrm{C}$ |
|  |  | $\mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ | -3.0 |  | +3.0 |  |
|  |  | $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ | -5.0 |  | +5.0 |  |
| Internal Temperature Threshold Accuracy, Rising Temperature | $\Delta \mathrm{T}_{\text {TH }}$ | Temperature trip thresholds from $+40^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}, \mathrm{~V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ | -3.0 |  | +3.0 | ${ }^{\circ} \mathrm{C}$ |
|  |  | Temperature trip thresholds $+110^{\circ} \mathrm{C}$ and $+115^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ | -3.5 |  | +3.5 |  |
| Temperature Threshold Hysteresis | THYST |  |  | 5.0 |  | ${ }^{\circ} \mathrm{C}$ |
| Temperature Threshold Supply Sensitivity |  |  |  |  | 0.6 | ${ }^{\circ} \mathrm{C} / \mathrm{V}$ |
| Output Voltage High | VOH | ISOURCE $=1 \mathrm{~mA}$, MAX6688 | VDD - 0.2 |  |  | V |
| Output Voltage Low | VOL | $\mathrm{ISINK}=1 \mathrm{~mA}$ |  |  | 0.2 | V |
| Logic Input Low Voltage $(\mathrm{S} 1, \mathrm{~S} 2)$ | VIL |  |  |  | 0.4 | V |
| Logic Input High Voltage (S1, S2) | $\mathrm{V}_{\mathrm{IH}}$ |  | 1.8 |  |  | V |
| Input Current (S1, S2) |  |  |  |  | 10 | $\mu \mathrm{A}$ |
| Open-Drain Output Leakage Current |  | Vout $=5.5 \mathrm{~V}, \mathrm{MAX} 6687$ |  |  | 1 | $\mu \mathrm{A}$ |

## Local/Remote Temperature Switches in a $\mu$ MAX Package

## ELECTRICAL CHARACTERISTICS (continued)

$\left(\mathrm{V}_{D D}=3 \mathrm{~V}\right.$ to $5.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note 1)

| PARAMETER | SYMBOL | CONDITION | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Temperature Conversion Time |  |  | 0.2 | 0.25 | 0.3 | s |
| Temperature Sample Period |  |  | 0.4 | 0.5 | 0.6 | s |
| Current Sourcing for External Diode |  | High level | 80 | 100 | 120 | $\mu \mathrm{A}$ |
|  |  | Low level | 8 | 10 | 12 |  |

Note 1: All parameters are tested at $+25^{\circ} \mathrm{C}$. Temperature specifications over a range of $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ are guaranteed by design.

## Typical Operating Characteristics

$\left(\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}, \mathrm{CS}=2200 \mathrm{pF}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$


TEMPERATURE TRIP THRESHOLD ERROR
vs. Cs CAPACITANCE


AVERAGE SUPPLY CURRENT
vs. SUPPLY VOLTAGE


REMOTE TEMPERATURE TRIP THRESHOLD ERROR vs. AMBIENT TEMPERATURE


# Local/Remote Temperature Switches in a $\mu$ MAX Package 

| PIN |  | NAME | FUNCTION |
| :---: | :---: | :---: | :---: |
| MAX6687 | MAX6688 |  |  |
| 1 | 1 | VDD | Power-Supply Input. Bypass to GND with a $0.1 \mu \mathrm{~F}$ capacitor. |
| 2 | 2 | GND | Ground |
| 3 | 3 | DXP | This pin connects to the positive (anode) terminal of the external P-N sense junction. It sources current into the external junction. A 2200pF capacitor should be connected across DXP and DXN. |
| 4 | 4 | DXN | This pin connects to the negative (cathode) terminal of the external P-N sense junction. It sinks current from the external junction. A 2200pF capacitor should be connected across DXP and DXN. DXN must be connected to the GND pin at the pin. |
| 5 | - | $\overline{T r e m o t e ~}$ | Open-Drain Active-Low Output. TREMOTE goes low when the temperature exceeds the factoryprogrammed temperature threshold, either $+120^{\circ} \mathrm{C}$ or $+125^{\circ} \mathrm{C}$. Connect a pullup resistor (typically $10 \mathrm{k} \Omega$ ) between TREMOTE and a positive power supply up to 5.5 V . |
| - | 5 | Tremote | CMOS Push-Pull, Active-High Output. TREMOTE goes high when the temperature exceeds the factory-programmed temperature threshold, either $+120^{\circ} \mathrm{C}$ or $+125^{\circ} \mathrm{C}$. |
| 6 | - | TLOCAL | Open-Drain Active-Low Output. TLOCAL goes low when the temperature exceeds the pinprogrammable temperature threshold set by S1 and S2. Connect a pullup resistor (typically $10 \mathrm{k} \Omega$ ) between TLOCAL and a positive power supply up to 5.5 V . |
| - | 6 | TLOCAL | CMOS Push-Pull, Active-High Output. Tlocal goes high when the temperature exceeds the pin-programmable temperature threshold set by S1 and S2. |
| 7 | 7 | S1 | Threshold Select Input. Used in conjunction with S2 to set the local threshold temperature (Table 1). It can be connected to $V_{D D}$, GND, or left floating. |
| 8 | 8 | S2 | Threshold Select Input. Used in conjunction with S1 to set the local threshold temperature (Table 1). It can be connected to $\mathrm{V}_{\mathrm{DD}}$, GND, or left floating. |

## Detailed Description

The MAX6687/MAX6688 sense the temperatures of both a remote $\mathrm{P}-\mathrm{N}$ junction and their own die. The external P-N junction is typically a base-emitter junction of a substrate PNP on a microprocessor, FPGA, or ASIC die (see the Typical Operating Circuit).
The remote temperature switch has a factory-programmed trip temperature of either $+120^{\circ} \mathrm{C}$ or $+125^{\circ} \mathrm{C}$ and is intended to be used for system shutdown when the die temperature of a remote IC, such as a microprocessor, FPGA, or ASIC exceeds the factory-programmed thresholds.
The local temperature switch has a pin-programmable threshold temperature (Table 1). This temperature switch may be used for such functions as system shutdown or for turning on a cooling fan when board temperature exceeds the temperature limit. Two temperature ranges are available for the local trip
threshold: $+40^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$ and $+75^{\circ} \mathrm{C}$ to $+115^{\circ} \mathrm{C}$. S 1 and S 2 pins must be set to the desired trip temperature before power is applied to the VDD pin. If S1 and S2 settings are changed after the power is turned on, the local trip threshold remains set to the point where S 1 and S2 were when power was applied.
Since the MAX6687/MAX6688 are often used for system shutdown, they are designed so that the outputs do not change on transient faults or when power is first applied. This eliminates the possibility that the IC could erroneously shut a system down.
The MAX6687/MAX6688 provide noise immunity by integration and oversampling of the diode voltage, but good design practice includes routing the DXP and DXN lines away from noise sources, such as highspeed digital lines, switching regulators, inductors, and transformers. The DXP and DXN traces should be paired together and surrounded by a ground plane whenever possible.

# Local／Remote Temperature Switches in a HMAX Package 

## Table 1．Local Temperature Trip Threshold Selection

| S1 | S2 | MAX6687AUA40L／MAX6687AUA40H／ MAX6688AUA40L／MAX6688AUA40H | MAX6687AUA75L／MAX6687AUA75H／ MAX6688AUA75L／MAX6688AUA75H |
| :---: | :---: | :---: | :---: |
|  |  | LOCALTEMPERATURE TRIP THRESHOLD（ ${ }^{\circ} \mathrm{C}$ ） | LOCAL TEMPERATURE TRIP THRESHOLD（ ${ }^{\circ} \mathrm{C}$ ） |
| GND | GND | ＋40 | ＋75 |
| GND | FLOAT | ＋45 | ＋80 |
| GND | VDD | ＋50 | ＋85 |
| FLOAT | GND | ＋55 | ＋90 |
| FLOAT | FLOAT | ＋60 | ＋95 |
| FLOAT | VDD | ＋65 | ＋100 |
| VDD | GND | ＋70 | ＋105 |
| VDD | FLOAT | ＋75 | ＋110 |
| VDD | VDD | ＋80 | ＋115 |

## Applications Information

## Remote－Diode selection

The MAX6687／MAX6688 are optimized to measure the die temperature of CPUs and other ICs that have on－ chip temperature－sensing diodes．These on－chip diodes are substrate PNPs with their collectors ground－ ed．Connect the base of the PNP to DXN and the emit－ ter to DXP．When using a discrete，diode－connected NPN or PNP as a sensing diode，use a good－quality small－signal device．Examples are listed in Table 2. Tight specifications for forward current gain indicate the manufacturer has good process controls and that the devices have consistent $V_{B E}$ characteristics． Always use a transistor for the sensing junction；diodes do not work．
The MAX6687／MAX6688 are optimized for use with thermal－sensing transistors with an ideality factor of 1．008．Different ideality factors cause predictable，usu－ ally small deviations in trip temperature thresholds．
Table 2．Sensor Transistor Manufacturers

| MANUFACTURER | MODEL NO． |
| :--- | :--- |
| Central Semiconductor（USA） | CMPT3904 |
| Rohm Semiconductor（Japan） | SST3904 |
| Samsung（Korea） | KST3904－TF |
| Siemens（Germany） | SMBT3904 |

Noise－Filtering Capacitors
A quality ceramic capacitor must be connected across the DXP／DXN inputs to maintain temperature threshold accuracy by filtering out noise．The capacitor should be located physically close to the DXP／DXN pins and
should typically have a value of 2200pF．Larger capaci－ tor values can cause temperature measurement errors． A 50\％increase from the recommended capacitor value can cause up to $\pm 1^{\circ} \mathrm{C}$ error．

## Sensing Circuit Board and Ambient Temperature

 Temperature switches like the MAX6687／MAX6688 that sense their own die（local）temperatures must be mounted on or close to the object whose temperature they are intended to measure．The MAX6687／MAX6688 can accurately measure the temperature of a circuit board to which they are soldered because the package leads provide a good thermal path between the circuit board and their own die．If the MAX6687／MAX6688 are intended to be triggered by the temperature of a heat－ generating component on the circuit board，they should be mounted as close as possible to that component and should share supply and ground traces（if they are not noisy）with that component where possible．The thermal path between the plastic package and the die is not as good as the path through the package leads， so the MAX6687／MAX6688 are less sensitive to the sur－ rounding air temperature than they are to the tempera－ ture of their package leads，but they can be successfully used to respond to the ambient tempera－ ture if the circuit board is designed to track the ambient temperature．TRANSISTOR COUNT： 7765
PROCESS：BiCMOS

## Local/Remote Temperature Switches in a $\mu$ MAX Package



Functional Block Diagrams


Figure 1a. MAX6687 Functional Block Diagram


Figure 1b. MAX6688 Functional Block Diagram

Selector Guide

| PART | OUTPUTS | REMOTE TRIP THRESHOLD <br> $\left({ }^{\circ} \mathbf{C}\right)$ | LOCAL TRIP THRESHOLD RANGE <br> $\left({ }^{\circ} \mathbf{C}\right)$ |
| :---: | :---: | :---: | :---: |
| MAX6687AU40L | Open drain, active low | +120 | +40 to +80 |
| MAX6687AU40H | Open drain, active low | +125 | +40 to +80 |
| MAX6687AU75L | Open drain, active low | +120 | +75 to +115 |
| MAX6687AU75H | Open drain, active low | +125 | +75 to +115 |
| MAX6688AU40L | Push pull, active high | +120 | +40 to +80 |
| MAX6688AU4OH | Push pull, active high | +125 | +40 to +80 |
| MAX6688AU75L | Push pull, active high | +120 | +75 to +115 |
| MAX6688AU75H | Push pull, active high | +125 | +75 to +115 |

# Local/Remote Temperature Switches in a HMAX Package 

Package Information
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)

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## MAX6687

## Part Number Table

## Notes:

. See the MAX6687 QuickView Data Sheet for further information on this product family or download the MAX6687 full data sheet (PDF, 144kB).
2. Other options and links for purchasing parts are listed at: http://www.maxim-ic.com/sales.
3. Didn't Find What You Need? Ask our applications engineers. Expert assistance in finding parts, usually within one business day.
4. Part number suffixes: $T$ or $T \& R=$ tape and reel; $+=$ RoHS/lead-free; \# = RoHS/lead-exempt. More: See full data sheet or Part Naming Conventions.
5. * Some packages have variations, listed on the drawing. "PkgCode/Variation" tells which variation the product uses

| Part Number | Free Sample | Buy <br> Direct | Package: TYPE PINS SIZE DRAWING CODE/VAR | Temp | RoHS/Lead-Free? Materials Analysis |
| :---: | :---: | :---: | :---: | :---: | :---: |
| MAX6687AU40H+ |  |  |  | -40C to +125 C | RoHS/Lead-Free: Yes |
| MAX6687AU75L+ |  |  |  | -40C to +125 C | RoHS/Lead-Free: Yes |
| MAX6687AU75H+ |  |  |  | -40C to +125C | RoHS/Lead-Free: Yes |
| MAX6687AU40L+ |  |  |  | -40C to +125C | RoHS/Lead-Free: Yes |
| MAX6687AU75H+T |  |  |  | -40C to +125C | RoHS/Lead-Free: Yes |
| MAX6687AU40L+T |  |  |  | -40C to +125C | RoHS/Lead-Free: Yes |
| MAX6687AU40H+T |  |  |  | -40C to +125 C | RoHS/Lead-Free: Yes |


| MAX6687AU40H | uMAX; 8 pin; $3 \times 3 \mathrm{~mm}$ <br> Dwg: 21-0036J (PDF) <br> Use pkgcode/variation: U8-1* | -40C to $+125 C$ | RoHS/Lead-Free: No Materials Analysis |
| :---: | :---: | :---: | :---: |
| MAX6687AU75L-T | uMAX; 8 pin; $3 \times 3 \mathrm{~mm}$ <br> Dwg: 21-0036J (PDF) <br> Use pkgcode/variation: U8-1* | -40C to +125 C | RoHS/Lead-Free: No Materials Analysis |
| MAX6687AU75L | uMAX; 8 pin; $3 \times 3 \mathrm{~mm}$ Dwg: 21-0036J (PDF) Use pkgcode/variation: U8-1* | -40 C to +125 C | RoHS/Lead-Free: No Materials Analysis |
| MAX6687AU75H-T |  | -40 C to +125 C | RoHS/Lead-Free: No |
| MAX6687AU75H | uMAX; 8 pin; $3 \times 3 \mathrm{~mm}$ <br> Dwg: 21-0036J (PDF) Use pkgcode/variation: U8-1* | -40C to $+125 C$ | RoHS/Lead-Free: No Materials Analysis |
| MAX6687AU40L-T |  | -40 C to +125 C | RoHS/Lead-Free: No |
| MAX6687AU40L | uMAX;8 pin;3 x 3mm Dwg: 21-0036J (PDF) Use pkgcode/variation: U8-1* | -40 C to $+125 C$ | RoHS/Lead-Free: No Materials Analysis |
| MAX6687AU40H-T |  | -40 C to +125 C | RoHS/Lead-Free: No |
| MAX6687AU75L+T |  | -40 C to +125 C | RoHS/Lead-Free: Yes |

Didn't Find What You Need?

