MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PIFD

MMST5401

Product specification





FEATURES

- Complementary to MMST5551
- Small Surface Mount Package
- Ideal for Medium Power Amplification and Switching

Reference News

PACKAGE OUTLINE		MARKING
3	1. BASE 2. EMITTER 3. COLLECTOR	K4M
SOT-323		

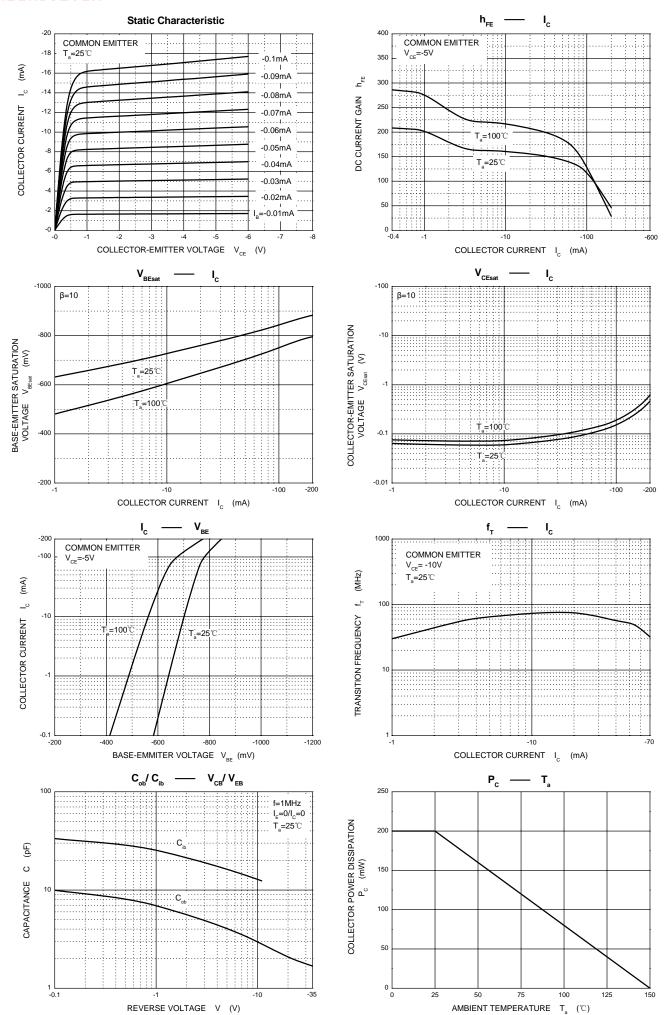
MAXIMUM RATINGS (Ta=25℃ unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-160	V
V _{CEO}	Collector-Emitter Voltage	-150	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ic	Collector Current	-600	mA
Pc	Collector Power Dissipation	200	mW
Roja	Thermal Resistance From Junction To Ambient	625	°C/W
T _J ,T _{stg}	Operation Junction and Storage Temperature Range	-55 ~ +150	℃

ELECTRICAL CHARACTERISTICS (Ta=25℃ unless otherwise specified)

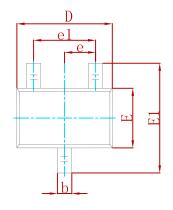
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	lc=-100μA, I _E =0	-160			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =-1mA, I _B =0	-150			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =-10μA, I _C =0	-5			V
Collector cut-off current	Ісво	V _{CB} =-120V, I _E =0			-50	nA
Emitter cut-off current	I _{EBO}	V _{EB} =-3V, I _C =0			-50	nA
DC current gain	hFE	V _{CE} =-5V, I _C =-1mA	50			
		V _{CE} =-5V, I _C =-10mA	100		300	
		V _{CE} =-5V, I _C =-50mA	50			
0-11	V	I _C =-50mA, I _B =-5mA			-0.5	V
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =-10mA, I _B =-1mA			-0.2	V
Dana amilitar actionation valtare	V _{BE(sat)}	I _C =-50mA, I _B =-5mA			-1	V
Base-emitter saturation voltage		lc=-10mA, l _B =-1mA			-1	V
Transition frequency	f⊤	V _{CE} =-10V,I _C =-10mA , f=100MHz	100			MHz
Collector output capacitance	Cob	V _{CB} =-10V, I _E =0, f=1MHz			6	pF

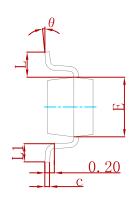


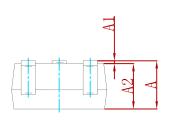




PACKAGEMECHANICALDATA

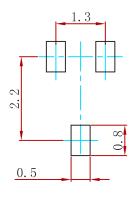






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.200	0.400	0.008	0.016	
С	0.080	0.150	0.003	0.006	
D	2.000	2.200	0.079	0.087	
E	1.150	1.350	0.045	0.053	
E1	2.150	2.450	0.085	0.096	
е	0.650 TYP		0.026 TYP		
e1	1.200	1.400	0.047	0.055	
L	0.525 REF		0.021	I REF	
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

Suggested Pad Layout



Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MMST5401	SOT-323	3000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents—or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.