MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

PESD1LIN-MS

Product specification





Features

- Ultra low leakage: nA level
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: ±25kV
 - Contact discharge: ±25kV
 - IEC61000-4-4 (EFT) 40A (5/50ns)
- RoHS Compliant
- AEC-Q101 qualified

Applications

- USB 2.0 power and data line
- Set-top box and digital TV
- Digital video interface (DVI)
- Notebook Computers
- SIM Ports
- 10/100 Ethernet
- LIN BUS

Mechanical Characteristics

- Package: SOD-323
- Lead Finish: Lead Free
- UL Flammability Classification Rating 94V-0
- Quantity Per Reel:3000pcs
- Reel Size:7 inch

Reference News

PACKAGE OUTLINE	Circuit Diagram	Marking
SOD-323		AM



Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ррр	180	W
ESD per IEC 61000−4 −2 (Air)	Vesd	± 20	Kv
ESD per IEC 61000-4 -2 (Contact)	VESD	± 20	
Operating Temperature Range	TJ	-55 to + 125	°C
Storage Temperature Range	Тѕтј	-55 to + 150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Pitt	V	Vc		с	R	C		
P/N	v rwm (V)	VRWM VBR IT (V) (V) (MA)		Vc @1A	(Max)	(@A)	μA (Max)	(Pf) (Typ.)
	15	18 9	5	25	44	5	0 05	13
PESD1LIN-MS	24	27.8	5	40	70	3	0.05	13



Characteristic Curves

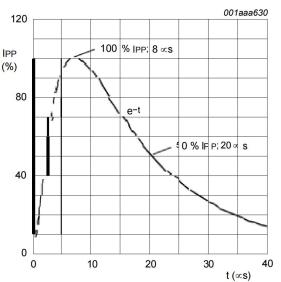
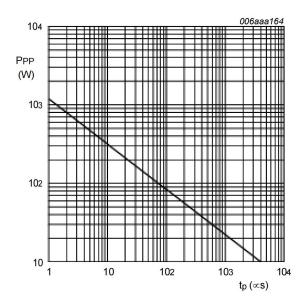


Fig 3. Peak pulse power as a function of exponential pulse duration; typical values



T_{amb} = 25 °C

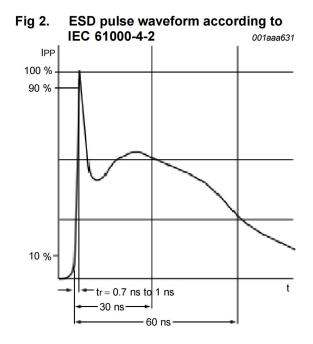
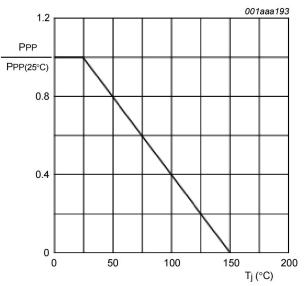
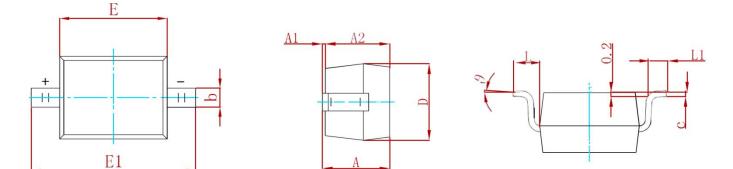


Fig 4. Relative variation of peak pulse power as a function of junction temperature; typical values



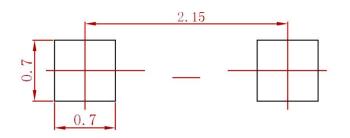


PACKAGE MECHANICAL DATA



Sumbol	Symbol Dimensions In Millimeters Min Max		Dimensions In Inches		
Symbol			Min.	Max	
А		1.000		0.039	
A1	0.000	0.100	0.000	0.004	
A2	0.800	0.900	0.031	0.035	
b	0.250	0.350	0.010	0.014	
С	0.080	0.150	0.003	0.006	
D	1.200	1.400	0.047	0.055	
E	1.600	1.800	0.063	0.071	
E1	2.550	2.750	0.100	0.108	
L	0.475	5 REF	0.019	9 REF	
L1	0.250	0.400	0.010	0.016	
θ	0 °	8 °	0 °	8 °	

Suggested Pad Layout



Note:

1.Controlling dimension: in millimeters.

2.General tolerance:±0.05mm.

3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
PESD1LIN-MS	SOD-323	3000



Attention

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.

Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuits for safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or

mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to

product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.