

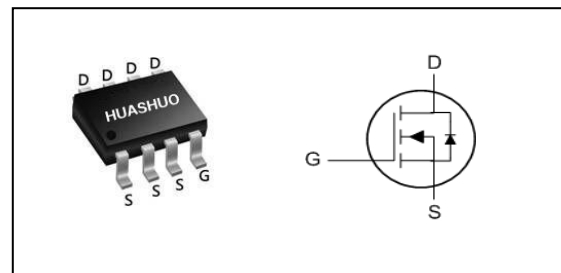
N-Ch 100V Fast Switching MOSFETs
Applications

Portable Equipment.
Battery Powered Systems.
Hard Switching and High-Speed Circuit.

- 100% EAS Guaranteed
- Low $R_{DS(ON)}$
- Low Gate Charge
- RoHs and Halogen-Free Compliant

Product Summary

| | | |
|------------------|-----|-----------|
| V_{DS} | 100 | V |
| $R_{DS(ON),Max}$ | 20 | $m\Omega$ |
| I_D | 9.5 | A |

SOP-8 Pin Configuration

Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|-----------------------|--|------------|------------|
| V_{DS} | Drain-Source Voltage | 100 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D@T_A=25^\circ C$ | Continuous Drain Current ^{1,6} | 9.5 | A |
| $I_D@T_A=100^\circ C$ | Continuous Drain Current ^{1,6} | 7.5 | A |
| I_{DM} | Pulsed Drain Current ² | 37 | A |
| EAS | Single Pulse Avalanche Energy ³ | 45 | mJ |
| I_{AS} | Avalanche Current | 30 | A |
| $P_D@T_A=25^\circ C$ | Total Power Dissipation ⁴ | 3.1 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | $^\circ C$ |
| T_J | Operating Junction Temperature Range | -55 to 150 | $^\circ C$ |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|---|------|------|--------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient ¹ ($t \leq 10s$) | --- | 40 | $^\circ C/W$ |
| | Thermal Resistance Junction-Ambient ¹ | --- | 85 | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | --- | 24 | $^\circ C/W$ |

N-Ch 100V Fast Switching MOSFETs
Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|---------------------|--|---|------|------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250uA | 100 | --- | --- | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V, I _D =8A | --- | 16 | 20 | mΩ |
| | Static Drain-Source On-Resistance ² | V _{GS} =4.5V, I _D =6A | --- | 22 | 30 | |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =250uA | 1.2 | 1.8 | 2.2 | V |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =80V, V _{GS} =0V, T _J =25°C | --- | --- | 1 | uA |
| | | V _{DS} =80V, V _{GS} =0V, T _J =55°C | --- | --- | 5 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±20V, V _{DS} =0V | --- | --- | ±100 | nA |
| R _g | Gate Resistance | V _{DS} =0V, V _{GS} =0V, f=1MHz | --- | 1.1 | --- | Ω |
| Q _g | Total Gate Charge (10V) | V _{DS} =50V, V _{GS} =10V, I _D =10A | --- | 17.9 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 2.8 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 5.2 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =30V, V _{GS} =10V, R _G =6Ω, I _D =1A | --- | 13 | --- | ns |
| T _r | Rise Time | | --- | 6 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 30 | --- | |
| T _f | Fall Time | | --- | 29 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =50V, V _{GS} =0V, f=1MHz | --- | 849 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 185 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 8 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|---|------|------|------|------|
| I _S | Continuous Source Current ^{1,5,6} | V _G =V _D =0V, Force Current | --- | --- | 9.5 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V, I _S =1A, T _J =25°C | --- | --- | 1.2 | V |

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=30A
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.
- The maximum current rating is package limited.

Typical Characteristics

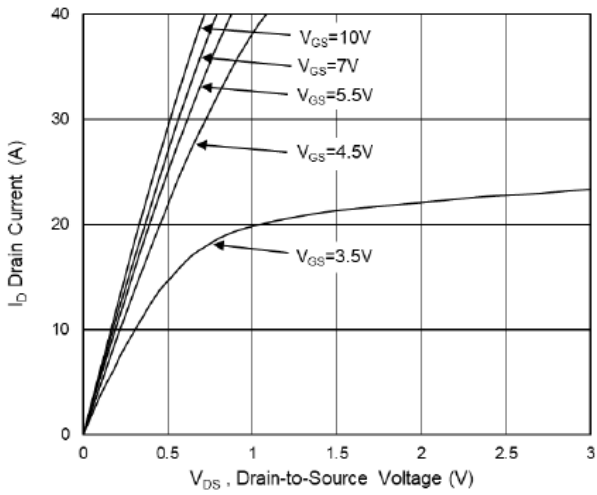


Fig.1 Typical Output Characteristics

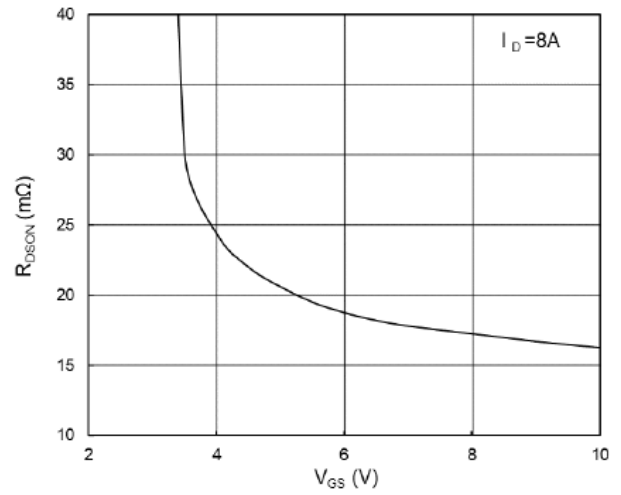


Fig.2 On-Resistance vs G-S Voltage

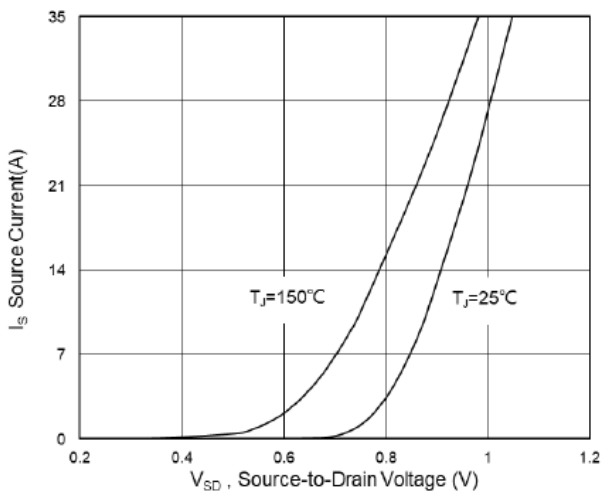


Fig.3 Source-Drain Forward Characteristics

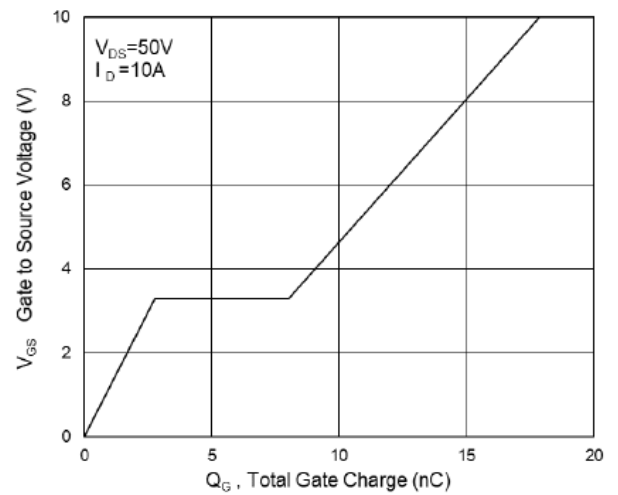


Fig.4 Gate-Charge Characteristics

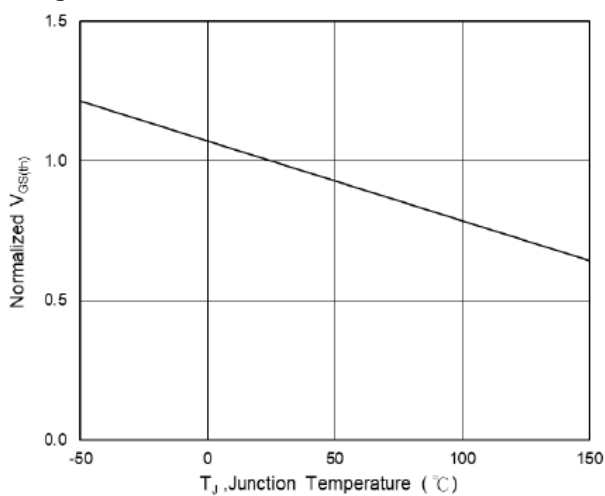


Fig.5 Normalized $V_{GS(th)}$ vs T_J

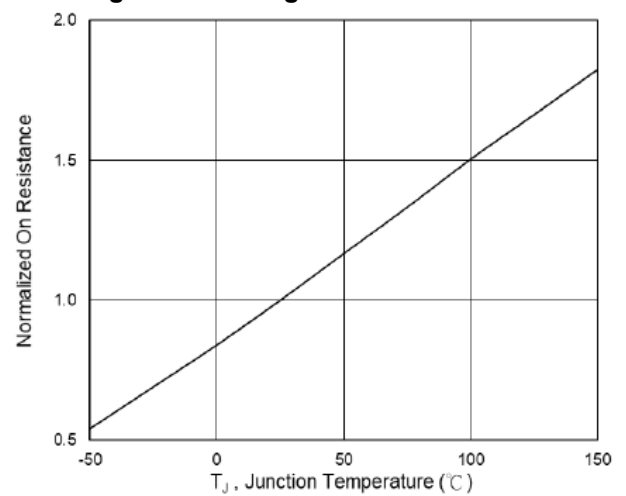


Fig.6 Normalized $R_{DS(on)}$ vs T_J

N-Ch 100V Fast Switching MOSFETs

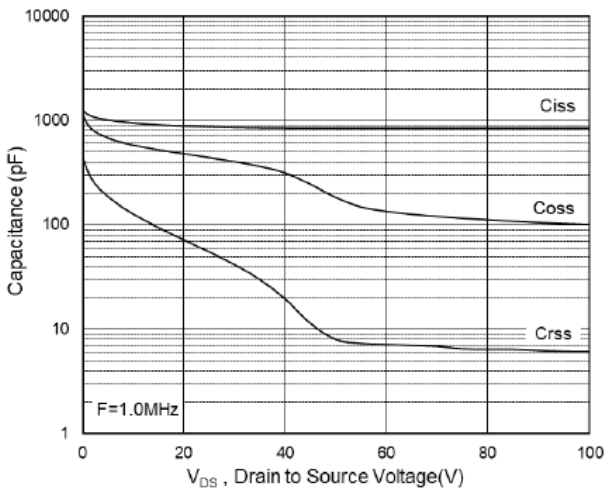


Fig.7 Capacitance

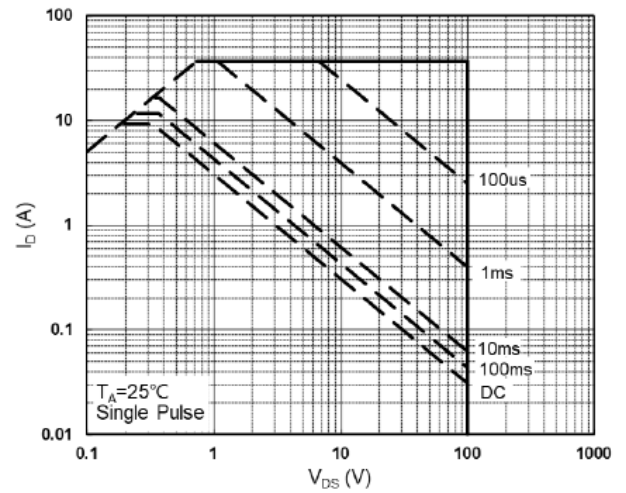


Fig.8 Safe Operating Area

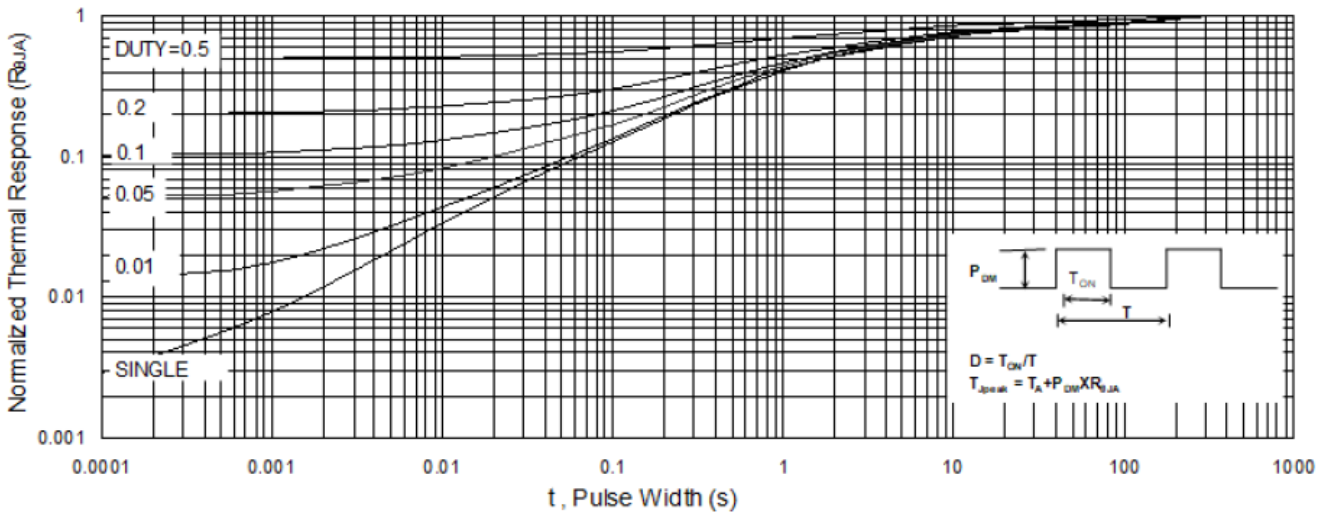


Fig.9 Normalized Maximum Transient Thermal Impedance

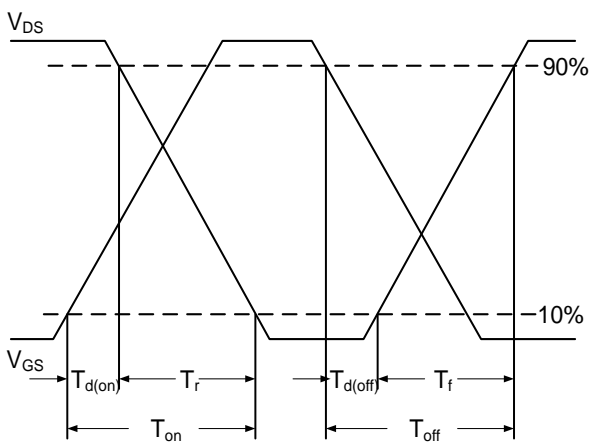


Fig.10 Switching Time Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

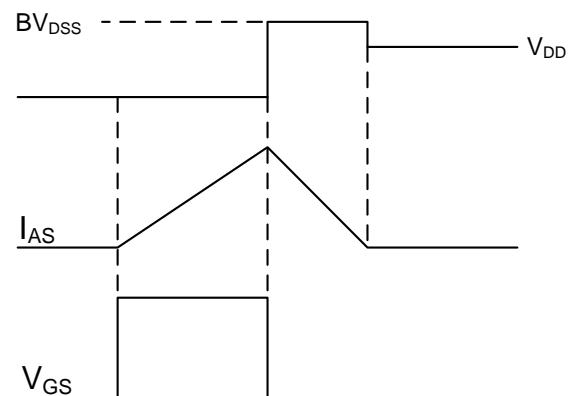


Fig.11 Unclamped Inductive Switching Waveform

Ordering Information

| Part Number | Package code | Packaging |
|-------------|--------------|----------------|
| HSM0056 | SOP-8 | 2500/Tape&Reel |

