

54LS279/DM54LS279/DM74LS279 Quad S-R Latches

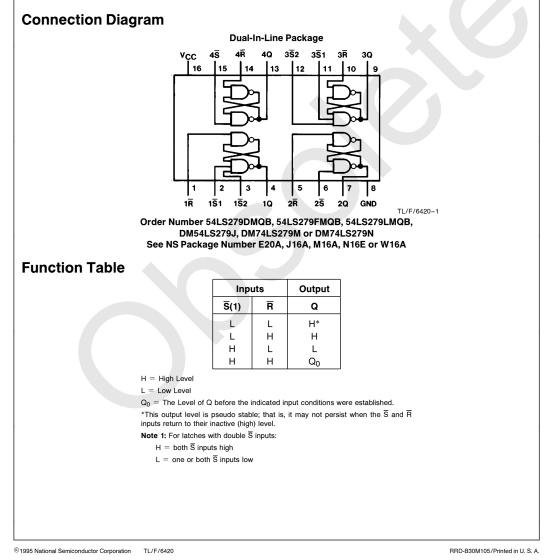
General Description

The 'LS279 consists of four individual and independent Set-Reset Latches with active low inputs. Two of the four latches have an additonal \overline{S} input ANDed with the primary \overline{S} input. A low on any \overline{S} input while the \overline{R} input is high will be stored in the latch and appear on the corresponding Q output as a high. A low on the \overline{R} input while the \overline{S} input is high will clear the Q output to a low. Simultaneous transistion of the \overline{R} and \overline{S} inputs from low to high will cause the Q output

to be indeterminate. Both inputs are voltage level triggered and are not affected by transition time of the input data.

Features

 Alternate military/aerospace device (54LS279) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.



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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM54LS and 54LS	-55°C to +125°C
DM74LS	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Parameter		DM54LS27	9		DM74LS27	9	Units
T arameter	Min	Nom	Max	Min	Nom	Max	onita
ly Voltage	4.5	5	5.5	4.75	5	5.25	V
Level Input Voltage	2			2			V
Level Input Voltage			0.7			0.8	V
Level Output Current			-0.4			-0.4	mA
Level Output Current			4			8	mA
Air Operating Temperature	-55		125	0		70	°C
	ly Voltage Level Input Voltage Level Input Voltage Level Output Current Level Output Current Air Operating Temperature	ly Voltage 4.5 Level Input Voltage 2 Level Input Voltage Level Output Current Level Output Current	ly Voltage 4.5 5 Level Input Voltage 2 Level Input Voltage Level Output Current Level Output Current	Iy Voltage 4.5 5 5.5 Level Input Voltage 2 2 Level Output Current 0.7 Level Output Current 4	Ivy Voltage 4.5 5 5.5 4.75 Level Input Voltage 2 2 2 Level Input Voltage 0.7 -0.4 Level Output Current 4 -0.4	Ivy Voltage 4.5 5 5.5 4.75 5 Level Input Voltage 2 2 2 Level Output Current -0.4 2 Level Output Current 4 2	Ivy Voltage 4.5 5 5.5 4.75 5 5.25 Level Input Voltage 2

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

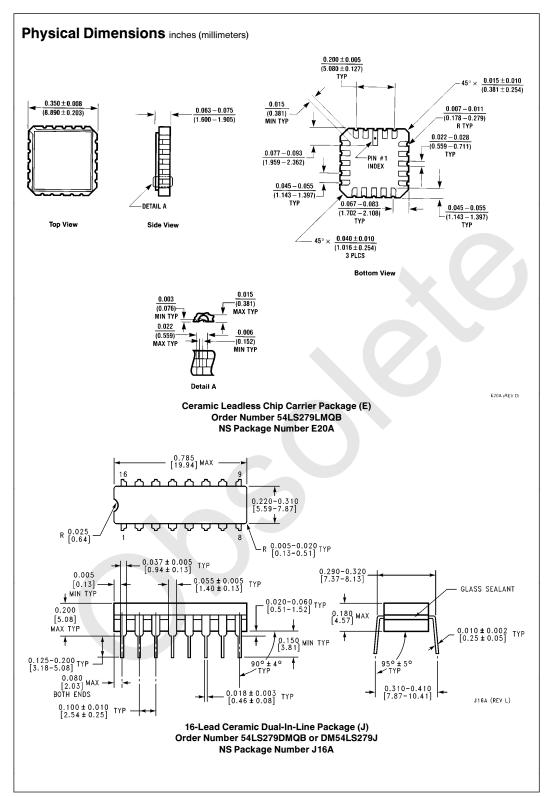
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Мах	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	V
V _{OH}	High Level Output	$V_{CC} = Min, I_{OH} = Max$	DM54	2.5	3.5		V
	Voltage	$V_{IL} = Max, V_{IH} = Min$	DM74	2.7	3.5		
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$	DM54		0.25	0.4	v
		$V_{IL} = Max, V_{IH} = Min$	DM74		0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$	DM74		0.25	0.4	
l _l	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$				0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_1 = 2.7V$				20	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.4	mA
I _{OS}	Short Circuit	V _{CC} = Max	DM54	-20		-100	mA
	Output Current	rent (Note 2)	DM74	-20		-100	110.
Icc	Supply Current	V _{CC} = Max (Note 3)			3.8	7	mA

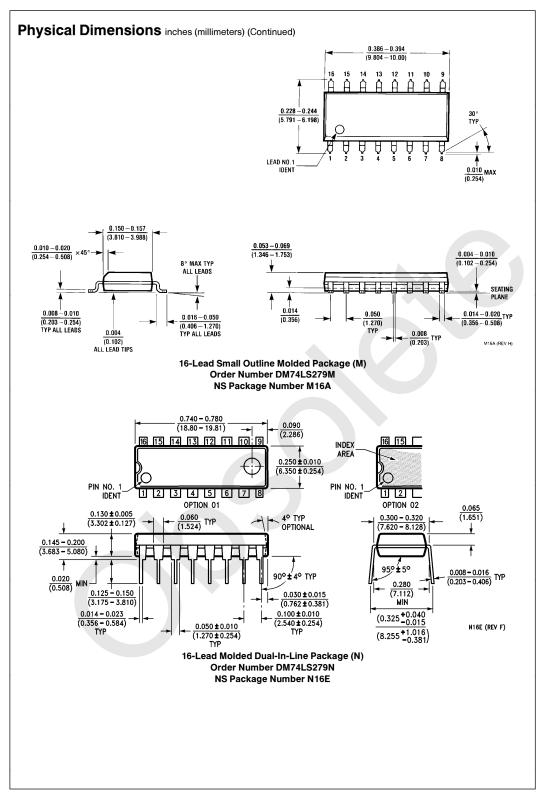
Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

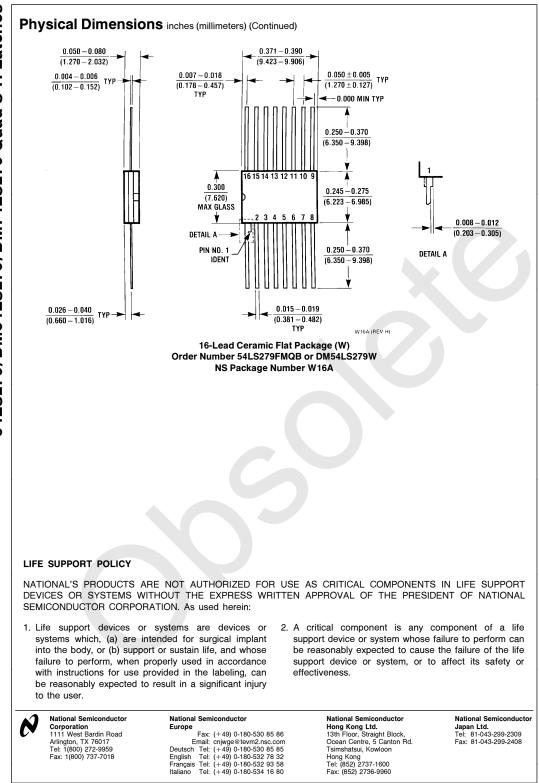
Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 3: I_{CC} is measured with all \overline{R} inputs grounded, all \overline{S} inputs at 4.5V and all outputs open.

		Erom (Innut)		R L =	2 k Ω		
Symbol	Parameter	From (Input) To (Output)	C _L =	15 pF	C _L =	$C_L = 50 \text{ pF}$	
			Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output	S to Q		22		25	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	S to Q		15		23	ns
^t PHL	Propagation Delay Time High to Low Level Output	R to Q		27		33	ns







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