

FAIRCHILD DIGITAL

RTL/CTL

FAIRCHILD RTL MICROLOGIC AND CTL COUNTING MICROLOGIC ELEMENTS

Item	DEVICE NO.	Description	Logic/Connection Diagram	Package(s)	Item	DEVICE NO.	Description	Logic/Connection Diagram	Package(s)
1	900	Buffer	F8	3F,5B	14	913	D Flip-Flop	F6	3F,5B
2	901	Counter Adapter	F18	3F,5B	15	914	Dual 2-NOR	F13	3F,5B
3	902	Flip-Flop	F19	3F,5B	16	915	Dual 3-NOR	F14	3F,5F
4	903	3-Input NOR	F9	3F,5B	17	921	Dual 2-Expander	F7	3F,5B
5	904	Half Adder	F10	3F,5B	18	923	JK Flip-Flop	F15	5B
6	905	Half Shift	F11	3F,5B	19	926	JK Flip-Flop	F16	3F,5F
7	906	Half Shift	F20	3F,5B	20	927	Quad Inverter	F17	3F,5F
8	907	4-Input NOR	F12	3F,5B	21	958	Decade Counter	F21	5B,6A
9	908	Adder	F1	3F,5B	22	959	4-Bit Latch	F22	6B
10	909	Buffer	F2	3F,5B	23	960	BCD Decoder/Dvr	F23	6B
11	910	Dual 2-NOR	F3	3F,5B	24	974	JK Flip-Flop	F15	5B
12	911	4-Input NOR	F4	3F,5B	25	989	Binary Counter	F21	5B,6A
13	912	Half Adder	F5	3F,5B					

DTL

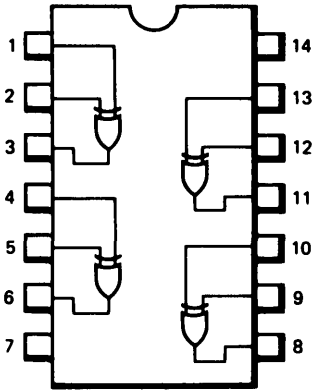
DTL MICROLOGIC

Item	DEVICE NO.	Description	Logic/Connection Diagram	Package(s)
26	930 ✓	Dual 4-Input Extendable NAND Gate	G1	3I,5F,6A,9A
27	932 ,	Dual 4-Input Extendable NAND Buffer Gate	G1	3I,5F,6A,9A
28	933	Extender	G9	5F,9A
29	935	Extendable Hex Inverter	G12	3I,6A,9A
30	936	Hex Inverter	G12	3I,6A,9A
31	937	Hex Inverter	G12	3I,6A,9A
32	941	Monostable Multivibrator	G17	3I,6A
33	944	Dual 4-Input Extendable NAND Buffer Gate (Open Collector)	G1	3I,5F,6A,9A
34	945	RS Flip-Flop	G18	3I,5F,6A,9A
35	946	Quad 2-Input NAND Gate	G10	3I,5F,6A,9A

FAIRCHILD LOGIC/CONNECTION DIAGRAMS

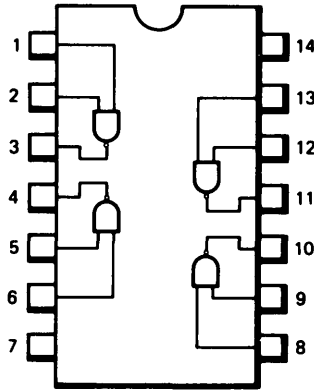
DIGITAL-DTL

G7
1812



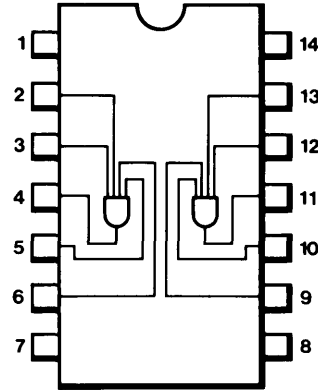
Vcc = Pin 14
GND = Pin 7

G8
9157, 9158



Vcc = Pin 14
GND = Pin 7

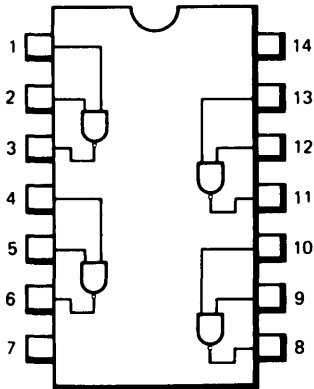
G9
933



No connection required to V_{CC} (Pin 14).

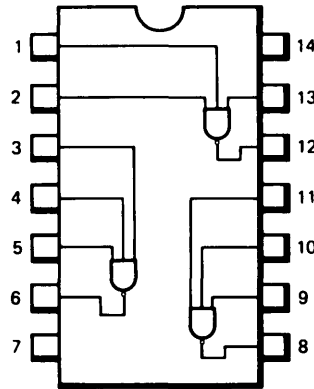
Vcc = Pin 14
GND = Pin 7

G10
946, 949



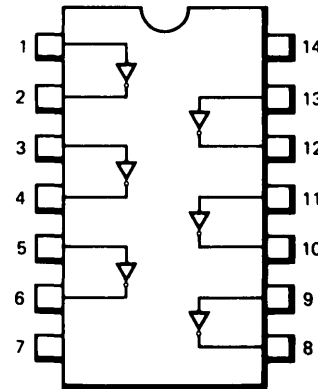
Vcc = Pin 14
GND = Pin 7

G11
962, 963



Vcc = Pin 14
GND = Pin 7

G12
9109, 9110, 9112
9135, 935, 936
937



Vcc = Pin 14
GND = Pin 7