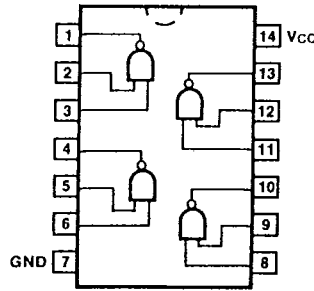


54/7439

QUAD 2-INPUT NAND BUFFER (With Open-Collector Output)

CONNECTION DIAGRAMS PINOUT A

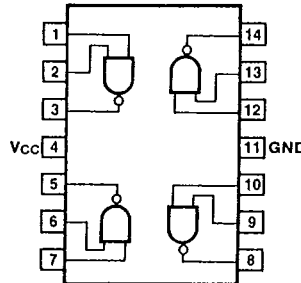


4

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		V _{CC} = +5.0 V ±5%, T _A = 0°C to +70°C	V _{CC} = +5.0 V ±10%, T _A = -55°C to +125°C	
Plastic DIP (P)	A	7439PC		9A
Ceramic DIP (D)	A	7439DC	5439DM	6A
Flatpak (F)	B	7439FC	5439FM	3I

PINOUT B



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.) HIGH/LOW
Inputs	1.0/1.0
Outputs	OC**/30

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74		UNITS	CONDITIONS	
		Min	Max			
V _{OL}	Output LOW Voltage	XM, XC	0.4	V	I _{OL} = 48 mA	V _{CC} = Min V _{IN} = 2.0 V Other Inputs = 2.0 V
		XM, XC	0.5		I _{OL} = 60 mA	
		XC	0.6		I _{OL} = 80 mA	
I _{CCH} I _{CCL}	Power Supply Current		8.5 54	mA	V _{IN} = Gnd V _{IN} = Open	V _{CC} = Max
t _{PLH} t _{PHL}	Propagation Delay		22 18	ns	Figs. 3-2, 3-4	

*DC limits apply over operating temperature range; AC limits apply at T_A = +25°C and V_{CC} = +5.0 V.
**OC—Open Collector