

Discription

The D5V0L1B2LP3-7 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN0603-2L

Features

- ★ Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- ★ Low Body Height: 0.28 mm
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 (> 16 kV) per Human Bod Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ These are Pb-Free Devices
- ★ We declare that the material of product compliance with RoHS requirements.

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Circuit Diagram

Ordering information

Product ID	Pack	Qty(PCS)
D5V0L1B2LP3-7	DFN0603-2L	15000

Absolute Ratings (T_{amb}=25°C)

Symbol	Parameter	Value	Units
P_{PP}	Peak Pulse Power (t _p = 8/20µs)	80	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T_{stg}	Storage Temperature Range	-55 to +150	°C
T _{op}	Operating Temperature Range		°C
Tj	Maximum junction temperature		°C
	IEC61000-4-2 (ESD) air discharge contact discharge		KV

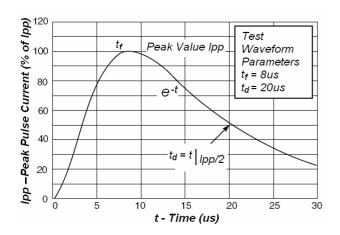


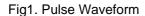
ELECTRICAL CHARACTERISTICS

	V _{RWM}	I _{R1} (μΑ) @ V _{RWM}	I _{R2} (μ A) @ V _R =3.5V	V _{BR} (V) @ I _T (Note 2)	Ι _Τ	V _C (V) @ I _{PP} = 1 A (Note 3)	V _C (V) @MAX I _{PP} (Note 3)	I _{PP} (A) (Note 3)	P _{PK} (W) (Note 3)	C (pF)
Device	Max	Max	Max	Min	mA	Max	Max	Max	Max	Тур
D5V0L1B2LP37	5.0	0.5	0.3	5.6	1.0	9	10	8	80	15

Other voltage available upon request.

- 2. V_{BR} is measured with a pulse test current IT at an ambient temperature of 25 $^{\circ}{\!\!\!^{\circ}}{\!\!\!^{\circ}}$
- 3. Surge current waveform per Figure 1.





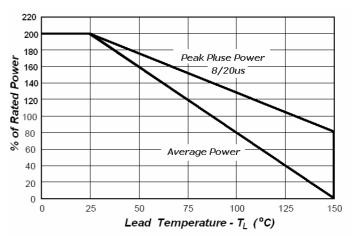
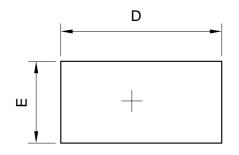
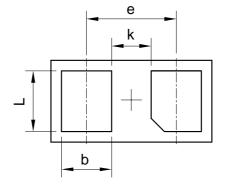


Fig2.Power Derating Curve

Package Outline Dimension

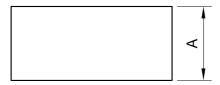




DFN0603-DL Dim Min Тур. Max D 0.58 0.61 0.64 Ε 0.28 0.31 0.34 0.34 е 0.20 0.23 0.26 b 0.16 0.19 0.22 Α 0.25 0.28 0.31 k 0.12 0.15 0.18 All Dimensions in mm

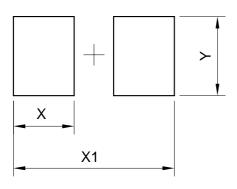
TOP VIEW

BOTTOM VVIEW



SSIDE VIEW

Suggested Pad layout



DFN0603-DL		
DIM	(mm)	
Χ	0.23	
X1	0.61	
Υ	0.30	



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