

ECL

SSI FUNCTIONS (Cont'd)

Item	Function	DEVICE NO.*	t _{pd} ns (Typ)	Power Dissipation mW (Typ)	Logic/ Connection Diagram	Package(s)
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OR/NOR Gates (Cont'd)

1	Quad OR/NOR	10101/10501	2.0	100	E74	4L,6B,9B
2	Quint OR/NOR	100102	0.7	230	E90	4Q,6Q
3	Quint Exc OR/NOR	100107	0.7	300	E91	4Q,6Q

OR/AND Gates

4	2-Wide OA	10118/10518	2.3	105	E86	4L,6B,9B
5	4-Wide OA	9505	2.6	315	E69	6B
6	4-Wide OA	10119/10519	2.3	105	E87	4L,6B,9B

OR/AND/Invert Gates

7	Triple 2-Wide OA/OAI	100117	0.7	240	E92	4Q,6Q
8	5-Wide OA/OAI	100118	0.7	175	E93	4Q,6Q
9	Dual 2-Wide OAI	10117/10517	2.3	105	E85	4L,6B,9B
10	4-Wide OAI	10121/10521	2.3	105	E88	4L,6B,9B

AND/NAND Gates

11	Quad AND/NAND	9507	3.2	315	E70	6B
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LATCHES/FLIP-FLOPS

Item	Function	DEVICE NO.*	Data Inputs	Direct Set/Clear or Common Clear	Enable/Clock Inputs (Level)	Required Enable/Clock Pulse Width-ns (Typ)	Enable /Clock to Q Delay-ns (Typ)	Data to Q Delay ns (Typ)	Power Dissipation mW (Typ)	Logic/Connection Diagram	Package(s)
12	750 MHz D Flip-Flop	11C06	2	No	┌	0.7	1.0	1.0	210	E43	6B
13	Dual D Flip-Flop	9528	2	Yes	┌	3.0	3.6	3.6	330	E26	6B
14	Dual D Flip-Flop	95H28	2	Yes	┌	2.0	3.0	3.0	330	E26	6B
15	Dual D Flip-Flop	95231	2	Yes	┌	2.5	2.8	2.8	245	E31	6B
16	Dual D Flip-Flop	10131/10531	2	Yes	┌	3.0	3.0	2.2	235	E31	4L,6B,9B
17	Dual D Flip-Flop	10231/10631	2	Yes	┌	2.5	2.8	2.8	245	E31	4L,6B,9B
18	Triple D Flip-Flop	100131	3	Yes	┌	1.0	1.3	0.85	475	E46	4Q,6Q
19	Hex D Flip-Flop	10176/10576	6	No	┌	3.0	3.0	5.0	455	E40	4L,6B,9B

*105XX and 106XX = Military temperature range