SBAS284A − JUNE 2003 − REVISED AUGUST 2003

60mA Switched-Cap Buck/Boost Converter

FEATURES

- **Pb−free SOT23−6 Package**
- **Wide Input Range: 2.7V to 5.5V**
- **Low Input Current Ripple**
- **Low Output Voltage Ripple**
- **Minimum Number of External Components—No Inductors**
- **1MHz Internal Oscillator Allows Small Capacitors**
- **Shutdown Mode**
- **Thermal and Current Limit Protection**

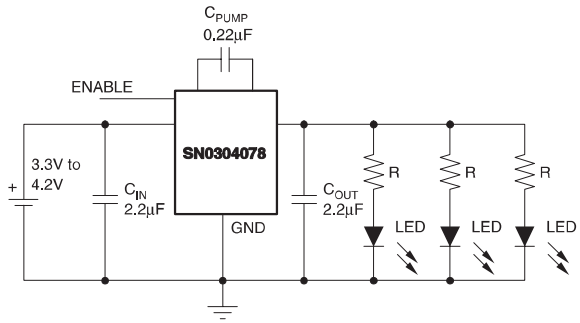
APPLICATIONS

- **Smart Card Readers**
- **SIM Card Supplies**
- **Cellular Phones**
- **Portable Communication Devices**
- **Personal Digital Assistants**
- **Notebook and Palm-Top Computers**
- **Modems**
- **Electronic Games**
- **Handheld Meters**
- **PCMCIA Cards**
- **Card Buses**
- **White LED Drivers**
- **LCD Displays**
- **Battery Backup Supplies**

DESCRIPTION

The SN0304078 is a switched capacitor voltage converter that produces a regulated, low-ripple output voltage from an unregulated input voltage. A wide-input supply voltage of 2.7V to 5.5V makes the SN0304078 ideal for a variety of battery sources, such as single cell Li-Ion, or two and three cell nickel- or alkaline-based chemistries.

The input voltage may vary above and below the output voltage and the output will remain in regulation. The SN0304078 provides low EMI dc/dc conversion without the need for an inductor. The high switching frequency allows the use of small surface-mount capacitors, saving board space and reducing cost. The SN0304078 is thermally protected and current limited, protecting the load and the regulator during fault conditions. Typical ground pin current (quiescent current) is 65µA with no load, and less than 1µA in shutdown mode.



SN0304078 Used in White LED Backlight Application

ABSOLUTE MAXIMUM RATINGS(1)

Supply Voltage	−0.3V to +6.0V
Enable Input	−0.3V to V_{IN}
Output Short-Circuit Duration	Indefinite
Operating Temperature Range	−55°C to +125°C
Storage Temperature Range	−65°C to +150°C
Junction Temperature	−55°C to +150°C
Lead Temperature (soldering, 3s)	+260°C

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability.

ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

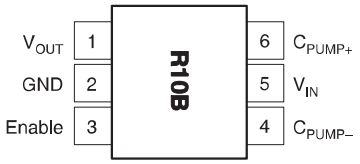
PACKAGE ORDERING INFORMATION

PRODUCT	OUTPUT VOLTAGE	PACKAGE-LEAD	PACKAGE DESIGNATOR(1)	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING(2)	ORDERING NUMBER	TRANSPORT MEDIA, QUANTITY
SN0304078DBV	5.0V	SOT23−6	DBV	−40°C to +85°C	R10B	SN0304078DBVT	Tape and Reel, 250
						SN0304078DBVR	Tape and Reel, 3000

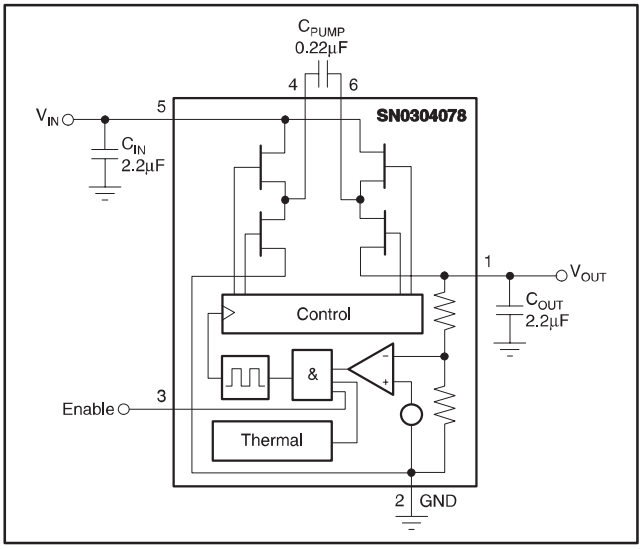
(1) For the most current specifications and product information, refer to our web site at www.ti.com.
(2) Voltage will be marked on reel.

PIN CONFIGURATION

Top View **TSOT23/SOT23**



SIMPLIFIED BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

Boldface limits apply over the specified temperature range, **T_A = −40°C to +85°C**

At T_A = +25°C, V_{IN} = V_{OUT}/2 + 0.75V, I_{OUT} = 10mA, C_{IN} = C_{OUT} = 2.2µF, C_{PUMP} = 0.22µF, V_{ENABLE} = 1.3V, unless otherwise noted.

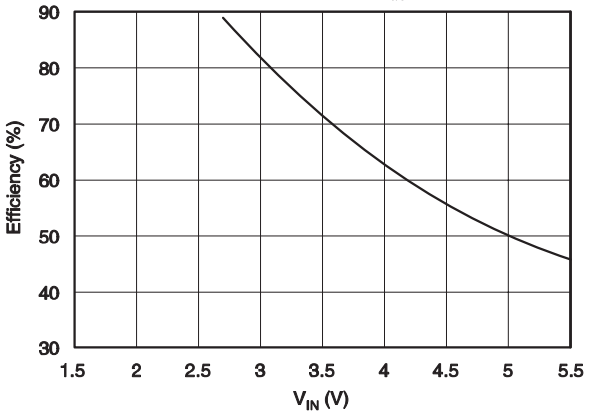
PARAMETER	CONDITIONS	SN0304078NA			UNITS
		MIN	TYP	MAX	
INPUT VOLTAGE	See conditions under Output Voltage				
Tested Startup	with a resistive load not lower than typical V_{OUT}/I_{OUT}.	**2.7**		**5.5**	V
OUTPUT VOLTAGE					
	I_{OUT} ≤ 10mA, 2.7V ≤ V_{IN} ≤ 5.5V	**4.7**	5.0	**5.3**	V
	I_{OUT} ≤ 30mA, 3.0V ≤ V_{IN} ≤ 5.5V	**4.7**	5.0	**5.3**	V
	I_{OUT} ≤ 60mA, 3.3V ≤ V_{IN} ≤ 4.2V	**4.6**	5.0	**5.4**	V
OUTPUT CURRENT					
Nominal		30			mA
Short Circuit(1)			100		mA
OSCILLATOR FREQUENCY(2)			1.0		MHz
EFFICIENCY(3)	I_{OUT} = 10mA, V_{IN} = 2.7V		90		%
RIPPLE VOLTAGE(4)	I_{OUT} = 30mA		35		mV_{PP}
ENABLE CONTROL	V_{IN} = 2.7V to 5.5V				
Logic High Input Voltage		**1.3**		**V_{IN}**	V
Logic Low Input Voltage		**−0.2**		**0.4**	V
Logic High Input Current				100	nA
Logic Low Input Current				100	nA
THERMAL SHUTDOWN					
Shutdown Temperature			160		°C
Shutdown Recovery			140		°C
SUPPLY CURRENT					
(Quiescent Current)	I_{OUT} = 0mA		65	100	µA
In Shutdown Mode	V_{IN} = 2.7V to 5.5V, Enable = 0V		0.01	1	µA
TEMPERATURE RANGE					
Specification Ambient Temperature T_A		−40		+85	°C
Operating Ambient Temperature T_A		−55		+125	°C
Storage Ambient Temperature T_A		−65		+150	°C
Thermal Resistance, θ_{JA}	SOT23−6		200		°C/W

(1) The supply current is twice the output short-circuit current.
(2) The converter regulates by enabling and disabling periods of switching cycles. The switching frequency is the oscillator frequency during an active period.
(3) See efficiency curves for other V_{IN}/V_{OUT} configurations.
(4) Effective Series Resistance (ESR) of capacitors is < 0.1Ω.

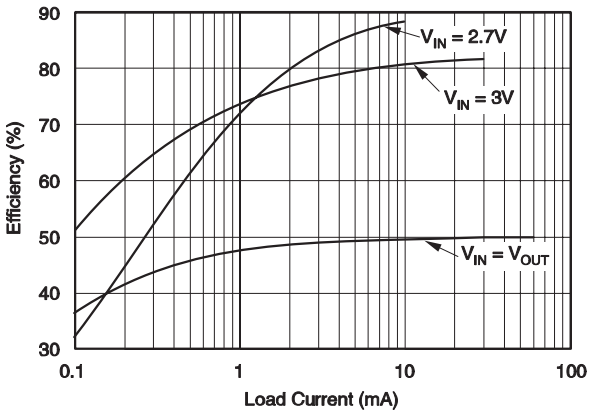
TYPICAL CHARACTERISTICS

At T_A = +25°C, V_{IN} = V_{OUT}/2 + 0.75V, I_{OUT} = 5mA, C_{IN} = C_{OUT} = 2.2µF, C_{PUMP} = 0.22µF, V_{ENABLE} = 1.3V, unless otherwise noted.

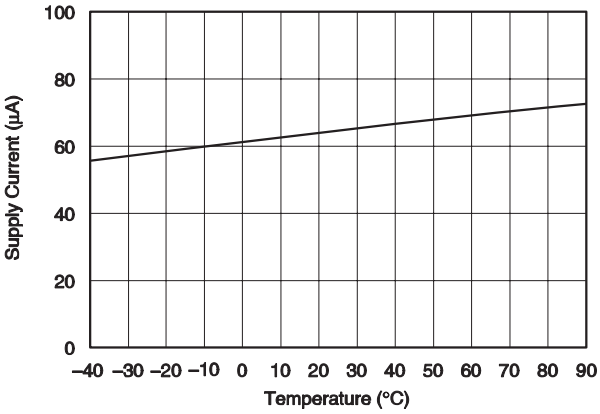
EFFICIENCY vs V_{IN}



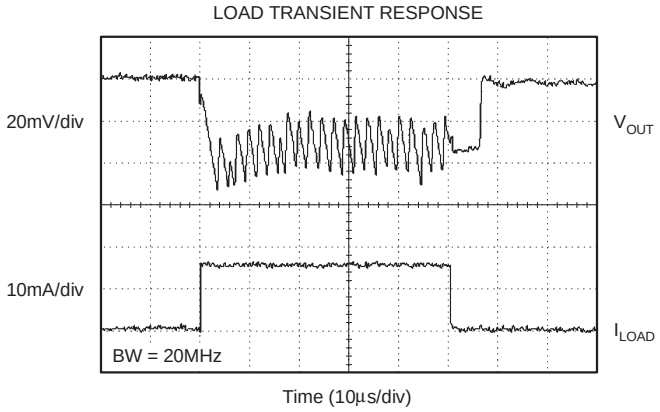
EFFICIENCY vs LOAD CURRENT



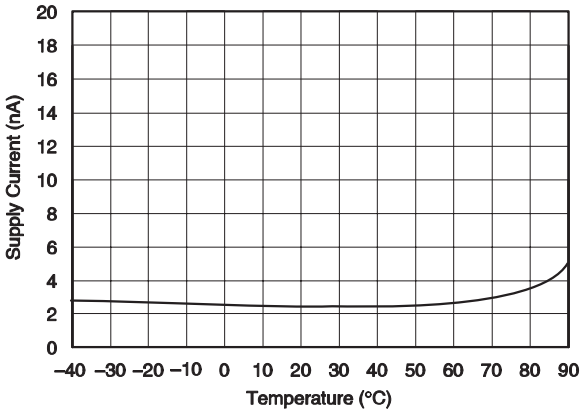
SUPPLY CURRENT vs TEMPERATURE
(No Load)



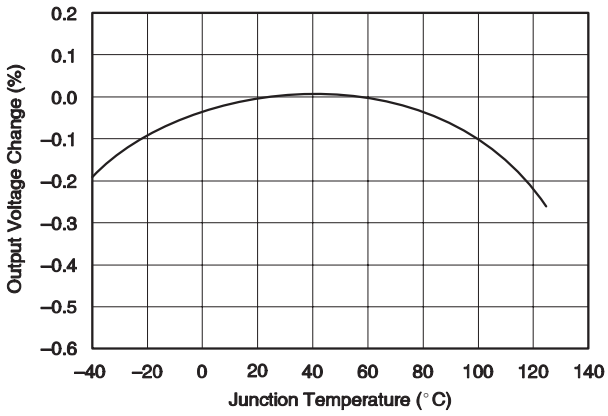
LOAD TRANSIENT RESPONSE



SUPPLY CURRENT vs TEMPERATURE
(Not Enabled)



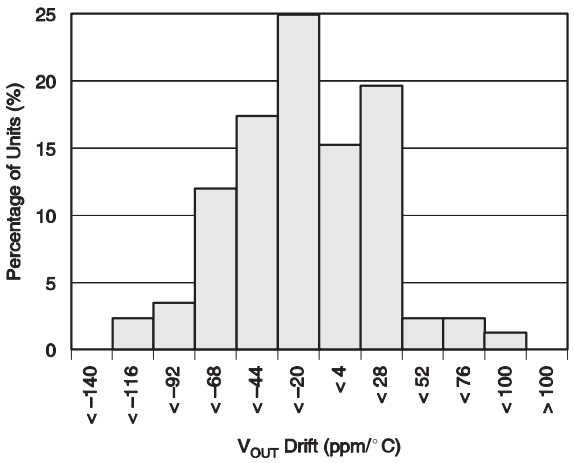
OUTPUT VOLTAGE vs TEMPERATURE



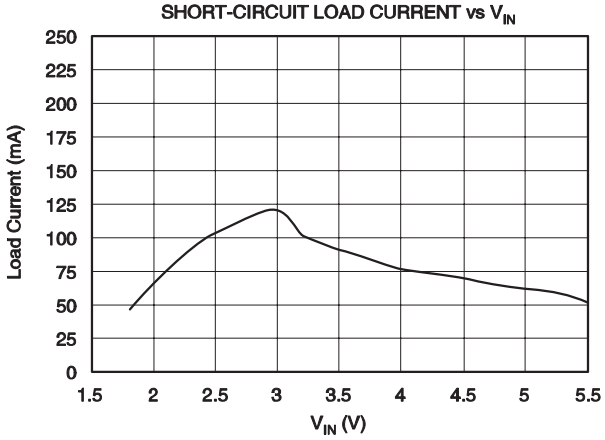
TYPICAL CHARACTERISTICS (Cont.)

At T_A = +25°C, V_{IN} = V_{OUT}/2 + 0.75V, I_{OUT} = 5mA, C_{IN} = C_{OUT} = 2.2µF, C_{PUMP} = 0.22µF, V_{ENABLE} = 1.3V, unless otherwise noted.

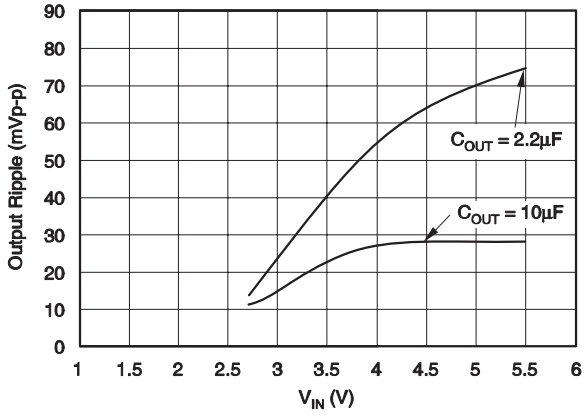
OUTPUT VOLTAGE DRIFT HISTOGRAM



SHORT-CIRCUIT LOAD CURRENT vs V_{IN}



OUTPUT RIPPLE VOLTAGE vs V_{IN}



THEORY OF OPERATION

The SN0304078 regulated charge pump provides a regulated output voltage for input voltages ranging from less than the output to greater than the output. The theory of operation is illustrated in Figure 1.

A conversion clock of 50% duty cycle is generated. During the first half−cycle the FET switches are configured as shown in Figure 1A, and C_{PUMP} charges to V_{IN}.

During the second half−cycle the FET switches are configured as shown in Figure 1B, and the voltage on C_{PUMP} is added to V_{IN}. The output voltage is regulated by skipping clock cycles as necessary.

PEAK CURRENT REDUCTION

In normal operation, the charging of the pump and output capacitors usually leads to relatively high peak input currents which can be much higher than the average load current. The regulator incorporates circuitry to limit the input peak current, lowering the total EMI production of the device and lowering output voltage ripple and input current ripple. Input capacitor (C_{IN}) supplies most of the charge required by input current peaks.

PROTECTION

The regulator has thermal shutdown circuitry that protects it from damage caused by overload conditions. The thermal protection circuitry disables the output when the junction temperature reaches approximately 160°C, allowing the device to cool. When the junction temperature cools to approximately 140°C, the output circuitry is automatically re−enabled. Continuously running the regulator into thermal shutdown can degrade device reliability. The regulator also provides current limit to protect itself and the load.

SHUTDOWN MODE

A control pin on the regulator can be used to place the device into an energy-saving shutdown mode. In this mode, the output is disconnected from the input as long as V_{IN} is greater than or equal to minimum V_{IN} and the input quiescent current is reduced to 1µA maximum.

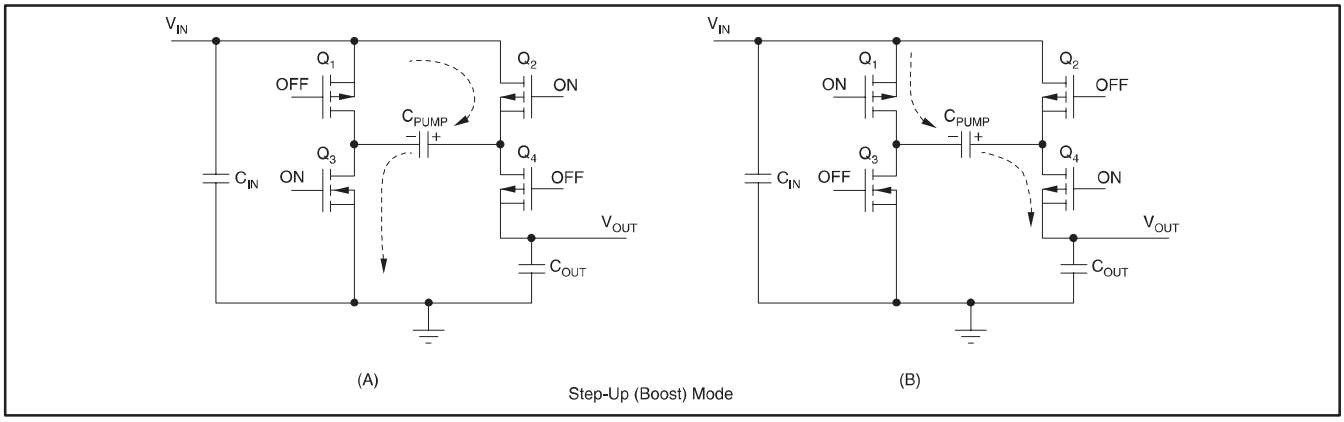


Figure 1. Simplified Schematic of the SN0304078 Operating in the Step-Up or Boost Mode

CAPACITOR SELECTION

For minimum output voltage ripple, the output capacitor C_{OUT} should be a ceramic, surface-mount type. Tantalum capacitors generally have a higher Effective Series Resistance (ESR) and may contribute to higher output voltage ripple. Leaded capacitors also increase ripple due to the higher inductance of the package itself. To achieve best operation with low input voltage and high load current, the input and pump capacitors (C_{IN} and C_{PUMP}, respectively) should also be surface-mount ceramic types. In all cases, X7R or X5R dielectric are recommended. See the typical operating circuit shown in Figure 2 for component values.

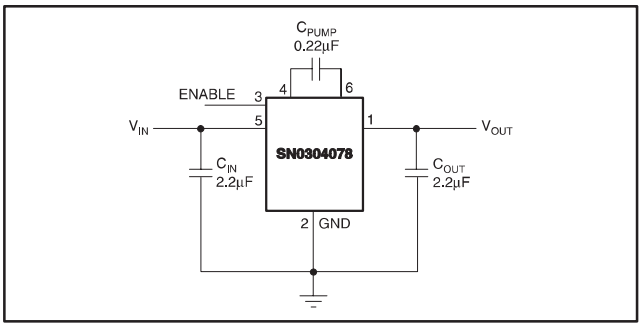


Figure 2. Typical Operating Circuit

With light loads or higher input voltage, a smaller 0.1µF pump capacitor (C_{PUMP}) and smaller 1µF input and output capacitors (C_{IN} and C_{OUT}, respectively) can be used. To minimize output voltage ripple, increase the output capacitor, C_{OUT}, to 10µF or larger.

The capacitors listed in Table I can be used with the SN0304078. This is only a representative list of those parts that are compatible.

EFFICIENCY

The efficiency of the charge pump regulator varies with the output voltage, the applied input voltage, the load current, and the internal operation mode of the device.

The approximate efficiency is given by:

$$\text{Efficiency (\%)} = V_{OUT}/(2 \times V_{IN}) \times 100$$

See the efficiency curves in the Typical Characteristics section for various loads and input voltages.

LAYOUT

Large transient currents flow in the V_{IN}, V_{OUT}, and GND traces. To minimize both input and output ripple, keep the capacitors as close as possible to the regulator using short, direct circuit traces.

A suggested PCB routing is shown in Figure 3. The trace lengths from the input and output capacitors have been kept as short as possible.

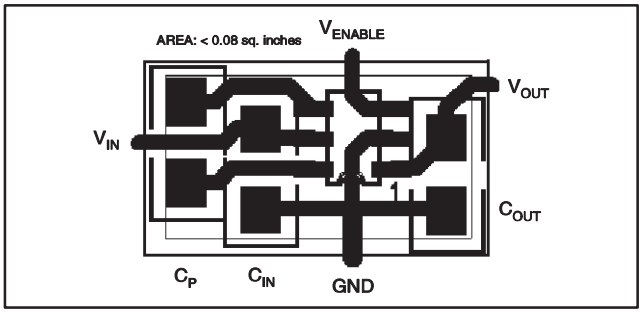


Figure 3. Suggested PCB Design for Minimum Ripple

Table 1. Suggested Capacitors.

MANUFACTURER	PART NUMBER	VALUE	TOLERANCE	DIELECTRIC MATERIAL	PACKAGE SIZE	RATED WORKING VOLTAGE
Kemet	C1206C255K8RAC	2.2µF	±10%	X7R	1206	10V
	C1206C224K8RAC	0.22µF	±10%	X7R	1206	10V
Panasonic	ECJ−2YBOJ225K	2.2µF	±10%	X5R	805	6.3V
	ECJ−2VBIC224K	0.22µF	±10%	X7R	805	16V
	ECJ−2VBIC104	0.1µF	±10%	X7R	805	16V
Taiyo Yuden	EMK316BJ225KL	2.2µF	±10%	X7R	1206	16V
	TKM316BJ224KF	0.22µF	±10%	X7R	1206	25V

APPLICATION CIRCUITS

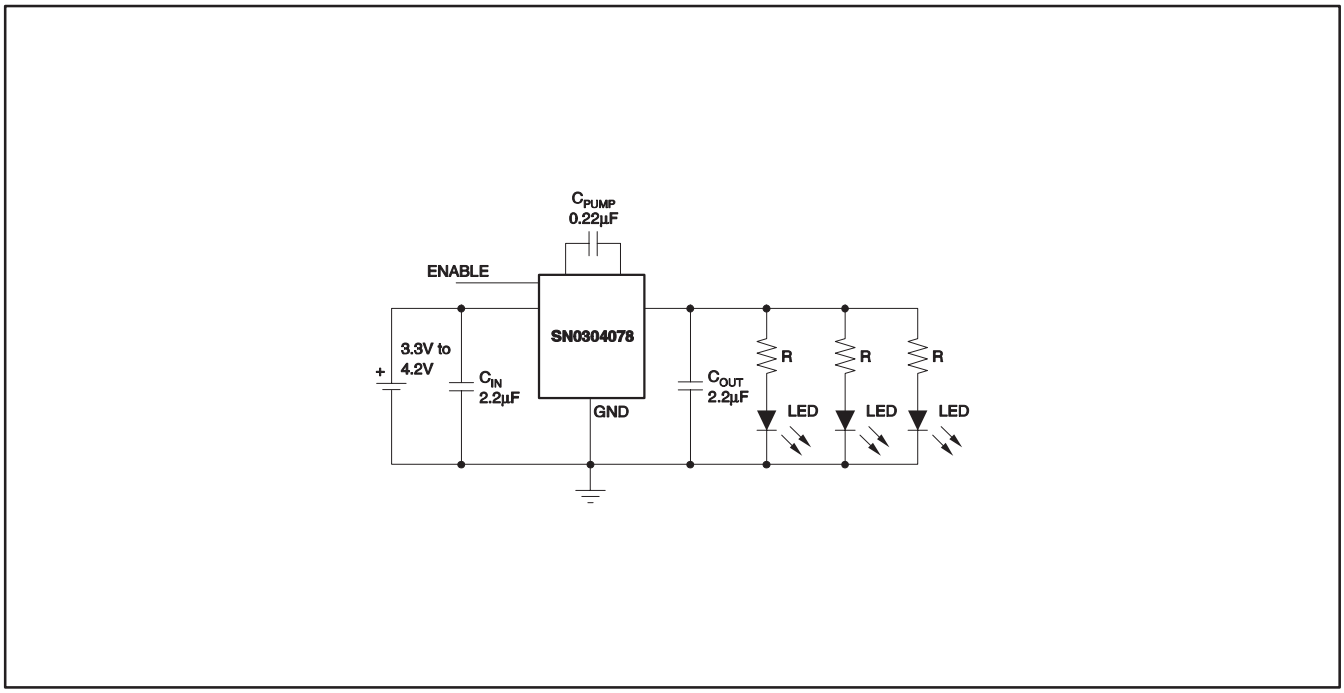


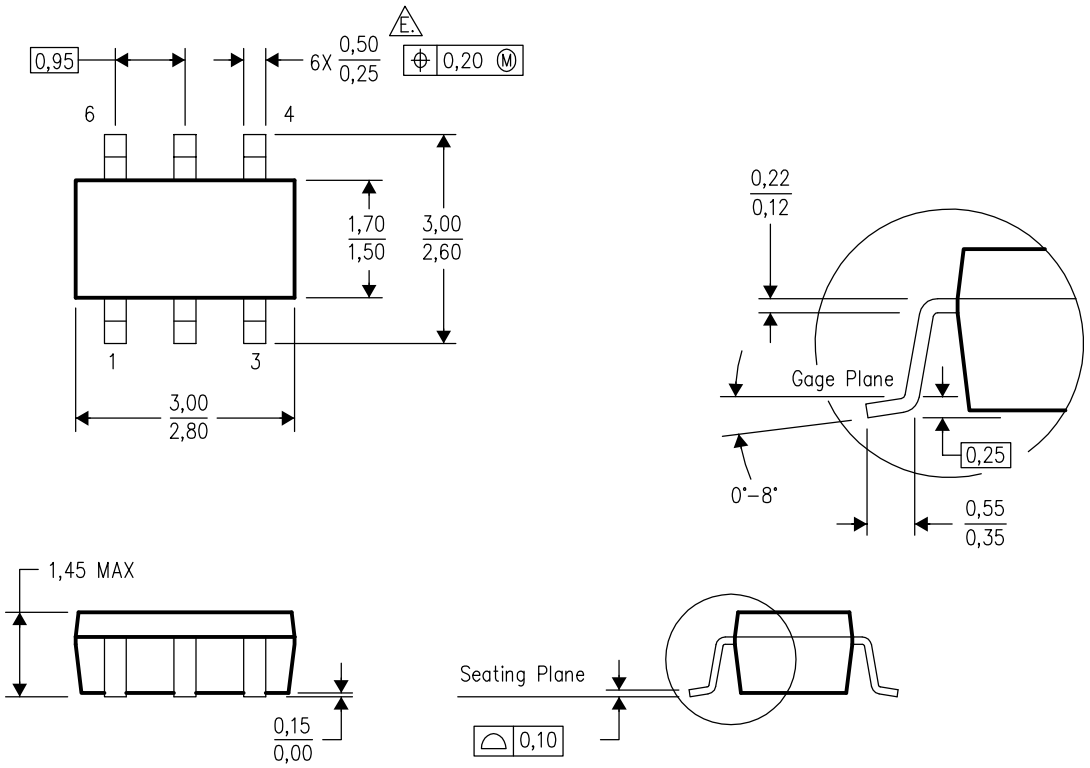
Figure 4. SN0304078 Circuit for Driving LEDs

PACKAGING INFORMATION

ORDERABLE DEVICE	STATUS(1)	PACKAGE TYPE	PACKAGE DRAWING	PINS	PACKAGE QTY
SN0304078DBVR	ACTIVE	SOP	DBV	6	3000
SN0304078DBVT	ACTIVE	SOP	DBV	6	250

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

DBV (R–PDSO–G6) PLASTIC SMALL–OUTLINE PACKAGE



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NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion.
D. Leads 1,2,3 may be wider than leads 4,5,6 for package orientation.
E. Falls within JEDEC MO–178 Variation AB, except minimum lead width.

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