SN54154, SN74154 4-LINE TO 16-LINE DECODERS/DEMULTIPLEXERS

SDLS056

- '154 is Ideal for High-Performance Memory Decoding
- Decodes 4 Binary-Coded Inputs into One of 16 Mutually Exclusive Outputs
- Performs the Demultiplexing Function by Distributing Data From One Input Line to Any One of 16 Outputs
- Input Clamping Diodes Simplify System
 Design
- High Fan-Out, Low-Impedance, Totem-Pole
 Outputs
- Fully Compatible with Most TTL and MSI Circuits

TYPICAL AVER		TYPICAL POWER DISSIPATION
3 LEVELS OF LOGIC	STROBE	FOWER DISSIPATION
23 ns	19 ns	170 mW

description

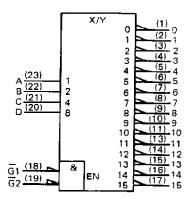
Each of these monolithic, 4-line-to-16-line decoders utilizes TTL circuitry to decode four binary-coded inputs into one of sixteen mutually exclusive outputs when both the strobe inputs, $\overline{G1}$ and $\overline{G2}$, are low. The demultiplexing function is performed by using the 4 input lines to address the output line, passing data from one of the strobe inputs with the other strobe input low. When either strobe input is high, all outputs are high. These demultiplexers are ideally suited for implementing high-performance memory decoders. For ultra-high speed systems, SN54S138/SN74S138 and SN54S139/SN74S139 are recommended.

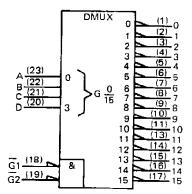
These circuits are fully compatible for use with most other TTL circuits. All inputs are buffered and input clamping diodes are provided to minimize transmissionline effects and thereby simplify system design.

The SN54154 is characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN74154 is characterized for operation from 0 °C to 70 °C.

SN74154.	J OR W PACKAGE N PACKAGE P VIEW)
0 1	24 VCC
1 2	23 A
2 3	22 B
3 4	21 C
4 5	20 D
5 6	19 G2
6 7	18 G1
7 8	17 15
8 9	16 14
9 10	15 13
10 11	14 12
GND 12	13 11

logic symbols (alternatives)† _





¹These symbols are in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

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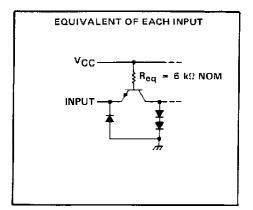
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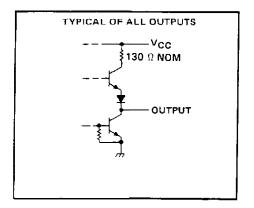
SN54154, SN74154 **4-LINE TO 16-LINE DECODERS/DEMULTIPLEXERS**

						1			FUN	ICTIO		3LE									
INPUTS											OUT	PUTS									
Ĝı	ĞΖ	D	<u> </u>	8	A	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
L	Ł	L	L	L	L	L	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н
L	L	L	L	L	н	н	L	H	н	н	н	н	н	н	н	н	н	н	н	н	н
L	L	L	L	н	L	н	н	L	н	н	н	н	н	н	н	н	н	н	н	н	н
L	L	ι.	I.	н	н	н	н	н	L	н	н	н	н	н	н	н	н	н	н	н	н
L	L	L	н	L	L	н	н	н	н	L	н	н	н	н	н	н	н	н	н	н	H
L	L	L	н	L	н	н	н	н	н	н	L	н	н	н	н	н	н	н	н	н	H
L	L	L	н	н	L	н	H	н	н	н	H	L	÷1	н	н	н	Н	н	н	н	н
L	L	L	н	н	н	н	н	н	н	н	н	н	L	н	н	н	н	н	н	н	+-
L	L	н	L.	L	L	н	н	н	н	н	н	н	н	L	н	н	н	н	н	н	н
L	L	н	L	L	н	н	н	н	н	н	н	н	н	н	L	н	н	н	н	н	н
L	L	н	L	н	L	н	н	н	н	н	н	н	н	н	13	L	+1	н	н	н	н
Ł	L	н	L	н	н	н	н	н	н	н	н	н	н	н	н	н	L	н	н	н	н
L	ι]	н	н	L	L	н	н	н	н	н	н	н	н	н	н	н	н	L	н	н	н
L	L	н	н	L	н	н	н	н	н	н	н	н	н	н	н	н	н	н	L	н	н
L	L	н	н	н	L	н	н	н	н	н	н	н	н	н	н	н	н	н	н	L	н
L	L	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	L
L	н	x	х	×	×	н	н	H	н	н	н	н	н	н	н	н	н	н	н	н	н
н	L	×	×	x	x	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н
н	н	х	х	х	х	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н	н

H = high level, L = low level, X = Irrelevant

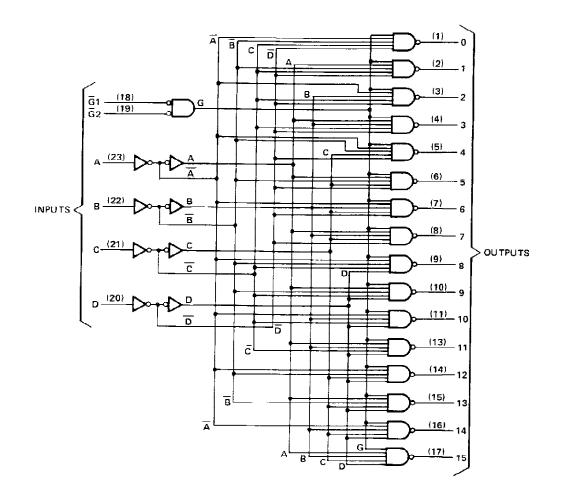
schematics of inputs and outputs







logic diagram (positive logic)





SN54154, SN74154 4-LINE TO 16-LINE DECODERS/DEMULTIPLEXERS

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	· · · · · · · · · · · · · · · · · · ·
Operating free-air temperature range: SN54154 Circuits	
SN74154 Circuits	0°C to 70°C
Storage temperature range	

NOTE 1: Voltage values are with respect to network ground terminal.

recommended operating conditions

		SN5415	4				
	MIN	NOM	MAX	MIN	NOM	MAX	
Supply voltage, V _{CC}	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH			-800			800	μA
Low-level output current, IOL			16			16	mA
Operating free-air temperature, T _A	-55		125	0		70	С

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TERT CONDUCTION		SN5415	54		4			
PARAMETER		TEST CONDITIONS [†]	MIN	түр	MAX	MIN	TYP‡	MAX		
VIH	High-level input voltage		2			2			V	
VIL	Low-level input voltage				0.8			0.8	V	
VIK	Input clamp voltage	V _{CC} = MIN, I _I = -12 mA			-1.5			-1.5	V	
v _{oн}	High-level output voltage	V _{CC} = MIN. V _{IH} = 2 V. V _{IL} = 0.8 V, I _{OH} = -800 µA	2.4	3.4		2.4	3.4		v	
VOL	Low-level output voltage	V _{CC} = MIN, V _{IH} = 2 V, V _{IL} = 0.8 V, I _{OL} = 16 mA		0.2	0.4		0.2	0.4	v	
1 ₁	Input current at maximum input voltage	V _{CC} = MAX, VI = 5.5 V	1		1			1	mΑ	
łн	High-level input current	V _{CC} = MAX, V ₁ = 2.4 V			40			40	μA	
4L	Low-level input current	V _{CC} = MAX, V _I = 0.4 V	T		-1.6			-1.6	mA	
los	Short-circuit output current §	V _{CC} = MAX	-20		-55	~18		-57	mΑ	
Icc	Supply current	V _{CC} = MAX, See Note 2		34	49		34	56	mА	

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type. [‡]All typical values are at V_{CC} = 5 V, T_A = 25 °C.

\$ Not more than one output should be shorted at a time,

NOTE 2: ICC is measured with all inputs grounded and all outputs open.

switching characteristics, VCC = 5 V, TA = 25°C

[PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
tpl.H	Propagation delay time, low-to-high-level output, from A, B, C, or D inputs through 3 levels of logic				24	36	пs
^t PHL	Propagation delay time, high-to-low-level output, from A, B, C, or D inputs through 3 levels of logic	 CL-15pF, RL-400Ω	RL-400 Ω,		22	33	ns
t P LH	Propagation delay time, low-to-high-level output, from either strobe input	See Note 3			20	30	17\$
TPHL	Propagation delay time, high-to-low-level output, from either strobe input				18	27	пs

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.





8-Oct-2017

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
5962-9558101QJA	LIFEBUY	CDIP	J	24	1	TBD	Call TI	N / A for Pkg Type	-55 to 125	5962-9558101QJ A SNJ54154J	
5962-9558101QKA	LIFEBUY	CFP	W	24	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9558101QK A SNJ54154W	
5962-9558101QKA	LIFEBUY	CFP	W	24	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9558101QK A SNJ54154W	
SN54154J	LIFEBUY	CDIP	J	24	1	TBD	Call TI	N / A for Pkg Type	-55 to 125	SN54154J	
SN54154J	LIFEBUY	CDIP	J	24	1	TBD	Call TI	N / A for Pkg Type	-55 to 125	SN54154J	
SNJ54154J	LIFEBUY	CDIP	J	24	1	TBD	Call TI	N / A for Pkg Type	-55 to 125	5962-9558101QJ A SNJ54154J	
SNJ54154J	LIFEBUY	CDIP	J	24	1	TBD	Call TI	N / A for Pkg Type	-55 to 125	5962-9558101QJ A SNJ54154J	
SNJ54154W	LIFEBUY	CFP	W	24	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9558101QK A SNJ54154W	
SNJ54154W	LIFEBUY	CFP	W	24	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9558101QK A SNJ54154W	

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

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8-Oct-2017

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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MECHANICAL DATA

MCDI004A - JANUARY 1995 - REVISED NOVEMBER 1997

CERAMIC DUAL-IN-LINE PACKAGE

J (R-GDIP-T**)



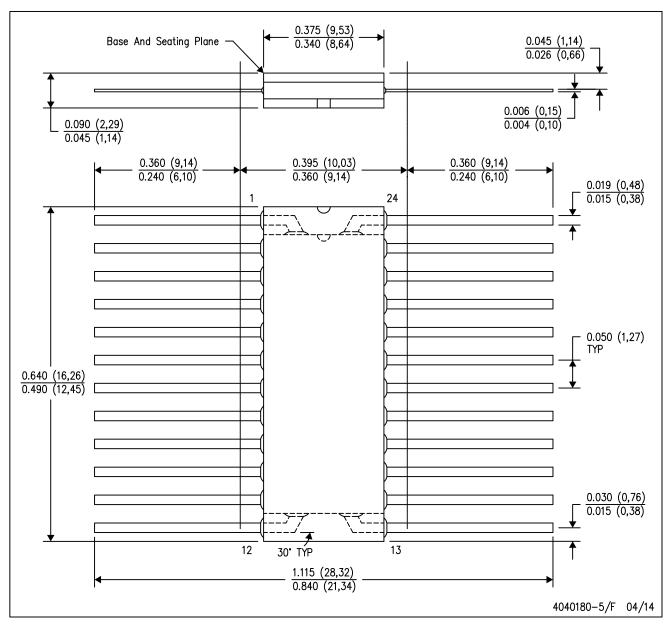
NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Window (lens) added to this group of packages (24-, 28-, 32-, 40-pin).
- D. This package can be hermetically sealed with a ceramic lid using glass frit.
- E. Index point is provided on cap for terminal identification.



CERAMIC DUAL FLATPACK

W (R-GDFP-F24)



NOTES: A. All linear dimensions are in inches (millimeters).

- This drawing is subject to change without notice. В.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
 E. Falls within Mil-Std 1835 GDFP2-F20



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