

# 5402/DM5402/DM7402 **Quad 2-Input NOR Gates**

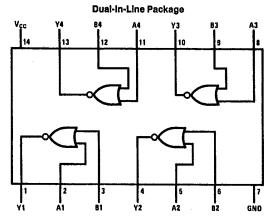
## **General Description**

This device contains four independent gates each of which performs the logic NOR function.

#### **Features**

Alternate Military/Areospace device (5402) is available.
 Contact a National Semiconductor Sales Office/Distributor for specifications.

#### **Connection Diagram**



TL/F/6492-1 Order Number 5402DMQB, 5402FMQB, DM5402J, DM5402W or DM7402N See NS Package Number J14A, N14A or W14B

#### **Function Table**

Inputs		Output
Α	B	Υ
٦	Ļ	Н
L	Н	L
Н	L	L
Н	н	L.

H = High Logic Level

L = Low Logic Level

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Absolute Maximum Ratings (Note)

if Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 5.5V

Operating Free Air Temperature Range DM54 and 54

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter	DM5402			DM7402			Units
	ratameter	Min	Nom	Max	Min	Nom	Max	Onno
Vcc	Supply Voltage	4.5	5	5.5	4.75	5	5.25	٧
VIH	High Level Input Voltage	2			2			· V
V <sub>IL</sub>	Low Level Input Voltage			8.0			0.8	>
Юн	High Level Output Current			-0.4			-0.4	mA
loL	Low Level Output Current			16			16	mA
TA	Free Air Operating Temperature	-55		125	0		70	°C

#### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units	
Vı	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -12 mA				-1.5	٧	
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max V <sub>IL</sub> = Max		2.4	3.4		٧	
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$			0.2	0.4	٧	
l <sub>i</sub>	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> = 5.5V			·	1	mA	
lн	High Level Input Current	$V_{CC} = Max, V_i = 2.4V$				40	μΑ	
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 0.4V				-1.6	mA	
los	Short Circuit	V <sub>CC</sub> = Max	DM54	-20		-55	mA	
	Output Current	(Note 2)	DM74	-18		55		
Госн	Supply Current with Outputs High	V <sub>CC</sub> = Max			8	16	mA	
ICCL	Supply Current with Outputs Low	V <sub>CC</sub> = Max			14	27	mA	

### Switching Characteristics at V<sub>CC</sub> = 5V and T<sub>A</sub> = 25°C (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units
tpLH	Propagation Delay Time Low to High Level Output	$C_L = 15  pF$ $H_L = 400 \Omega$		22	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output			15	ns

Note 1: All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

Note 2: Not more than one output should be shorted at a time.