

# 54AC08 Quad 2-Input AND Gate

#### **General Description**

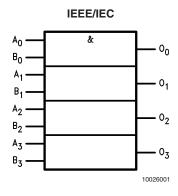
The 'AC08 contains four, 2-input AND gates.

#### **Features**

- I<sub>CC</sub> reduced by 50%
- Outputs source/sink 24 mA

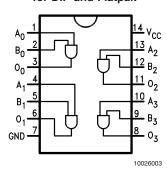
- Standard Microcircuit Drawing (SMD) 5962-87615
- 54AC08 now qualified to 300Krad RHA designation, refer to the SMD for more information
- For Military 54ACT08 device, see 54ACTQ08

#### **Logic Symbol**

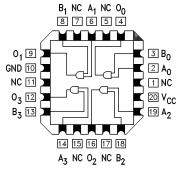


#### **Connection Diagrams**

Pin Assignment for DIP and Flatpak



### Pin Assignment for LCC



10026002

Pin Names	Description		
A <sub>n</sub> , B <sub>n</sub>	Inputs		
O <sub>n</sub>	Outputs		

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#### **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ ) -0.5V to +7.0V DC Input Diode Current ( $I_{IK}$ ) -20 mA

 $V_{\rm I} = V_{\rm CC} + 0.5 \mbox{V}$  +20 mA DC Input Voltage (V<sub>I</sub>) -0.5V to  $V_{\rm CC}$  + 0.5V

DC Output Diode Current ( $I_{OK}$ )

 $\begin{aligned} &V_{\rm O} = -0.5 \text{V} & -20 \text{ mA} \\ &V_{\rm O} = V_{\rm CC} + 0.5 \text{V} & +20 \text{ mA} \\ &\text{DC Output Voltage (V}_{\rm O}) & -0.5 \text{V to V}_{\rm CC} + \\ &0.5 \text{V} \end{aligned}$ 

DC Output Source

or Sink Current (I<sub>O</sub>) ±50 mA

 $\operatorname{DC}\,\operatorname{V}_{\operatorname{CC}}$  or Ground Current

per Output Pin ( $I_{CC}$  or  $I_{GND}$ )  $\pm 50$  mA Storage Temperature ( $T_{STG}$ )  $-65^{\circ}$ C to  $+150^{\circ}$ C Junction Temperature (T<sub>J</sub>)
CDIP

175°C

# Recommended Operating Conditions

Supply Voltage  $(V_{CC})$ 

'AC 2.0V to 6.0V Input Voltage  $(V_I)$  0V to  $V_{CC}$  Output Voltage  $(V_O)$  0V to  $V_{CC}$ 

Operating Temperature (T<sub>A</sub>)

54AC -55°C to +125°C

Minimum Input Edge Rate  $(\Delta V/\Delta t)$ 

'AC Devices

 $V_{\text{IN}}$  from 30% to 70% of  $V_{\text{CC}}$ 

V<sub>CC</sub> @ 3.3V, 4.5V, 5.5V 125 mV/ns

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT™ circuits outside databook specifications.

#### DC Characteristics for 'AC Family Devices

	Parameter		54AC		Conditions	
Symbol		V <sub>cc</sub>	$T_A = -55^{\circ}C \text{ to } +125^{\circ}C$	Units		
		(V)	0	-		
			Guaranteed Limits			
$V_{IH}$	Minimum High Level	3.0	2.1		$V_{OUT} = 0.1V$	
	Input Voltage	4.5	3.15	V	or V <sub>CC</sub> – 0.1V	
		5.5	3.85			
$V_{IL}$	Maximum Low Level	3.0	0.9		V <sub>OUT</sub> = 0.1V	
	Input Voltage	4.5	1.35	V	or V <sub>CC</sub> – 0.1V	
		5.5	1.65			
V <sub>OH</sub>	Minimum High Level	3.0	2.9		I <sub>OUT</sub> = -50 μA	
	Output Voltage	4.5	4.4	V		
		5.5	5.4			
					(Note 2) V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	
		3.0	2.4		-12 mA	
		4.5	3.7	V	I <sub>OH</sub> –24 mA	
		5.5	4.7		–24 mA	
V <sub>OL</sub>	Maximum Low Level	3.0	0.1		Ι <sub>ΟυΤ</sub> = 50 μΑ	
	Output Voltage	4.5	0.1	V		
		5.5	0.1			
					(Note 2) V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	
		3.0	0.5		12 mA	
		4.5	0.5	V	I <sub>OL</sub> 24 mA	
		5.5	0.5		24 mA	
I <sub>IN</sub>	Maximum Input	5.5	±1.0	μA	$V_I = V_{CC}$ , GND	
""	Leakage Current					
I <sub>OLD</sub>	(Note 3) Minimum	5.5	50	mA	V <sub>OLD</sub> = 1.65V Max	
I <sub>OHD</sub>	Dynamic Output Current	5.5	-50	mA	V <sub>OHD</sub> = 3.85V Min	
I <sub>CC</sub>	Maximum Quiescent	5.5	40.0	μA	$V_{IN} = V_{CC}$	
	Supply Current			1 '	or GND	

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## DC Characteristics for 'AC Family Devices (Continued) Note 2: All outputs loaded; thresholds on input associated with output under test.

Note 3: Maximum test duration 2.0 ms, one output loaded at a time.

#### **AC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub> (V) (Note 4)	54AC  T <sub>A</sub> = -55°C  to +125°C  C <sub>L</sub> = 50 pF		Units	Fig. No.
			Min	Max	1	
t <sub>PLH</sub>	Propagation Delay	3.3	1.0	12.5	ns	
		5.0	1.0	9.0		
t <sub>PHL</sub>	Propagation Delay	3.3	1.0	12.5	ns	
		5.0	1.0	9.0		

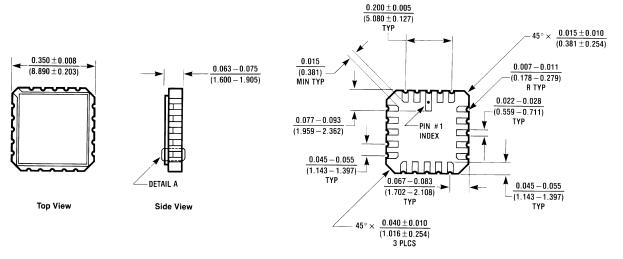
Note 4: Voltage Range 3.3 is  $3.3V \pm 0.3V$ Voltage Range 5.0 is 5.0V  $\pm$  0.5V

#### **Capacitance**

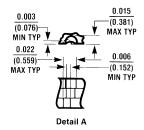
Symbol	Parameter	Тур	Units	Conditions
C <sub>IN</sub>	Input Capacitance	4.5	pF	V <sub>CC</sub> = OPEN
C <sub>PD</sub>	Power Dissipation	20.0	pF	$V_{CC} = 5.0V$
	Capacitance			

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#### Physical Dimensions inches (millimeters) unless otherwise noted

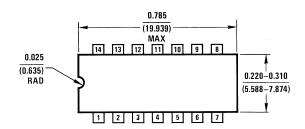


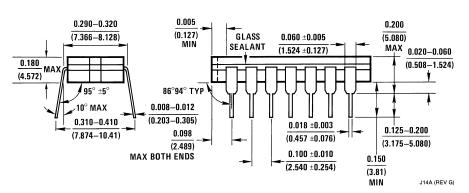
**Bottom View** 



E20A (REV D)

### 20-Terminal Ceramic Leadless Chip Carrier (L) NS Package Number E20A

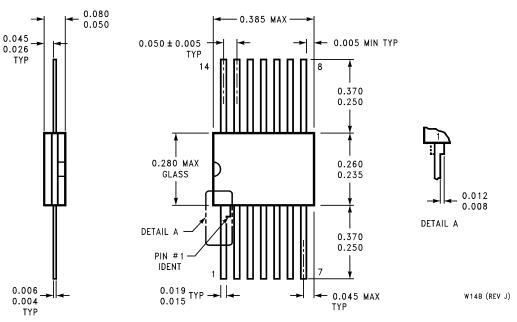




14-Lead Ceramic Dual-In-Line Package (D)
NS Package Number J14A

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#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Ceramic Flatpak (F) NS Package Number W14B

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