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LM195QML

Ultra Reliable Power Transistors

General Description

The LM195 is a fast, monolithic power integrated circuit with complete overload protection. This device, which acts as a high gain power transistor, has included on the chip, current limiting, power limiting, and thermal overload protection making it virtually impossible to destroy from any type of overload.

The inclusion of thermal limiting, a feature not easily available in discrete designs, provides virtually absolute protection against overload. Excessive power dissipation or inadequate heat sinking causes the thermal limiting circuitry to turn off the device preventing excessive heating.

The LM195 offers a significant increase in reliability as well as simplifying power circuitry. In some applications, where protection is unusually difficult, such as switching regulators, lamp or solenoid drivers where normal power dissipation is low, the LM195 is especially advantageous.

The LM195 is easy to use and only a few precautions need be observed. Excessive collector to emitter voltage can destroy the LM195 as with any power transistor. When the device is used as an emitter follower with low source impedance, it is necessary to insert a 5.0k resistor in series with the base lead to prevent possible emitter follower oscillations. Although the device is usually stable as an emitter follower, the resistor eliminates the possibility of trouble without degrading performance. Finally, since it has good high frequency response, supply bypassing is recommended.

Features

- Internal thermal limiting
- Greater than 1.0A output current
- 3.0 µA typical base current
- 500 ns switching time
- 2.0V saturation
- Base can be driven up to 40V without damage
- Directly interfaces with CMOS or TTL
- 100% electrical burn-in

Ordering Information

| NS Part Number | SMD Part Number | NS Package Number | Package Description |
|----------------|-----------------|-------------------|---------------------|
| LM195H/883 | 5962-8777801XA | H03B | 3LD T0–5 Metal Can |

Connection Diagram



Bottom View See NS Package Number H03B

Schematic Diagram



Absolute Maximum Ratings (Note 1)

| Collector to Emitter Voltage | 42V | | |
|---------------------------------------|--|--|--|
| Collector to Base Voltage | 42V | | |
| Base to Emitter Voltage (Forward) | 42V | | |
| Base to Emitter Voltage (Reverse) | 20V | | |
| Collector Current | Internally Limited | | |
| Power Dissipation (<i>Note 2</i>) | Internally Limited | | |
| Operating Temperature Range | | | |
| H-Pkg | –55°C ≤ T _A ≤ +125°C | | |
| Storage Temperature Range | $-65^{\circ}C \le T_A \le +150^{\circ}C$ | | |
| Lead Temperature (Soldering, 10 sec.) | 260°C | | |
| Thermal Resistance | | | |
| θ_{JA} | | | |
| H-Pkg; Still Air @ 0.5W | 192°C/W | | |
| H-Pkg; 500LF/Min Air Flow @ 0.5W | 66°C/W | | |
| θ.ις | | | |
| H-Pkg @ 1.0W | 29°C/W | | |
| - | | | |

Quality Conformance Inspection

Mil-Std-883, Method 5005 - Group A

| Subgroup | Description | Temp (°C) |
|----------|---------------------|-----------|
| 1 | Static tests at | +25 |
| 2 | Static tests at | +125 |
| 3 | Static tests at | -55 |
| 4 | Dynamic tests at | +25 |
| 5 | Dynamic tests at | +125 |
| 6 | Dynamic tests at | -55 |
| 7 | Functional tests at | +25 |
| 8A | Functional tests at | +125 |
| 8B | Functional tests at | -55 |
| 9 | Switching tests at | +25 |
| 10 | Switching tests at | +125 |
| 11 | Switching tests at | -55 |
| 12 | Settling time at | +25 |
| 13 | Settling time at | +125 |
| 14 | Settling time at | -55 |

LM195H/883 Electrical Characteristics

DC Parameter Collector to Emitter

| Symbol | Parameter | Conditions | Notes | Min | Max | Unit | Sub- groups |
|-----------------------------------|--------------------|---|-------------------|-------|------|------|----------------|
| V _{CE} | Operating Voltage | I _C ≤ I _{Max} | (<i>Note 3</i>) | | 42 | V | 1, 2, 3 |
| DC Pa | rameter Base to Er | nitter | | | | | |
| Symbol | Parameter | Conditions | Notes | Min | Max | Unit | Sub- groups |
| BV _{BE} | Breakdown Voltage | $V_{CE} \le 42V$ | (<i>Note 3</i>) | 42 | | V | 1, 2, 3 |
| I _{SC} Collector Current | Collector Current | V _{CE} ≤ 7V | | 1.2 | | А | 1 |
| | | | | 1 | | А | 2, 3 |
| V | Saturation Voltage | Ι – 1Δ | | | 2 | V | 1, 2 |
| ♥ Sat | Saturation voltage | | | | 2.5 | V | 3 |
| I _B | Base Current | $0 \le V_{BE} \le 42V,$ $I_C \le I_{Max}$ | | | 5 | μΑ | 1, 2, 3 |
| l _Q | Quiescent Current | $V_{CE} = 42V, V_{BE} = 0V$ | | | 5 | mA | 1, 2, 3 |
| | | V _C = 46-42V, I _L = 50mA | | -0.03 | 0.01 | V | 1 |

-0.03

-0.03

-10

-10

-10

-10

0.01

0.01

100

70

50

10

V

V

mV

mV

mV

mV

1

1

1

1

1

1

 $V_{\rm C} = 46-38V$

V_C = 50-42V

100µS

500µS

2mS

20mS

| AC P | arameter |
|------|----------|
|------|----------|

 V_{Bk}

Thr

Breakdown Delta V_{BE}

Thermal Response

| Symbol | Parameter | Conditions | Notes | Min | Max | Unit | Sub- groups |
|------------------|---------------|---|-------|-----|-----|------|----------------|
| t _{ON} | Response Time | $V_{I} = 0.2V, R_{L} = 36\Omega,$ V+ = 36V | | | 1.8 | μS | 9, 10, 11 |
| t _{OFF} | Response Time | $V_{I} = 2-0V, R_{L} = 36\Omega,$ V+ = 36V | | | 1.8 | μS | 9, 10, 11 |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower.

Note 3: Parameter tested go-no-go only.

Typical Performance Characteristics





TEMPERATURE (°C)







20156538

Response Time











1.0

COLLECTOR CURRENT (A)

10

20156509

0.3

0.1

0.01

0.1





Power PNP BASE 5.0k* 5.0k* 5.0k* 5.0k* 5.0k* Collector

*Protects against excessive base drive **Needed for stability

 LM195QML





†Solid Tantalum

LM195QML



†Solid Tantalum

Fast Optically Isolated Switch



CMOS or TTL Lamp Interface



Optically Isolated Power Transistor



Two Terminal Current Limiter





LM195QML

High Input Impedance AC Emitter Follower

+15V



Emitter Follower

v

LM195QML



20156532

*Sixty turns wound on Arnold Type A-083081-2 core. **Four devices in parallel

†Solid tantalum

| Released | Revision | Section | Changes |
|------------|----------|-------------------------------|---|
| 11/30/2010 | A | New Release, Corporate format | 1 MDS data sheets converted into one Corp. data sheet format. MNLM195-H Rev 0BL will be archived |
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LM195QML

Physical Dimensions inches (millimeters) unless otherwise noted



Notes

LM195QML

Notes

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