

FDP5680/FDB5680

60V N-Channel PowerTrench™ MOSFET

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

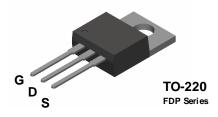
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{{\rm DS(on)}}$ specifications resulting in DC/DC power supply designs with higher overall efficiency.

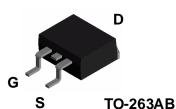
Features

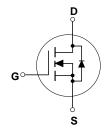
- 40 A, 60 V. R $_{\rm DS(ON)}$ = 0.020 Ω @ V $_{\rm GS}$ = 10 V $_{\rm RDS(ON)}$ = 0.023 Ω @ V $_{\rm GS}$ = 6 V.
- Critical DC electrical parameters specified at evevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- * High performance trend technology for extremely low $R_{_{\mathsf{DS}(\mathsf{ON})}}.$

FDB Series

• 175°C maximum junction temperature rating.







Absolute Maximum Ratings $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	FDP5680	FDB5680	Units
V _{DSS}	Drain-Source Voltage 60		60	V
V _{GSS}	Gate-Source Voltage ±20		V	
I _D	Maximum Drain Current - Continuous 40		10	Α
	- Pulsed	120		1
P _D	Total Power Dissipation @ T _C = 25°C	65		W
	Derate above 25°C		0.43	
T_J , T_{STG}	Operating and Storage Junction Temperature Range	-65 to +175		°C

Thermal Characteristics

R _e JC	Thermal Resistance, Junction-to-Case	2.3	°C/W
$R_{_{\theta}JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
FDB5680	FDB5680	13"	24mm	800
FDP5680	FDP5680	Tube	N/A	45

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-So	urce Avalanche Ratings (No	ote1)				
W _{DSS}	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 30 \text{ V}, I_{D} = 40 \text{A}$			90	mJ
I _{AR}	Maximum Drain-Source Avalanche	e Current			40	Α
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	60			V
<u>A</u> BVdss ΛTJ	Breakdown Voltage Temperature Coefficient	I _D = -250 _{LL} A, Referenced to 25°C		60		mV/∘C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 1)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	2.5	4	V
ΔVGS(th) ΔTJ	Gate Threshold Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		-6.4		mV/∘C
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 \circ \text{C}$ $V_{GS} = 6 \text{ V}, I_D = 19 \text{ A}$		0.016 0.022 0.018	0.020 0.035 0.023	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	20			Α
g _{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 20 \text{ A}$		43		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		1850		pF
Coss	Output Capacitance	f = 1.0 MHz		230		pF
C _{rss}	Reverse Transfer Capacitance	1		95		pF
Switchin	g Characteristics (Note 1)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 30 \text{ V}, I_D = 1 \text{ A},$		15	27	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		9	18	ns
t _{d(off)}	Turn-Off Delay Time			35	56	ns
t _f	Turn-Off Fall Time			16	26	ns
Q _g	Total Gate Charge	$V_{DS} = 30 \text{ V}, I_{D} = 20 \text{ A}$		33	46	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		6.5		nC
Q_{gd}	Gate-Drain Charge			7.5	· · · · ·	nC
Drain-So	urce Diode Characteristics	and Maximum Ratings				
I _s	Maximum Continuous Drain-Source				40	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 20 \text{ A}$ (Note 1)		0.9	1.2	V

^{1.} Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%

Typical Characteristics

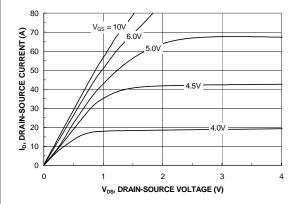


Figure 1. On-Region Characteristics.

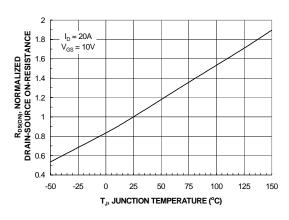


Figure 3. On-Resistance Variation with Temperature.

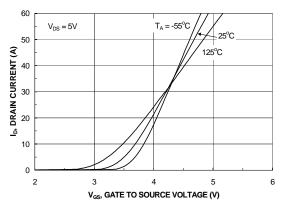


Figure 5. Transfer Characteristics.

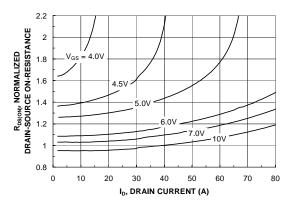


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

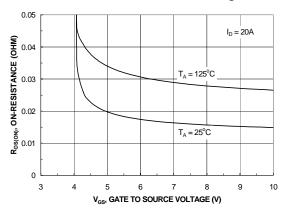


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

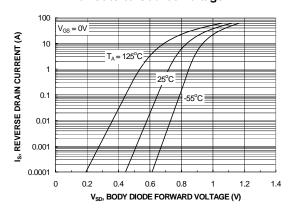
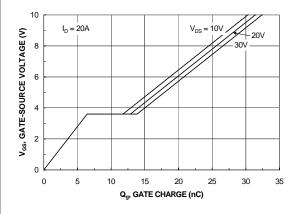


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.





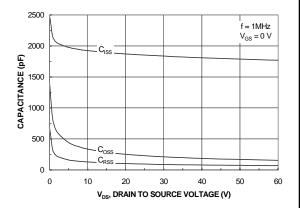
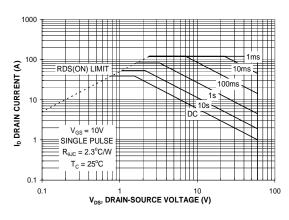


Figure 7. Gate-Charge Characteristics.





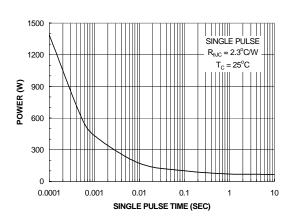


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

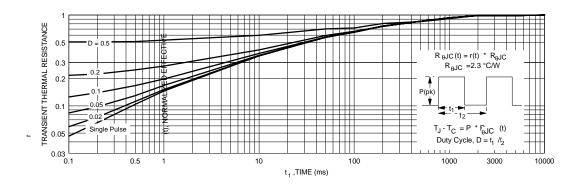


Figure 11. Transient Thermal Response Curve.

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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