

May 2007

DM74ALS151 1 of 8 Line Data Selector/Multiplexer

Features

- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Switching performance is guaranteed over full temperature and V_{CC} supply range
- Pin and functional compatible with LS family counterpart
- Improved output transient handling capability

General Description

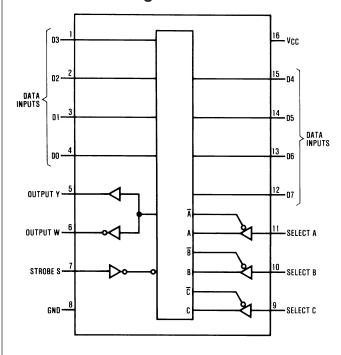
This Data Selector/Multiplexer contains full on-chip decoding to select one-of-eight data sources as a result of a unique three-bit binary code at the Select inputs. Two complementary outputs provide both inverting and non-inverting buffer operation. A Strobe input is provided which, when at the high level, disables all data inputs and forces the Y output to the LOW state and the W output to the HIGH state. The Select input buffers incorporate internal overlap features to ensure that select input changes do not cause invalid output transients.

Ordering Information

Order Number	Package Number	Package Description
DM74ALS151M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering number.

Connection Diagram



Function Table

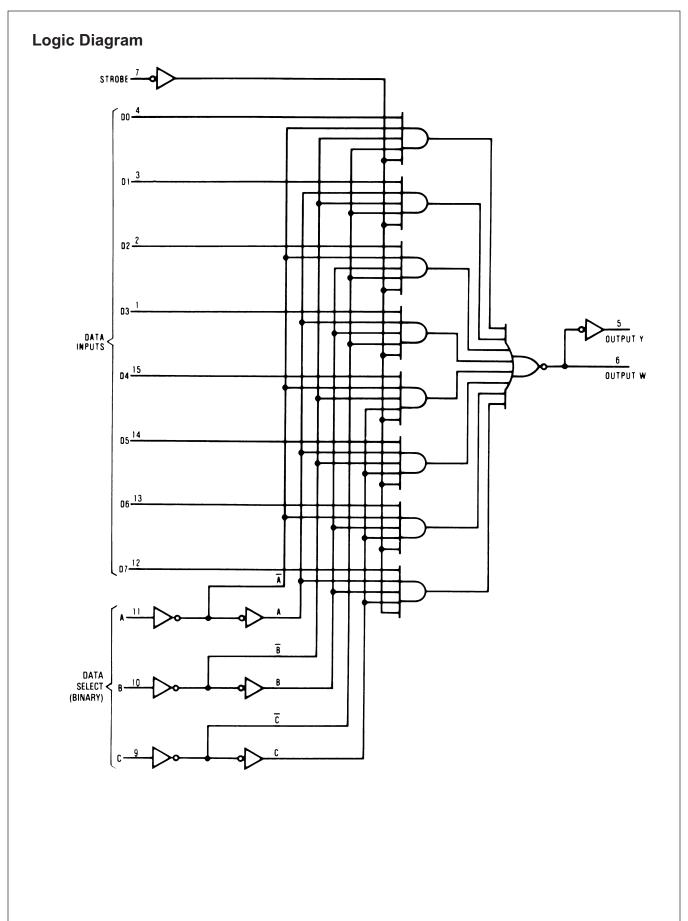
Inputs				Outp	outs	
	Select		Select Strobe			
С	В	Α	S	Υ	W	
Х	Х	Х	Н	L	Н	
L	L	L	L	D0	D0	
L	L	Н	L	D1	D1	
L	Н	L	L	D2	D2	
L	Н	Н	L	D3	D3	
Н	L	L	L	D4	D4	
Н	L	Н	L	D5	D5	
Н	Н	L	L	D6	D6	
Н	Н	Н	L	D7	D7	

H = HIGH Level

L = LOW Level

X = Don't Care

D0 thru D7 = the level of the respective D input



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	7V
V _I	Input Voltage	
T _A	Operating Free Air Temperature Range	0°C to +70°C
T _{STG}	Storage Temperature Range -65	
θ_{JA}	Typical Thermal Resistance	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Nom.	Max.	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-2.6	mA
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

Over recommended operating free-air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25$ °C.

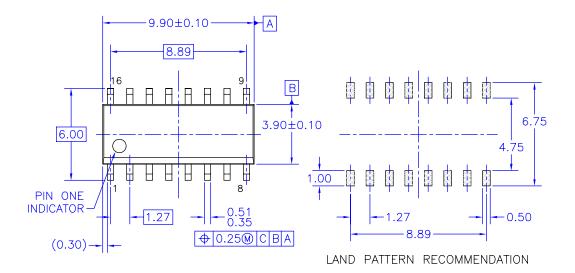
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_{IN} = -18mA$			-1.5	V
V _{OH}	HIGH Level Output Voltage	$V_{CC} = 4.5V$, $I_{OH} = Max$.	2.4	3.2		V
		$I_{OH} = -400 \mu A,$ $V_{CC} = 4.5 V \text{ to } 5.5 V$	V _{CC} – 2			
V _{OL}	LOW Level Output Voltage	$V_{CC} = 4.5V, I_{OL} = 24mA$		0.35	0.5	V
I _I	Input Current at Maximum Input Voltage	$V_{CC} = 5.5V, V_{IN} = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IN} = 2.7V$			20	μA
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IN} = 0.4V$			-0.1	mA
Io	Output Drive Current	$V_{CC} = 5.5V, V_{OUT} = 2.25V$	-30		-112	mA
I _{CC}	Supply Current	$V_{CC} = 5.5V$, All Inputs = 4.5V		7.5	12	mA

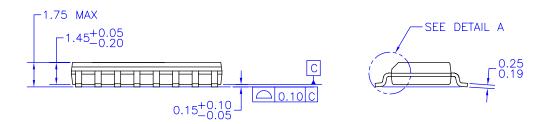
Switching CharacteristicsOver recommended operating free air temperature range.

Symbol	Parameter	Conditions	From	То	Min.	Max.	Units
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output	$V_{CC} = 4.5V \text{ to } 5.5V,$ $C_{L} = 50pF,$	Select	Υ	4	18	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output	$R_L = 500\Omega$	Select	Υ	8	24	ns
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output		Select	W	7	24	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output		Select	W	7	23	ns
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output		Data	Υ	3	10	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output		Data	Υ	5	15	ns
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output		Data	W	3	15	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output		Data	W	4	15	ns
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output		Strobe	Y	4	18	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output		Strobe	Υ	4	19	ns
t _{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output		Strobe	W	5	19	ns
t _{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output		Strobe	W	5	23	ns

Physical Dimensions

Dimensions are in millimeters unless otherwise noted.





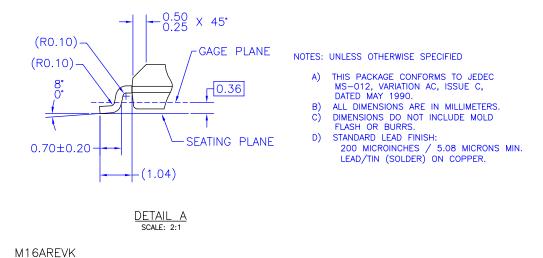


Figure 1. 16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Package Number M16A





TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks

ACEx® Build it Now™ CorePLUS™ CROSSVOLT™ CTL™ Current Transfer Logic™ EcoSPARK® FACT Quiet Series™ FACT® FAST® FastvCore™ FPS™ FRFET® Global Power Resource™	Green FPS™ e-Series™ GTO™ i-Lo™ IntelliMAX™ ISOPLANAR™ MegaBuck™ MICROCOUPLER™ MicroPak™ Motion-SPM™ OPTOLOGIC® OPTOPLANAR® PDP-SPM™ Power220® Power247®	Power-SPM™ PowerTrench® Programmable Active Droop™ QFET® QS™ QT Optoelectronics™ Quiet Series™ RapidConfigure™ SMART START™ SPM® STEALTH™ SuperFET™ SuperSOT™-3 SuperSOT™-6	SyncFETTM The Power Franchise® TM TinyBoost™ TinyBuck™ TinyLogic® TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ µSerDes™ UHC® UniFET™
Global Power Resource SM	Power247 [®]	SuperSOT™-6	VCX™
Green FPS™	POWEREDGE [®]	SuperSOT™-8	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information Formative or In Design		This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I28