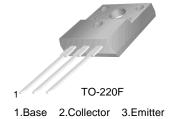


KSA1304

Vertical Output Applications Power Amplifier Applications

Complement to KSC3296



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
V _{CBO}	Collector-Base Voltage	- 150	V	
V _{CEO}	Collector-Emitter Voltage	- 150	V	
V _{EBO}	Emitter-Base Voltage	- 5	V A A	
I _C	Collector Current	- 1.5		
I _B	Base Current	- 0.5		
P _C	Collector Dissipation (T _C =25°C)	20	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 55 ~ 150	°C	

Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = -120V, I_{E} = 0$			- 10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 10	μΑ
h _{FE}	DC Current Gain	$V_{CE} = -10V, I_{C} = -500mA$	40	75	140	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 500mA, I _B = - 50mA			- 1.5	V
V _{BE} (on)	Base-Emitter ON Voltage	V _{CE} = - 10V, I _C = - 500mA	- 0.65	- 0.75	- 0.85	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 10V, I _C = - 500mA		4		MHz
C _{ob}	Output Capacitance	V _{CB} = - 10V, f = 1MHz		55		pF

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Typical Characteristics

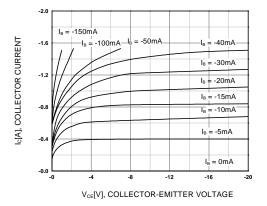


Figure 1. Static Characteristic

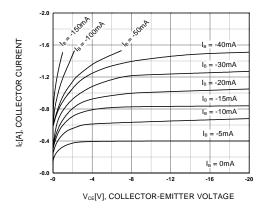


Figure 2. Static Characteristic

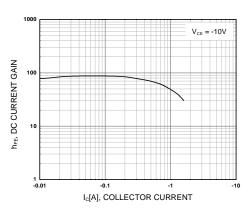


Figure 3. DC current Gain

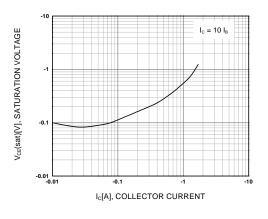


Figure 4. Collector-Emitter Saturation Voltage

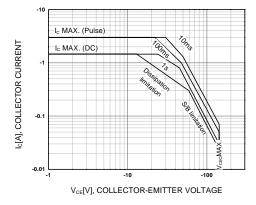


Figure 5. Safe Operating Area

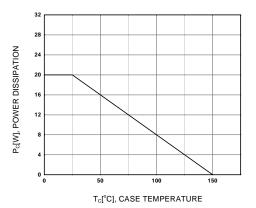
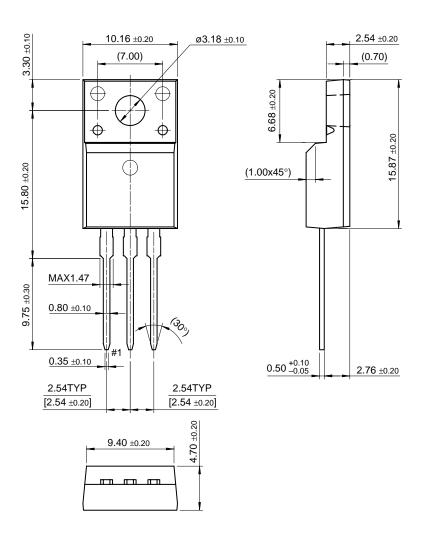


Figure 6. Power Derating

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Package Demensions

TO-220F



Dimensions in Millimeters

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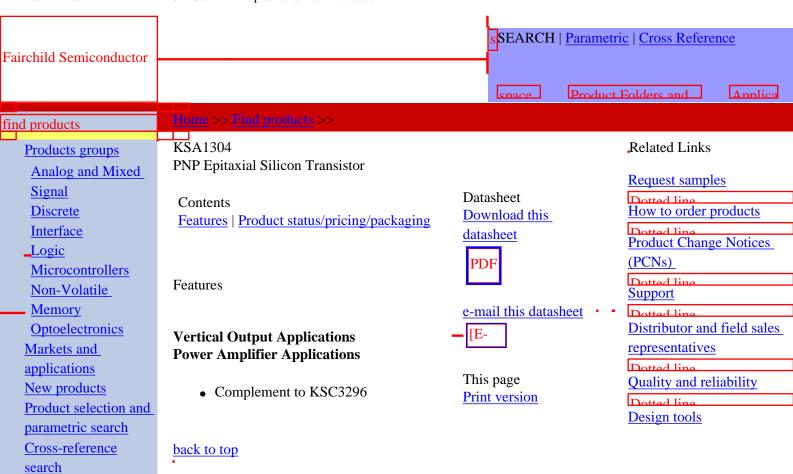
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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
KSA1304YTU	Full Production	\$0.318	<u>TO-220F</u>	3	RAIL
KSA1304OTU	Full Production	\$0.318	<u>TO-220F</u>	3	RAIL

^{* 1,000} piece Budgetary Pricing

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