

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

2SC6082 — NPN Epitaxial Planar Silicon Transistor 50V / 15A High-Speed Switching Applications

Applications

· High-speed switching applications (switching regulator, driver circuit)

Features

- · Adoption of MBIT process
- · Low collector-to-emitter saturation voltage
- · Large current capacitance
- · High-speed switching

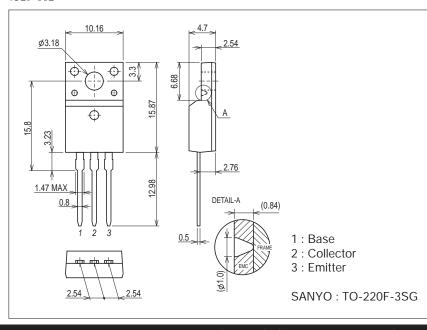
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCES		60	V
	VCEO		50	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		15	А
Collector Current (Pulse)	ICP	PW≤10μs, duty cycle≤1%	20	А
Base Current	IB		3	Α
Collector Dissipation	PC		2	W
		Tc=25°C	23	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7529-002



Product & Package Information

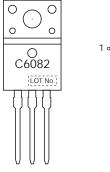
• Package : TO-220F-3SG

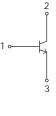
• JEITA, JEDEC : SC-67

• Minimum Packing Quantity

: 50 pcs./magazine

Marking Electrical Connection





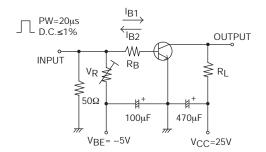
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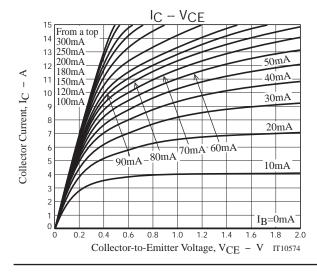
Electrical Characteristics at Ta=25°C

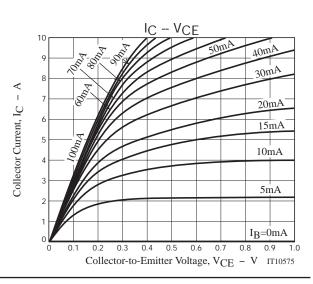
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0A			10	μА
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0A			10	μΑ
DC Current Gain	hFE1	V _{CE} =2V, I _C =330mA	200		560	
	h _{FE} 2	V _{CE} =2V, I _C =10A	50			
Gain-Bandwidth Product	fŢ	V _{CE} =10V, I _C =2A		195		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		85		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =7.5A, I _B =375mA		200	400	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =7.5A, I _B =375mA			1.2	٧
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=100μA, IE=0A	60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =100μA, R _{BE} =0Ω	60			V
Collector-to-Emitter Breakdown voltage	V(BR)CEO	IC=1mA, RBE=∞	50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =100μA, I _C =0A	6			٧
Turn-On Time	ton			52		ns
Storage Time	tstg	See specified Test Circuit		560		ns
Fall Time	t _f			37		ns

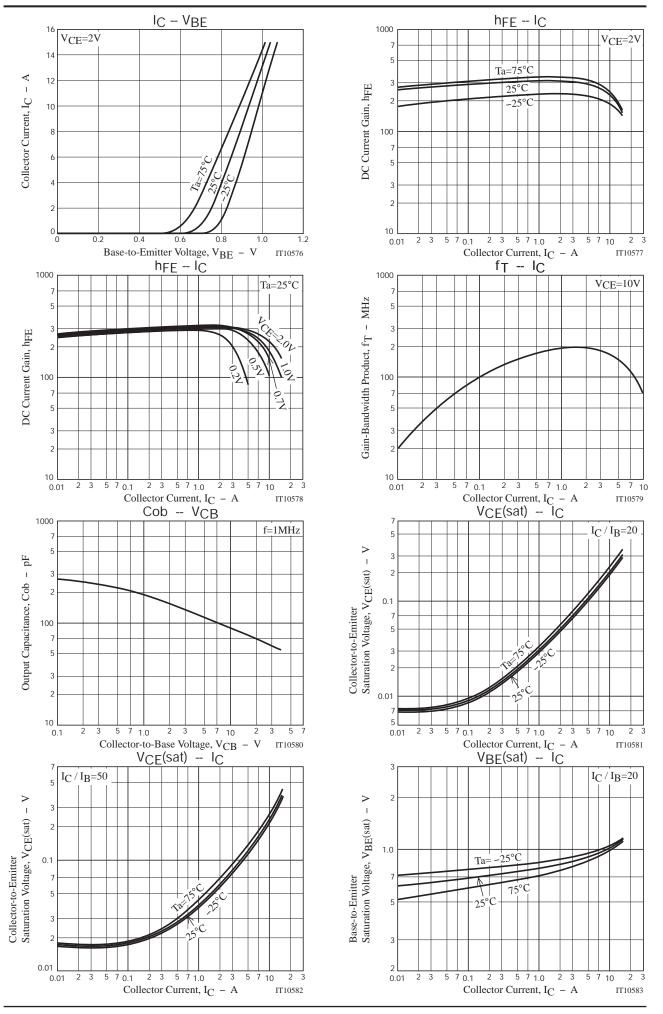
Switching Time Test Circuit

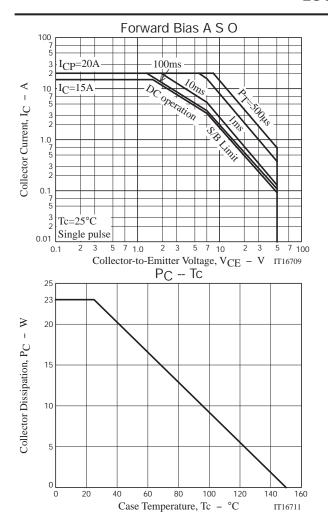


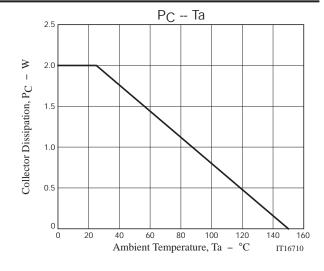
 $I_{C}=20I_{B1}=-20I_{B2}=5A$











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