

FGH40T65SPD_F085 650V, 40A Field Stop Trench IGBT

Features

- AEC-Q101 Qualified
- + Low Saturation Voltage : $V_{CE(sat)}$ = 1.85 V(Typ.) @ I_C = 40 A
- 100% of the parts are dynamically tested (Note 1)
- Short Circuit Ruggedness > 5 μs @ 25 °C
- Maximum Junction Temperature : T₁ = 175 °C
- Fast Switching
- Tight Parameter Distribution
- Positive Temperature Co-efficient for Easy Parallel Operating
- · Copacked with soft, fast recovery diode
- · RoHS Compliant

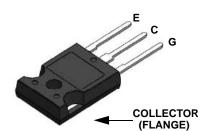


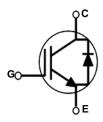
General Description

Using the novel field stop 3rd generation IGBT technology, FGH40T65SPD_F085 offers the optimum performance with both low conduction loss and switching loss for a high efficiency operation in various applications, while provides 50V higher blocking voltage and rugged high current switching reliability. Meanwhile, this part also offers and advantage of outstanding performance in parallel operation.

Applications

- Onboard Charger
- AirCon Compressor
- PTC Heater
- Motor Drivers
- Other automotive power-train appliactions





Absolute Maximum Ratings

Symbol	Description		Ratings	Units
V _{CES}	Collector to Emitter Voltage	650	V	
V _{GES}	Gate to Emitter Voltage	± 20	V	
	Transient Gate to Emitter Voltage		± 30	V
I _C	Collector Current	@ T _C = 25 °C	80	A
·C	Collector Current	@ T _C = 100 °C	40	A
I _{CM}	Pulsed Collector Current	(Note 2)	120	A
I _F	Diode Forward Current	@ T _C = 25 °C	40	A
	Diode Forward Current	@ T _C = 100 °C	20	A
I _{FM}	Pulsed Diode Maximum Forward Curren	t (Note 2)	120	A
D	Maximum Power Dissipation	@ T _C = 25 °C	267	W
P _D	Maximum Power Dissipation	@ T _C = 100 °C	134	W
SCWT	Short Circuit Withstand Time @ $T_C = 25 ^{\circ}C$		5	μs
TJ	Operating Junction Temperature	-55 to +175	°C	
T _{stg}	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	°C	

Notes:

1: V_{CC} = 400 V, V_{GE} = 15 V, I_C = 120 A, R_G = 20 Ω , Inductive Load 2: Repetitive rating: pulse width limited by max. junction temperature

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	I Character									
Symbo	bl	Paramete	r		Тур.		Max.	(Units	
$R_{\theta JC}(IGBT)$) Thermal Re	esistance, Junction to Ca	se			-	0.56		°C/W	
$R_{\theta JC}(Diode$	e) Thermal Re	esistance, Junction to Ca	se			-	1.71		°C/W	
R_{\thetaJA}	Thermal Re	esistance, Junction to An	nbient			-	40	°C/W		
Packag	e Marking a	and Ordering In	form	nation						
Devic	e Marking	Device		Package	;	Pacin	д Туре	Qty pe	r Tube	
FGH	40T65SPD	FGH40T65SPD_F0	85	TO-247 G0	3 Tube		ube	30ea		
Electric	al Characte	eristics of the I	GBT	T _C = 25 °C unless othe	erwise no	ted				
Symbol	Pa	irameter		Test Conditio	ons Min		. Тур.	Max.	Units	
Off Charac	teristics									
BV _{CES}	Collector to Emi	tter Breakdown Voltage	V _{GF} =	: 0V, I _C = 1mA		650	-	-	V	
$\frac{\Delta BV_{CES}}{\Delta T_{J}}$	Temperature Co Voltage	efficient of Breakdown		$V_{GE} = 0V, I_C = 1mA$		-	0.6	-	V/ºC	
I _{CES}	Collector Cut-O	bllector Cut-Off Current		$V_{CE} = V_{CES}, V_{GE} = 0V$		-	-	250	μA	
I _{GES}	G-E Leakage C	urrent	$V_{GE} = V_{GES}, V_{CE} = 0V$		-	-	± 400	nA		
On Charac	teristics		_				1			
V _{GE(th)}	G-E Threshold	/oltage	ge $I_{C} = 40 \text{mA}, V_{CE} = V_{GE}$			4.0	5.5	7.5	V	
02(11)			$I_{\rm C} = 40$ A, $V_{\rm GE} = 15$ V		-	1.85	2.4	V		
V _{CE(sat)}	Collector to Emi	Collector to Emitter Saturation Voltage		$I_{C} = 40A, V_{GE} = 15V,$ $T_{C} = 175 \text{ °C}$		-	2.51	-	V	
Dynamic C	haracteristics									
C _{ies}	Input Capacitan	се				-	1518	-	pF	
C _{oes}	Output Capacita	utput Capacitance everse Transfer Capacitance		V _{CE} = 30V, V _{GE} = 0V, f = 1MHz		-	91	-	pF	
C _{res}	Reverse Transfe					-	15	-	pF	
Switching	Characteristics						•			
T _{d(on)}	Turn-On Delay	Time				-	18	-	ns	
T _r	Rise Time		$V_{CC} = 400V, I_C = 40A,$ $R_G = 6\Omega, V_{GE} = 15V,$ Inductive Load, $T_C = 25$			-	42	-	ns	
T _{d(off)}	Turn-Off Delay	Time				-	35	-	ns	
T _f	Fall Time				0.0	-	10	-	ns	
Eon	Turn-On Switch	ing Loss			J.C	-	1.16	-	mJ	
E _{off}	Turn-Off Switch	ing Loss				-	0.27	-	mJ	
E _{ts}	Total Switching	Loss	1			-	1.43	-	mJ	
T _{d(on)}	Turn-On Delay	Time				-	16	-	ns	
T _r	Rise Time		$V_{CC} = 400V, I_C = 40A, R_G = 6\Omega, V_{GE} = 15V,$			-	40	-	ns	
T _{d(off)}	Turn-Off Delay	Time				-	37	-	ns	
Τ _f	Fall Time				E ⁰ C	-	11	-	ns	
Eon	Turn-On Switch	ing Loss	mauci	tive Load, T _C = 17	ິບ	-	1.59	-	mJ	
E _{off}	Turn-Off Switch	ing Loss	-			-	0.42	-	mJ	
E _{ts}	Total Switching	Loss				-	2.01	-	mJ	

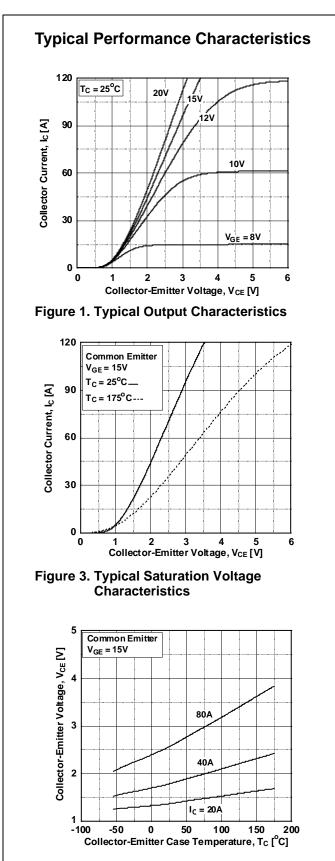
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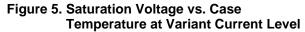
Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Units
Qg	Total Gate Charge		-	36	-	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400V, I _C = 40A, V _{GE} = 15V	-	11	-	nC
Q _{gc}	Gate to Collector Charge	VGE - 13V	-	12	-	nC

Electrical Characteristics of the Diode $T_{C} = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Units
V _{FM}	Diode Forward Voltage	I _F = 20A	T _C = 25 °C	-	2.2	2.7	V
* FIM	2.000 Portana Ponago	·F _0/1	T _C = 175 °C	-	1.9	-	-
E _{rec}	Reverse Recovery Energy		T _C = 175 °C	-	51	-	μJ
T _{rr}	Diode Reverse Recovery Time	I _F = 20A,	T _C = 25 ^o C	-	35	-	ns
'm L		$dI_F/dt = 200A/\mu s$	T _C = 175 °C	-	214	-	
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25 °C	-	58	-	μC
~ 11	2.000 Hororor Hororory Charge		T _C = 175 ^o C	-	776	-	





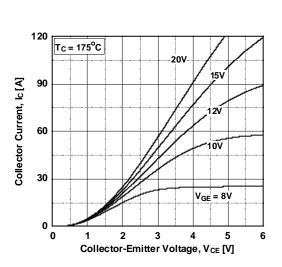


Figure 2. Typical Output Characteristics

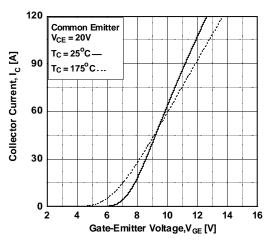
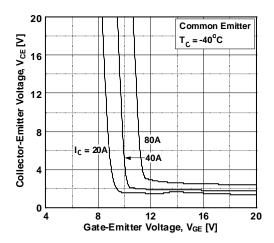
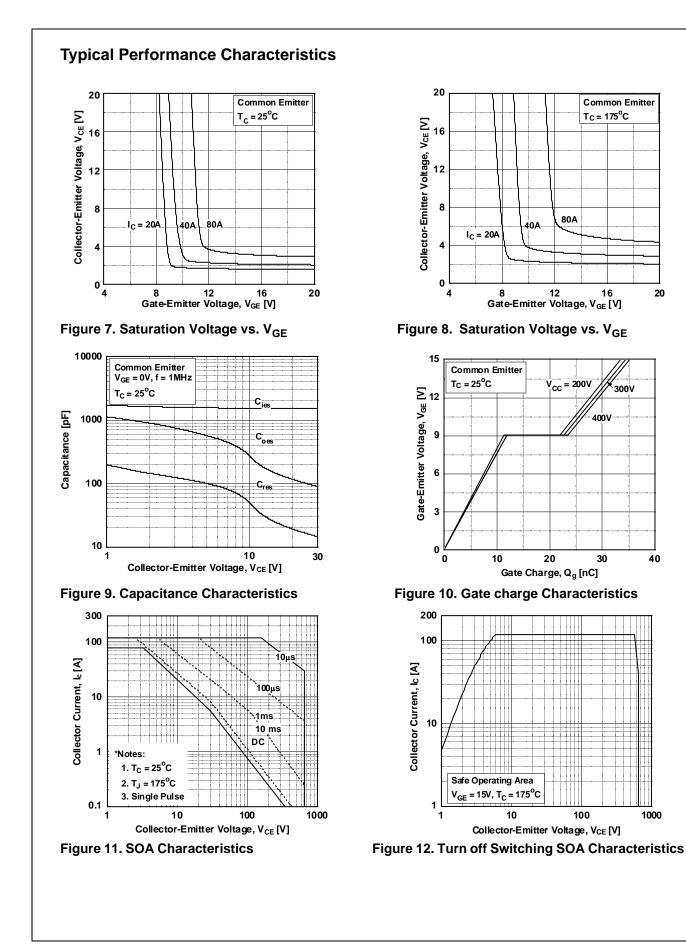


Figure 4. Transfer Characteristic





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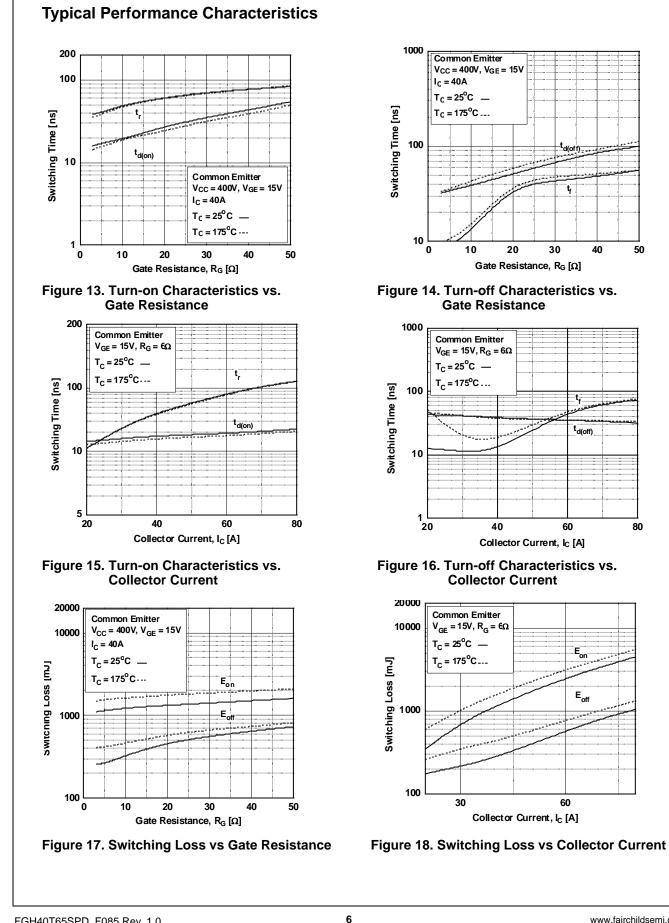


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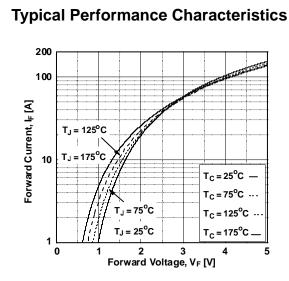
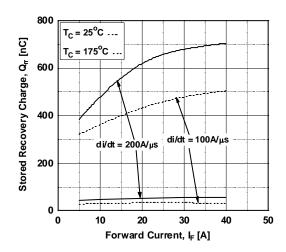
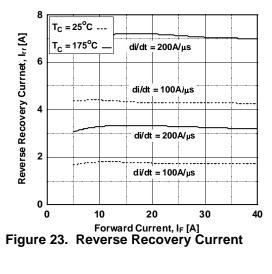


Figure 19. Forward Characteristics







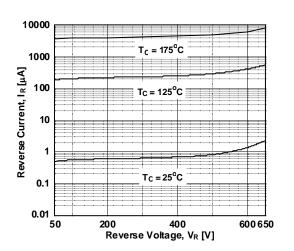


Figure 20. Reverse Current

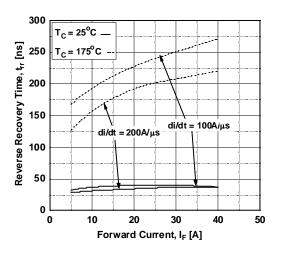
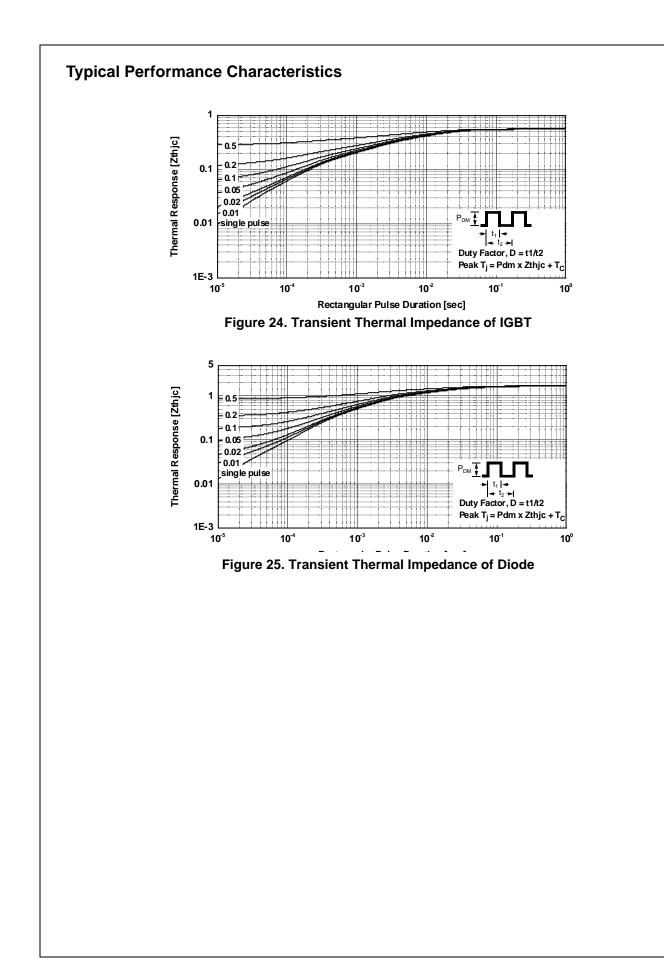
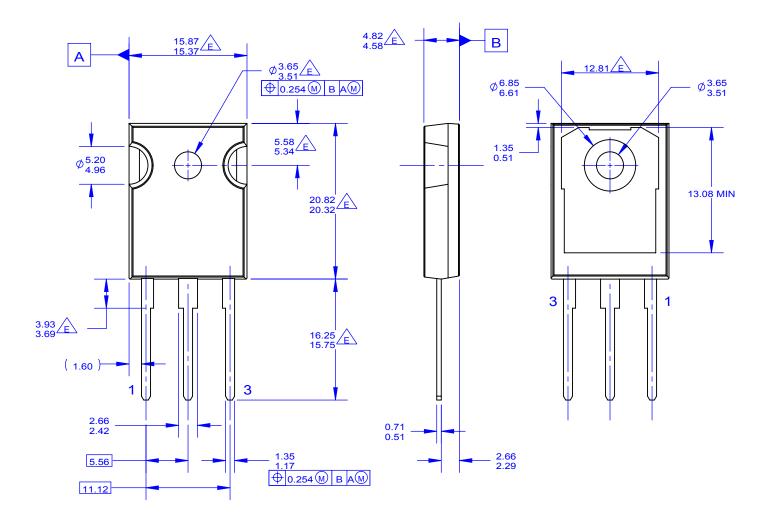


Figure 22. Reverse Recovery Time





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