

MOTOROLA

SEMICONDUCTOR
TECHNICAL DATA

ADVANCE INFORMATION

DUAL LOW POWER
OPERATIONAL AMPLIFIER

These dual operational amplifiers feature 1) low power drain, 2) a common mode input voltage range extending to ground/VEE, 3) Single Supply or Split Supply operation and 4) pin outs compatible with the popular MC1558 dual operational amplifier. The LMT358 series are equivalent to one-half of an LMT324.

These amplifiers have several distinct advantages over standard operational amplifier types in single supply applications. They can operate at supply voltages as low as 3.0 Vdts or as high as 32 Volts with quiescent currents about one-fifth of those associated with the MC1741 (on a per amplifier basis). The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications. The output voltage range also includes the negative power supply voltage.

- Short Circuit Protected Outputs
- True Differential Input Stage
- Single Supply Operation: 3.0 to 32 Volts
- Low Input Bias Currents
- Internally Compensated
- Common Mode Range Extends to Negative Supply
- Single and Split Supply Operation
- Similar Performance to the MC1558

MAXIMUM RATINGS (TA = 25°C Unless otherwise noted)

Rating	Symbol	LMT358	LMT2904	Unit
Power Supply Voltages Single Supply Split Supplies	V _{CC} V _{CC} · V _{EE}	32 ±16	26 ±13	Vdc
Input Differential Voltage Range	V _{IDR}	±32	±26	Vdc
Input Common Mode Voltage Range	V _{ICR}	0.3 to 32	-0.3 to 26	Vdc
Output Short Circuit Duration	t _S	Continuous	Continuous	Sec
Junction Temperature	T _J	150	150	°C
Storage Temperature Range	T _{STG}	-55 to +125	-55 to +125	°C
Ambient Operating Temperature Range	T _A	0 to +70	-40 to +105	°C

CAUTION: These devices do not have internal ESD protection circuitry and are rated as CLASS 1 devices per the ESD test method in Mil-Std-883D. They should be handled using standard ESD prevention methods to avoid damage to the device.

LMT358, LMT2904

DUAL
DIFFERENTIAL INPUT
OPERATIONAL AMPLIFIERS

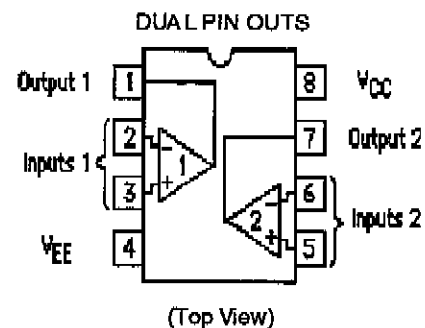
SILICON MONOLITHIC
INTEGRATED CIRCUIT



N SUFFIX
PLASTIC PACKAGE
CASE 626



D SUFFIX
PLASTIC PACKAGE
CASE 751-02
SO-8



ORDERING INFORMATION

Device	Package	Temperature Range
LMT358N LMT358D	8 Pin Plastic DIP SO-8	0°C to 70°C
LMT2904N LMT2904D	8 Pin Plastic DIP SO-8	-40°C to 105°C

Mac Rev 3.0

ELECTRICAL CHARACTERISTICS ($V_{CC} = +5.0V$, $V_{EE} = \text{Ground}$, $T_A = 25^\circ\text{C}$ unless otherwise noted)

CHARACTERISTICS	SYMBOL	LMT358			LMT2904			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage $V_{CC} = 5.0\text{ V to }30.0\text{ V}$, $V_{ICR} = 0\text{ V to }V_{CC} - 1.7\text{ V}$, $V_O = 1.4\text{ V}$, $R_S = 0\ \Omega$ $T_A = +25^\circ\text{C}$ $T_A = T_{\text{high to }T_{\text{low}}}$ (Note 1)	V_{IO}	—	2.0	7.0	—	2.0	7.0	mV
Average Temperature Coefficient of Input Offset Voltage, $T_A = T_{\text{high to }T_{\text{low}}}$	$\Delta V_{IO}/\Delta T$	—	7.0	—	—	7.0	—	$\mu\text{V}/^\circ\text{C}$
Input Offset Current $T_A = T_{\text{high to }T_{\text{low}}}$ (Note 1)	I_{IO}	—	5.0	50	—	5.0	50	nA
Average Temperature Coefficient of Input Offset Current, $T_A = T_{\text{high to }T_{\text{low}}}$	$\Delta I_{IO}/\Delta T$	—	10	—	—	10	—	$\text{pA}/^\circ\text{C}$
Input Bias Current $T_A = T_{\text{high to }T_{\text{low}}}$	I_{IB}	—	-45	-250	—	-45	-250	nA
Input Common-Mode Voltage range (Note 2) $V_{CC} = 30\text{ V}$, $T_A = +25^\circ\text{C}$ $V_{CC} = 30\text{ V}$, $T_A = T_{\text{high to }T_{\text{low}}}$	V_{ICR}	0	—	28.3	0	—	24.3	V
Differential Input Voltage Range	V_{IDR}	—	—	V_{CC}	—	—	V_{CC}	V
Large Signal Open-Loop Voltage Gain $R_L = 2.0\text{ k}\Omega$, $V_{CC} = 15\text{ V}$, For Large V_O Swing, $T_A = T_{\text{high to }T_{\text{low}}}$	A_{VOL}	25 15	100 —	— —	25 15	100 —	— —	V/mV
Channel Separation 1.0 kHz $\leq f \leq 20\text{ kHz}$, Input Referenced	CS	—	-120	—	—	-120	—	dB
Common-Mode Rejection Ratio $R_S \leq 10\text{ k}\Omega$	CMRR	65	85	—	50	85	—	dB
Power Supply Rejection Ratio	PSRR	65	100	—	50	100	—	dB
Output Voltage Range $R_L = 2.0\text{ k}\Omega$	V_{OR}	0	—	3.3	0	—	3.3	V
Output Voltage - High Limit ($T_A = T_{\text{high to }T_{\text{low}}}$) $V_{CC} = 30\text{ V}$, $R_L = 2.0\text{ k}\Omega$ $V_{CC} = 30\text{ V}$, $R_L = 10\text{ k}\Omega$	V_{OH}	26 27	— 28	— —	22 23	— 24	— —	V
Output Voltage - Low Limit $V_{CC} = 5.0\text{ V}$, $R_L = 10\text{ k}\Omega$, $T_A = T_{\text{high to }T_{\text{low}}}$	V_{OL}	—	5.0	20	—	5.0	20	mV
Output Source Current ($V_{ID} = +1.0\text{ V}$, $V_{CC} = 15\text{ V}$)	I_{O+}	20	40	—	20	40	—	mA
Output Sink Current $V_{ID} = -1.0\text{ V}$, $V_{CC} = 15\text{ V}$ $V_{ID} = -1.0\text{ V}$, $V_O = 200\text{ mV}$, $T_A = 25^\circ\text{C}$	I_{O-}	10 12	20 50	— —	10 —	20 —	— —	mA μA
Output Short Circuit to Ground (Note 3)	I_{SC}	—	40	60	—	40	60	mA
Power Supply Current ($T_A = T_{\text{high to }T_{\text{low}}}$) $V_{CC} = 30\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$ $V_{CC} = 5.0\text{ V}$, $V_O = 0\text{ V}$, $R_L = \infty$	I_{CC}	—	1.5 0.7	3.0 1.2	—	1.5 0.7	3.0 1.2	mA

- NOTES: 1. $T_{\text{low}} = 0^\circ\text{C}$, $T_{\text{high}} = +70^\circ\text{C}$ for LMT358, $T_{\text{low}} = -40^\circ\text{C}$, $T_{\text{high}} = +105^\circ\text{C}$ for LMT2904
 2. The input common-mode (cm) voltage or either input signal voltage **should not** be allowed to go **NEG** by more than 0.3 V. The upper end of the cm voltage range is $V_{CC} - 1.7V$; either or both inputs can go to $+32V$ w/o damage.
 3. Short circuits from the output to V_{CC} can cause excessive heating and eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

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