

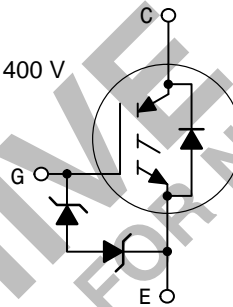
# Designer's™ Data Sheet

## Insulated Gate Bipolar Transistor with Anti-Parallel Diode

### N-Channel Enhancement-Mode Silicon Gate

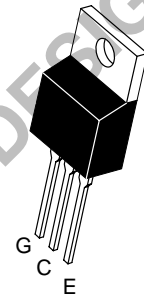
This Insulated Gate Bipolar Transistor (IGBT) is co-packaged with a soft recovery ultra-fast rectifier and uses an advanced termination scheme to provide an enhanced and reliable high voltage-blocking capability. Its new 600 V IGBT technology is specifically suited for applications requiring both a high temperature short circuit capability and a low  $V_{CE(on)}$ . It also provides fast switching characteristics and results in efficient operation at high frequencies. Co-packaged IGBTs save space, reduce assembly time and cost. This new E-series introduces an energy efficient, ESD protected, and short circuit rugged device.

- Industry Standard TO-220 Package
- High Speed:  $E_{off} = 70 \mu\text{J/A}$  typical at 125°C
- High Voltage Short Circuit Capability – 10  $\mu\text{s}$  minimum at 125°C, 400 V
- Low On-Voltage 2.0 V typical at 5.0 A, 125°C
- Soft Recovery Free Wheeling Diode is Included in the Package
- Robust High Voltage Termination
- ESD Protection Gate-Emitter Zener Diodes



**MGP7N60ED**

IGBT & DIODE IN TO-220  
7.0 A @ 90°C  
10 A @ 25°C  
600 VOLTS  
SHORT CIRCUIT RATED  
LOW ON-VOLTAGE



CASE 221A-09  
STYLE 9  
TO-220AB

#### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CES}$	600	Vdc
Collector-Gate Voltage ( $R_{GE} = 1.0 \text{ M}\Omega$ )	$V_{CGR}$	600	Vdc
Gate-Emitter Voltage — Continuous	$V_{GE}$	$\pm 20$	Vdc
Collector Current — Continuous @ $T_C = 25^\circ\text{C}$ — Continuous @ $T_C = 90^\circ\text{C}$ — Repetitive Pulsed Current (1)	$I_{C25}$ $I_{C90}$ $I_{CM}$	10 7.0 14	Adc Apk
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	$P_D$	81 0.65	Watts W/°C
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to 150	°C
Short Circuit Withstand Time ( $V_{CC} = 400 \text{ Vdc}$ , $V_{GE} = 15 \text{ Vdc}$ , $T_J = 125^\circ\text{C}$ , $R_G = 20 \Omega$ )	$t_{sc}$	10	$\mu\text{s}$
Thermal Resistance — Junction to Case – IGBT — Junction to Case – Diode — Junction to Ambient	$R_{\theta JC}$ $R_{\theta Jc}$ $R_{\theta JA}$	1.5 2.7 65	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	$T_L$	260	°C
Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.13 N•m)		

(1) Pulse width is limited by maximum junction temperature. Repetitive rating.

**Designer's Data for "Worst Case" Conditions** — The Designer's Data Sheet permits the design of most circuits entirely from the information presented. SOA Limit curves — representing boundaries on device characteristics — are given to facilitate "worst case" design. Designer's™ is a trademark of Motorola, Inc.

# MGP7N60ED

## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector-to-Emitter Breakdown Voltage (V <sub>GE</sub> = 0 Vdc, I <sub>C</sub> = 25 μAdc) Temperature Coefficient (Positive)	V <sub>(BR)CES</sub>	600 —	— 870	— —	Vdc mV/°C
Zero Gate Voltage Collector Current (V <sub>CE</sub> = 600 Vdc, V <sub>GE</sub> = 0 Vdc) (V <sub>CE</sub> = 600 Vdc, V <sub>GE</sub> = 0 Vdc, T <sub>J</sub> = 125°C)	I <sub>CES</sub>	— —	— —	10 200	μAdc
Gate-Body Leakage Current (V <sub>GE</sub> = ± 20 Vdc, V <sub>CE</sub> = 0 Vdc)	I <sub>GES</sub>	—	—	50	μAdc

### ON CHARACTERISTICS (1)

Collector-to-Emitter On-State Voltage (V <sub>GE</sub> = 15 Vdc, I <sub>C</sub> = 2.5 Adc) (V <sub>GE</sub> = 15 Vdc, I <sub>C</sub> = 2.5 Adc, T <sub>J</sub> = 125°C) (V <sub>GE</sub> = 15 Vdc, I <sub>C</sub> = 5.0 Adc)	V <sub>CE(on)</sub>	— — —	1.6 1.5 2.0	1.9 — 2.4	Vdc
Gate Threshold Voltage (V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 1.0 mAdc) Threshold Temperature Coefficient (Negative)	V <sub>GE(th)</sub>	4.0 —	6.0 10	8.0 —	Vdc mV/°C
Forward Transconductance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 5.0 Adc)	g <sub>fe</sub>	—	2.5	—	Mhos

### DYNAMIC CHARACTERISTICS

Input Capacitance	(V <sub>CE</sub> = 25 Vdc, V <sub>GE</sub> = 0 Vdc, f = 1.0 MHz)	C <sub>ies</sub>	—	610	—	pF
Output Capacitance		C <sub>oes</sub>	—	60	—	
Transfer Capacitance		C <sub>res</sub>	—	10	—	

### SWITCHING CHARACTERISTICS (1)

Turn-On Delay Time	(V <sub>CC</sub> = 360 Vdc, I <sub>C</sub> = 5.0 Adc, V <sub>GE</sub> = 15 Vdc, L = 300 μH, R <sub>G</sub> = 20 Ω) Energy losses include "tail"	t <sub>d(on)</sub>	—	22	—	ns
Rise Time		t <sub>r</sub>	—	24	—	
Turn-Off Delay Time		t <sub>d(off)</sub>	—	64	—	
Fall Time		t <sub>f</sub>	—	196	—	μJ
Turn-Off Switching Loss		E <sub>off</sub>	—	200	340	
Turn-On Switching Loss		E <sub>on</sub>	—	71	—	
Total Switching Loss		E <sub>ts</sub>	—	271	—	
Turn-On Delay Time	(V <sub>CC</sub> = 360 Vdc, I <sub>C</sub> = 5.0 Adc, V <sub>GE</sub> = 15 Vdc, L = 300 μH, R <sub>G</sub> = 20 Ω, T <sub>J</sub> = 125°C) Energy losses include "tail"	t <sub>d(on)</sub>	—	31	—	ns
Rise Time		t <sub>r</sub>	—	24	—	
Turn-Off Delay Time		t <sub>d(off)</sub>	—	195	—	
Fall Time		t <sub>f</sub>	—	220	—	μJ
Turn-Off Switching Loss		E <sub>off</sub>	—	350	—	
Turn-On Switching Loss		E <sub>on</sub>	—	135	—	
Total Switching Loss		E <sub>ts</sub>	—	485	—	
Gate Charge	(V <sub>CC</sub> = 360 Vdc, I <sub>C</sub> = 5.0 Adc, V <sub>GE</sub> = 15 Vdc)	Q <sub>T</sub>	—	27.2	—	nC
		Q <sub>1</sub>	—	7.0	—	
		Q <sub>2</sub>	—	13.7	—	

(1) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

**DIODE CHARACTERISTICS**

Diode Forward Voltage Drop ( $I_{EC} = 2.3 \text{ Adc}$ ) ( $I_{EC} = 2.3 \text{ Adc}, T_J = 125^\circ\text{C}$ ) ( $I_{EC} = 4.6 \text{ Adc}$ )		$V_{FEC}$	—	1.7	—	Vdc
			—	1.3	—	
			—	2.0	2.3	
Reverse Recovery Time	$(I_F = 4.6 \text{ Adc}, V_R = 360 \text{ Vdc}, di_F/dt = 200 \text{ A}/\mu\text{s})$	$t_{rr}$	—	40	—	ns
		$t_a$	—	17	—	
		$t_b$	—	23	—	
Reverse Recovery Stored Charge		$Q_{RR}$	—	60	—	nC
Reverse Recovery Time	$(I_F = 4.6 \text{ Adc}, V_R = 360 \text{ Vdc}, di_F/dt = 200 \text{ A}/\mu\text{s}, T_J = 125^\circ\text{C})$	$t_{rr}$	—	105	—	ns
		$t_a$	—	36	—	
		$t_b$	—	69	—	
Reverse Recovery Stored Charge		$Q_{RR}$	—	247	—	nC

**INTERNAL PACKAGE INDUCTANCE**

Internal Emitter Inductance (Measured from the emitter lead 0.25" from package to emitter bond pad)	$L_E$	—	7.5	—	nH
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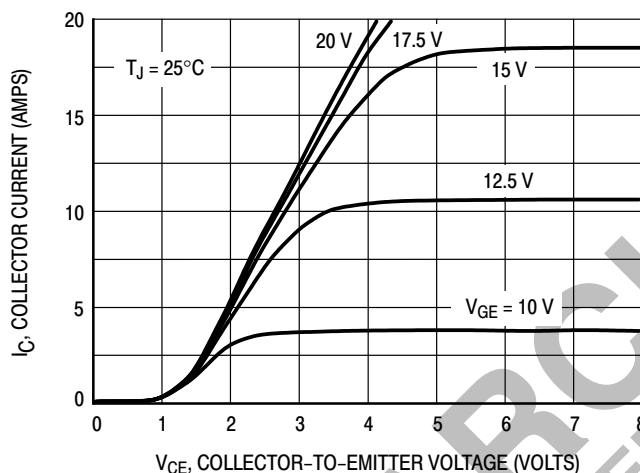


Figure 1. Output Characteristics

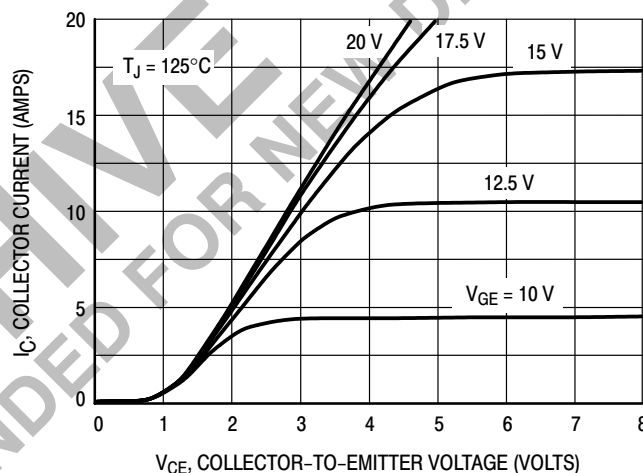


Figure 2. Output Characteristics

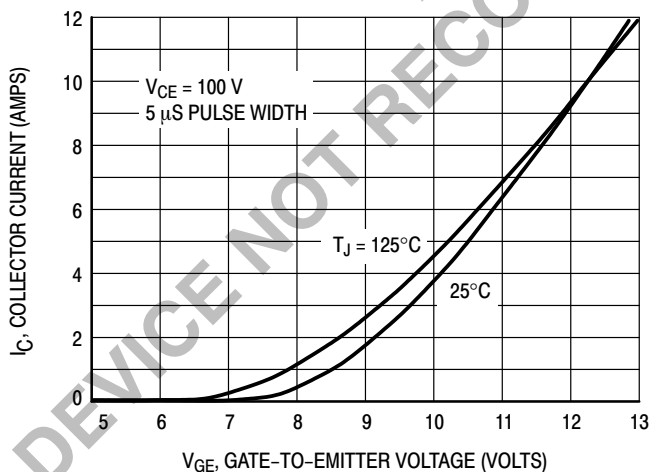


Figure 3. Transfer Characteristics

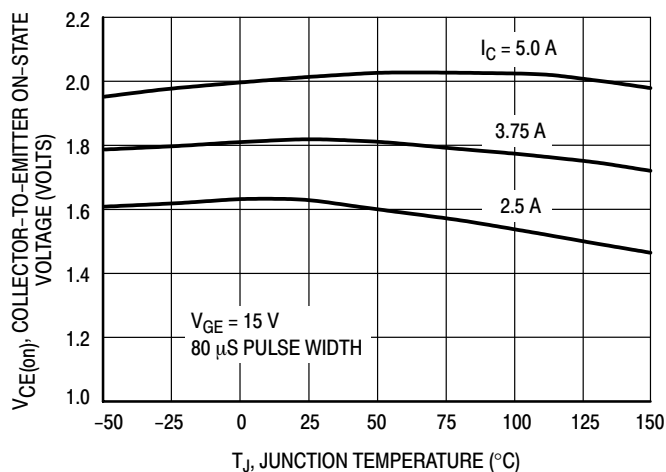
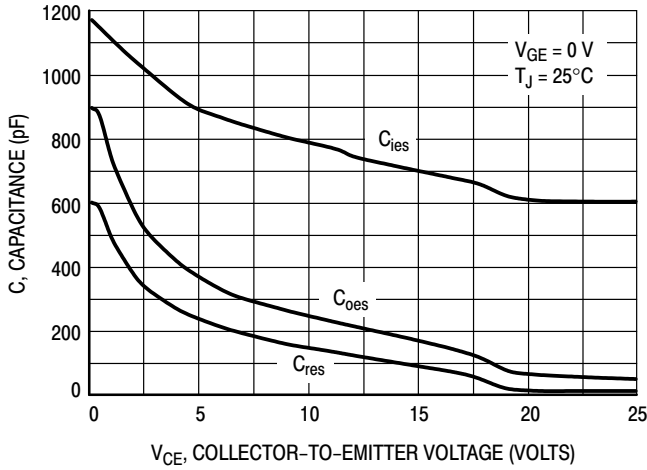
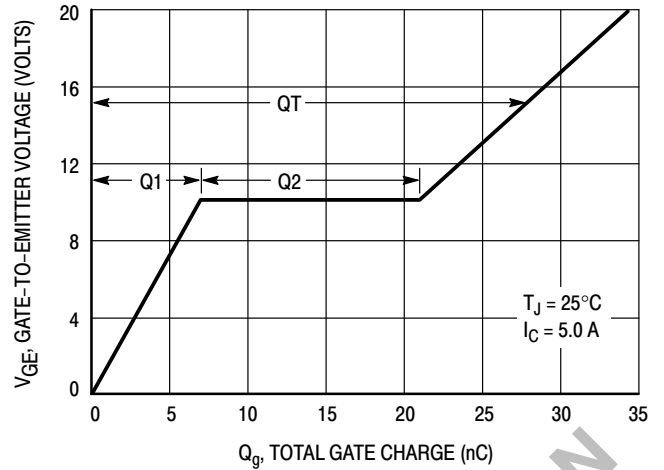


Figure 4. Collector-To-Emitter Saturation Voltage versus Junction Temperature

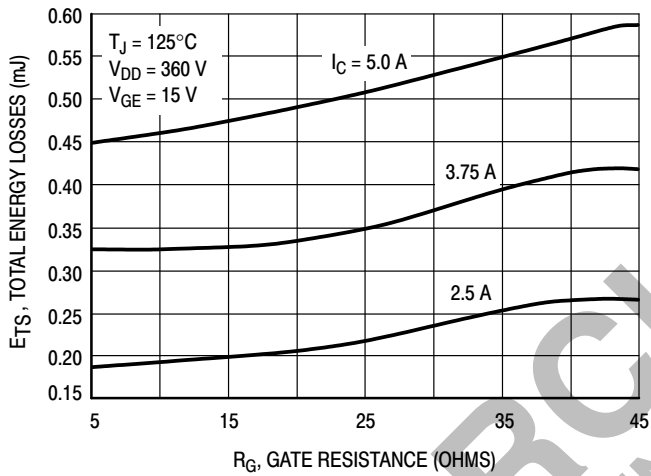
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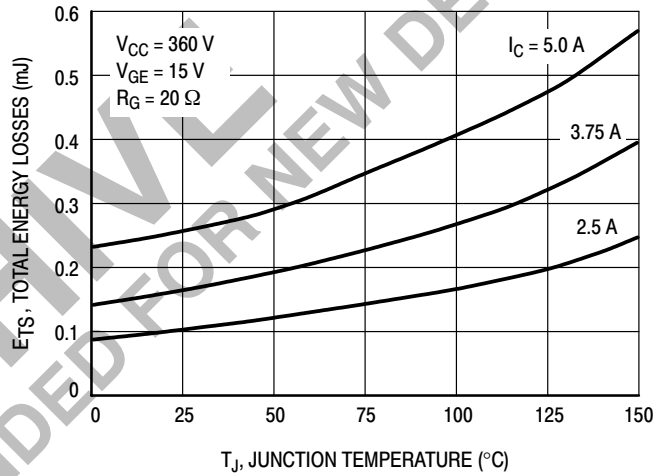
**Figure 5. Capacitance Variation**



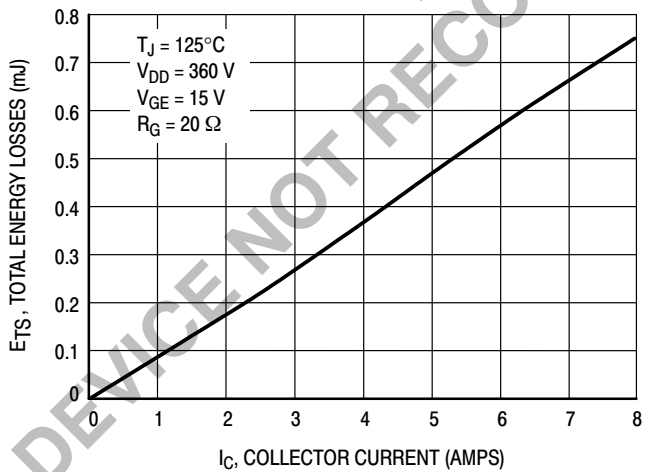
**Figure 6. Gate-To-Emitter Voltage versus Total Charge**



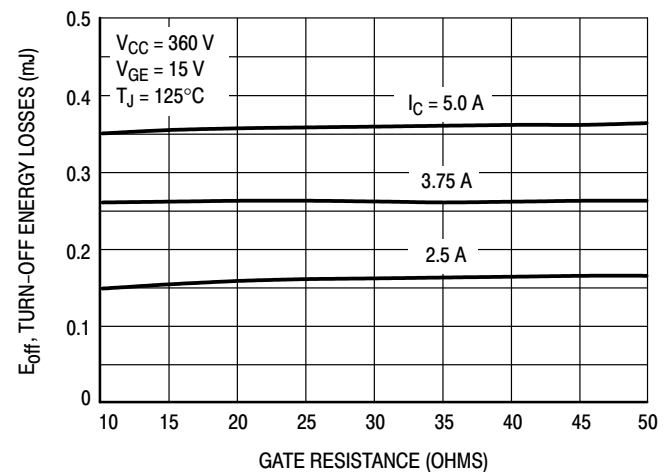
**Figure 7. Total Energy Losses versus Gate Resistance**



**Figure 8. Total Energy Losses versus Junction Temperature**



**Figure 9. Total Energy Losses versus Collector Current**



**Figure 10. Turn-Off Losses versus Gate Resistance**

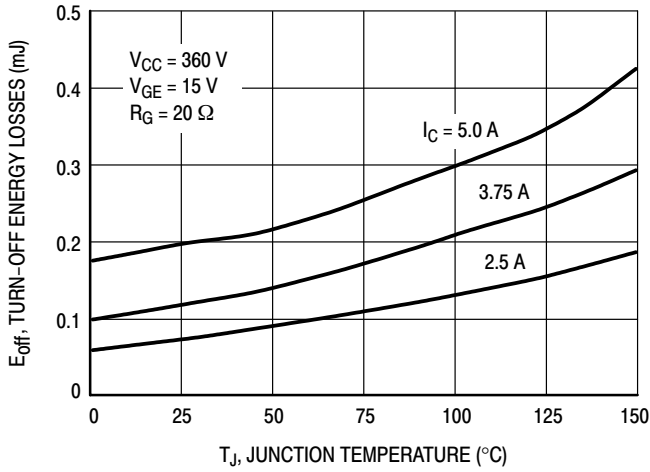


Figure 11. Turn-Off Losses versus Junction Temperature

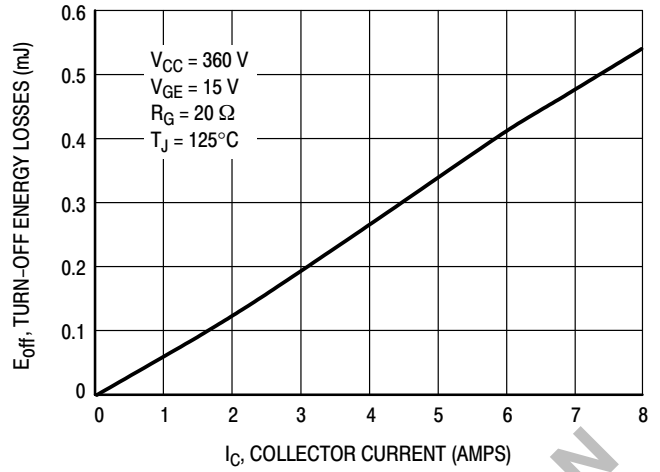


Figure 12. Turn-Off Losses versus Collector Current

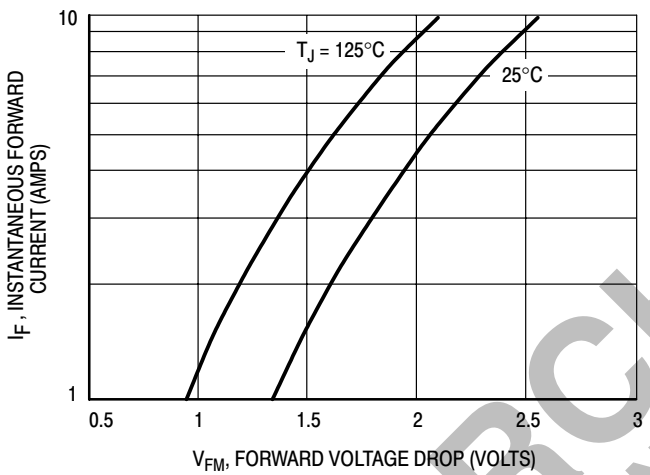


Figure 13. Forward Characteristics versus Current

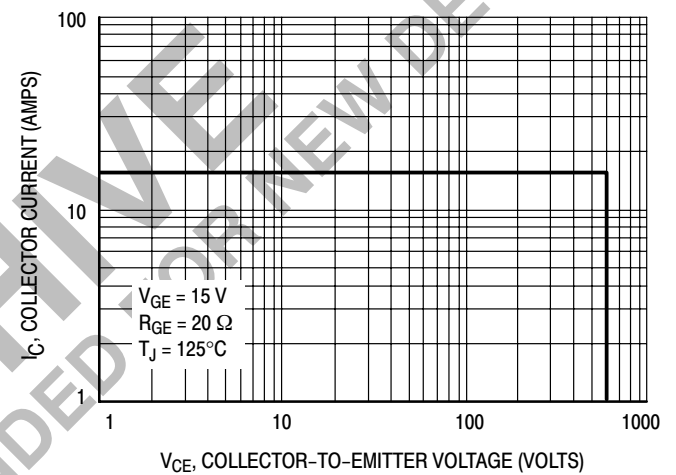
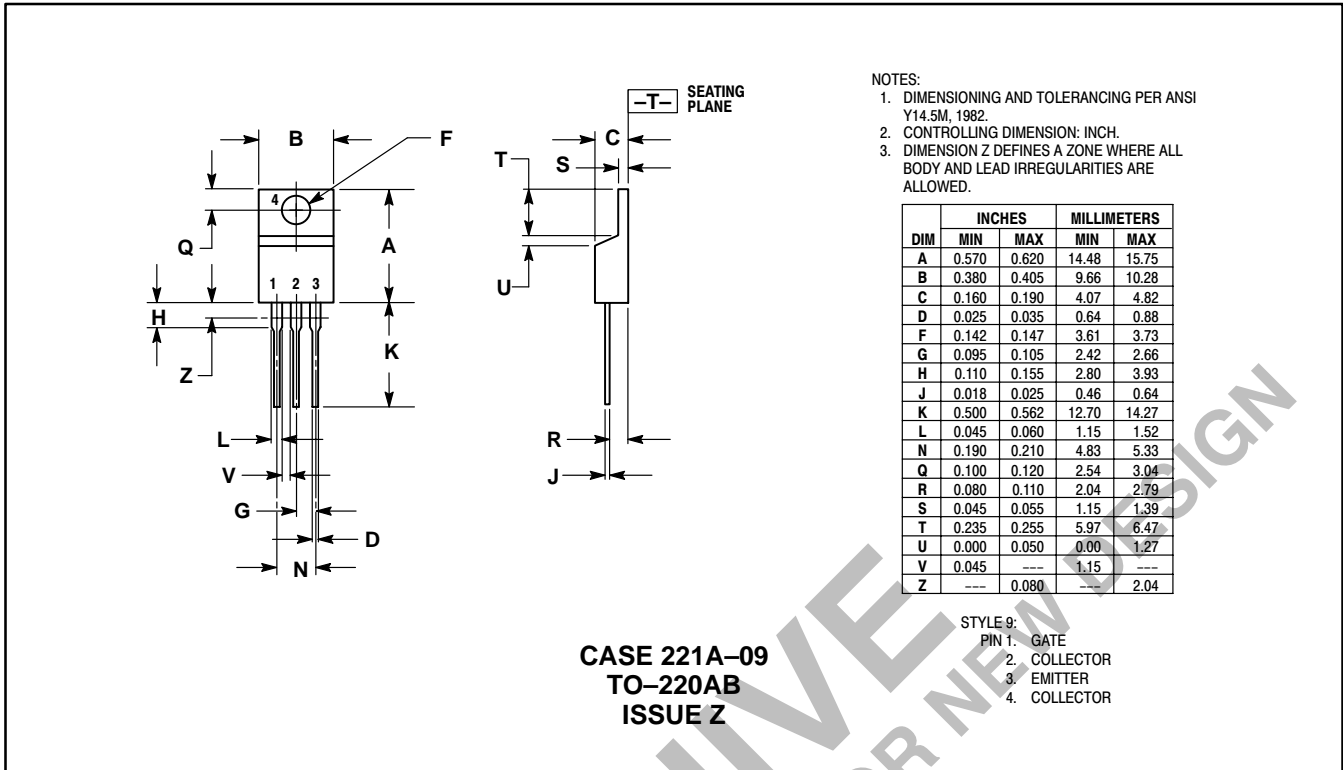


Figure 14. Reverse Biased Safe Operating Area

PACKAGE DIMENSIONS



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