

Description

The SSM3J332R uses advanced trench technology

to provide excellent R_{DS(ON)}, low gate charge and

operation with gate voltages as low as 2.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

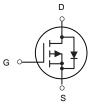


General Features

 $V_{DS} = -30V I_{D} = -4.2A$

 $R_{DS(ON)} < 54 \text{m}\Omega$ @ $V_{GS}=10V$

 $R_{DS(ON)} < 77 m\Omega @ V_{GS} = 4.5 V$



Application

Battery protection

Load switch

Uninterruptible power supply

P-Channel MOSFET

Package Marking and Ordering Information

| Product ID | Pack | Marking | Qty(PCS) |
|------------|----------|---------|----------|
| SSM3J332R | SOT23-3L | X1KX | 3000 |

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

| Symbol | Parameter | Limit | Unit |
|-----------------|--|------------|---------------|
| V _{DS} | Drain-Source Voltage | -30 | V |
| Vgs | Gate-Source Voltage | ±12 | V |
| I _D | Drain Current-Continuous | -4.2 | А |
| Ідм | Drain Current-Pulsed (Note 1) | -30 | А |
| P _D | Maximum Power Dissipation | 1.2 | W |
| TJ,TSTG | Operating Junction and Storage Temperature Range | -55 To 150 | ${\mathbb C}$ |
| Reja | Thermal Resistance,Junction-to-Ambient (Note 2) | 104 | °C/W |



Electrical Characteristics (T_A=25°C unless otherwise noted)

| Electrical Sharacteristics (TA 20 | o arriogo oti | 101 11100 110104) | | | | |
|------------------------------------|---------------------|--|------|-----|------|----|
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =-24V,V _{GS} =0V | - | - | -1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±10V,V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} ,I _D =-250μA | -0.7 | -1 | -1.3 | V |
| | | V _{GS} =-10V, I _D =-4.2A | - | 46 | 54 | mΩ |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =-4.5V, I _D =-4A | - | 58 | 77 | mΩ |
| | | V _{GS} =-2.5V, I _D =-1A | | 74 | 130 | mΩ |
| Forward Transconductance | g Fs | V _{DS} =-5V,I _D =-4.2A | - | 10 | - | S |
| Dynamic Characteristics (Note4) | | | | | | |
| Input Capacitance | C _{lss} | V _{DS} =-15V,V _{GS} =0V, F=1.0MHz | - | 880 | _ | PF |
| Output Capacitance | C _{oss} | | - | 105 | - | PF |
| Reverse Transfer Capacitance | C _{rss} | F-1.UIVITZ | - | 65 | _ | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | | - | 7 | _ | nS |
| Turn-on Rise Time | t _r | V _{DD} =-15V,I _D =-4.2A | - | 3 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | V_{GS} =-10V, R_{GEN} =6 Ω | - | 30 | - | nS |
| Turn-Off Fall Time | t _f | | - | 12 | - | nS |
| Total Gate Charge | Qg | | - | 8.5 | - | nC |
| Gate-Source Charge | Q _{gs} | V _{DS} =-15V,I _D =-4.2A,V _{GS} =-4.5V | - | 1.8 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 2.7 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V _{SD} | V _{GS} =0V,I _S =-4.2A | - | - | -1.2 | V |
| | | | | | | |

Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

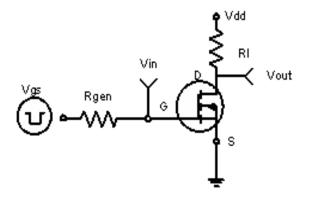
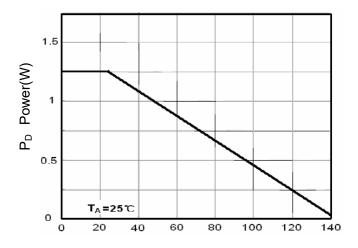
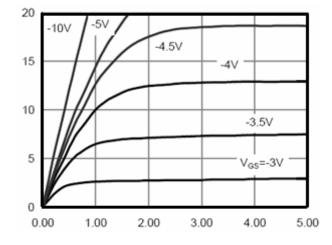


Figure 1:Switching Test Circuit



 T_J -Junction Temperature(${}^{\circ}$ C) Figure 3 Power Dissipation



Ip- Drain Current (A)

Vds Drain-Source Voltage (V) Figure 5 Output Characteristics

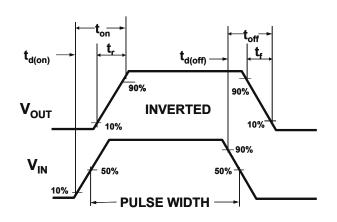


Figure 2:Switching Waveforms

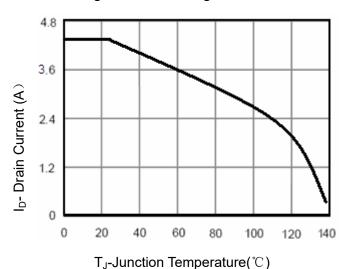


Figure 4 Drain Current

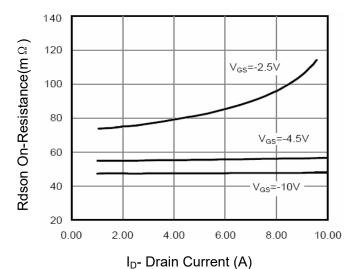
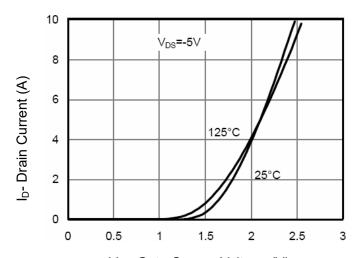
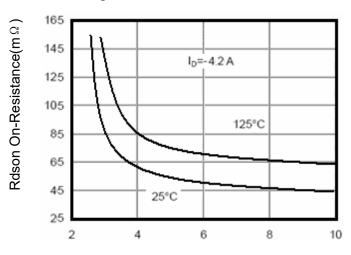


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V) Figure 9 Rdson vs Vgs

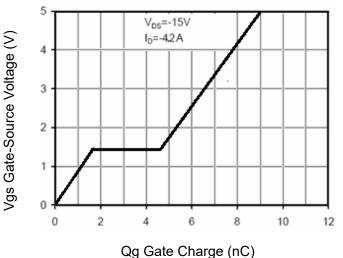
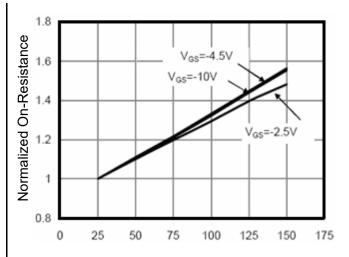
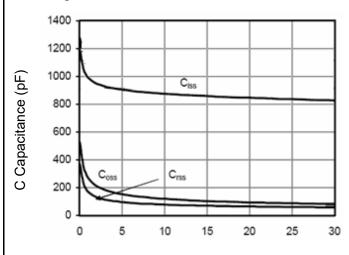


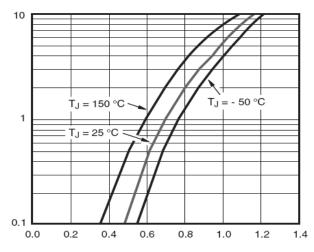
Figure 11 Gate Charge



 T_J -Junction Temperature(${}^{\circ}$ C) Figure 8 Drain-Source On-Resistance

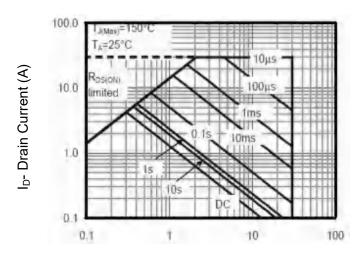


Vds Drain-Source Voltage (V)
Figure 10 Capacitance vs Vds



l_s- Reverse Drain Current (A)

Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 13 Safe Operation Area

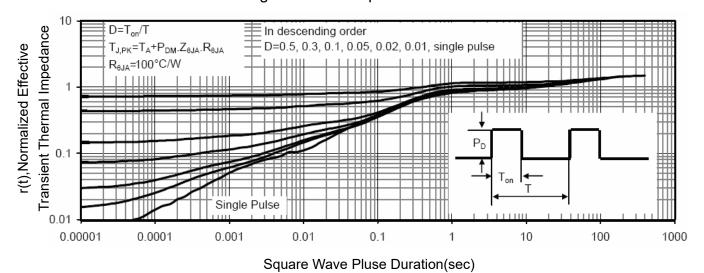
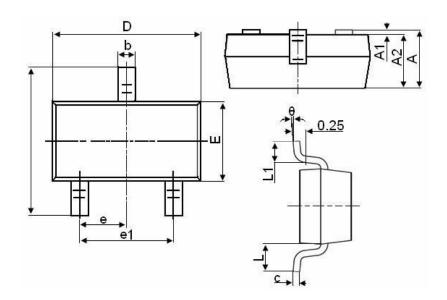


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT23-3L Package Information



| Symbol | Dimensions in Millimeters | | | |
|--------|---------------------------|----------|--|--|
| | MIN. | MAX. | | |
| А | 1.050 | 1.250 | | |
| A1 | 0.000 | 0.100 | | |
| A2 | 1.050 | 1.150 | | |
| b | 0.300 | 0.500 | | |
| С | 0.100 | 0.200 | | |
| D | 2.800 | 3.000 | | |
| E | 1.500 | 1.700 | | |
| E1 | 2.650 | 2.950 | | |
| е | | 0.950TYP | | |
| e1 | 1.800 | 2.000 | | |
| L | 0.550REF | | | |
| L1 | 0.300 | 0.600 | | |
| θ | 0° | 8° | | |



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