

Description

The 2N60L can be used in various power swithching circuit for system miniaturization and higher efficiency. The package form is TO-252-2L, which accords with the RoHS standard.

General Features

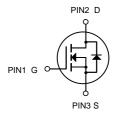
 $V_{DS} = 650 \text{ V}, I_{D} = 2\text{A}$ $R_{DS(ON)} < 5 \Omega \text{@ } V_{GS} = 10\text{V}$



TO252-2L

Application

• Power switch circuit of adaptor and charger.



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
2N60L	TO252-2L	2N65 XXX YYYY	2500

Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	650	V
VGS	Gate-Source Voltage	<u>+</u> 30	V
I _D @T _C =25°C	Drain Current, V _{GS} @ 4.5V	2	Α
ID@Tc=100°C	Drain Current, V _{GS} @ 4.5V	1.3	А
IDM	Pulsed Drain Current ¹	8	А
P _D @T _C =25°C	Total Power Dissipation	35	W
Eas	Single Pulse Avalanche Energy ⁴	50	mJ
TSTG	Storage Temperature Range	-45 to 125	°C
TJ	Operating Junction Temperature Range	-45 to 125	°C



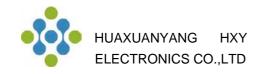
Electrical Characteristics (Tc= 25°C unless otherwise specified):

OFF Characteristics						
Symbol	Demonstrati	Test Conditions	Rating			Unit
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	S
$V_{ m DSS}$	Drain to Source Breakdown Voltage	V_{GS} =0V, I_{D} =250 μ A	650			V
Δ BV _{DSS} / Δ T _J	Bvdss Temperature Coefficient	ID=250uA,Reference25℃		0.7		V/℃
T	D G. J. I. G	$V_{DS} = 650 \text{V}, V_{GS} = 0 \text{V},$ $T_a = 25 ^{\circ}\text{C}$			1	μА
I_{DSS}	Drain to Source Leakage Current	$V_{DS} = 520 \text{V}, V_{GS} = 0 \text{V},$ $T_a = 125 ^{\circ}\text{C}$			100	μΑ
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS} = +30V$			100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS} = -30V$			-100	nA

ON Characteristics						
Cymbol	Parameter	Test Conditions		Rating		
Symbol	Farameter	Test Conditions	Min.	Тур.	Max.	Units
R _{DS(ON)}	Drain-to-Source On-Resistance	$V_{GS}=10V,I_{D}=1A$		4.2	5	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0		4.0	V
Pulse width $tp \le 300 \mu s$, $\delta \le 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions		Rating		
Symbol	raiametei	Test Conditions	Min.	Тур.	Max.	Units
g_{fs}	Forward Transconductance	$V_{DS}=15V, I_{D}=1A$		1.8		S
C _{iss}	Input Capacitance			335		
C_{oss}	Output Capacitance	$V_{GS} = 0V V_{DS} = 25V$ f = 1.0MHz		33		pF
C_{rss}	Reverse Transfer Capacitance			3		

Resistive Switching Characteristics						
G1 1	Parameter	Test Conditions		Rating		
Symbol	Farameter	Test Conditions	Min.	Тур.	Max.	Units
$t_{d(ON)}$	Turn-on Delay Time			11		
tr	Rise Time	$I_D = 2A$ $V_{DD} = 325V$		13		na
$t_{d(OFF)}$	Turn-Off Delay Time	$R_G = 10\Omega$		29		ns
$t_{\rm f}$	Fall Time			12		
Q_g	Total Gate Charge			9.5		
Q_{gs}	Gate to Source Charge	$I_D = 2A$ $V_{DD} = 520V$ $V_{GS} = 10V$		1.5		nC
Q_{gd}	Gate to Drain ("Miller")Charge			4.9		



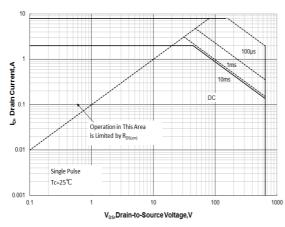
Source-Drain Diode Characteristics							
Cymbol	Parameter	Test Conditions	Rating			T I:4.	
Symbol	ratameter	Test Conditions	Min.	Тур.	Max.	Units	
I_S	Continuous Source Current (Body Diode)				2	A	
I_{SM}	Maximum Pulsed Current (Body Diode)				8	A	
V_{SD}	Diode Forward Voltage	I _S =2.0A,V _{GS} =0V			1.5	V	
trr	Reverse Recovery Time	$I_{S}=2.0A, T_{i}=25$ °C		187		ns	
Qrr	Reverse Recovery Charge	$dI_F/dt=100A/us$,		610		nC	
I_{RRM}	Reverse Recovery Current	$V_{GS}=0V$		6.6		A	
Pulse width $tp \le 300 \mu s$, $\delta \le 2\%$							

Symbol	Parameter	Max.	Units
R _f JC	Junction-to-Case	3.57	°C/W
R o JA	Junction-to-Ambient	100	°C/W

 $^{^{}a1}$: Repetitive rating; pulse width limited by maximum junction temperature a2 : L=10mH, I_D =3.1A, Start T_J =25°C a3 : I_{SD} =2A,di/dt $\leqslant 100 A/us, V_{DD} \leqslant BV_{DS,}$ Start T_J =25°C



Characteristics Curve:



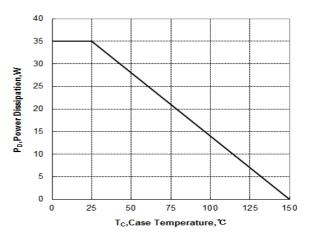
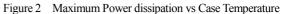
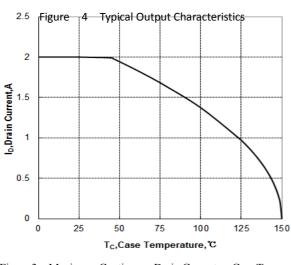


Figure 1 Maximum Forward Bias Safe Operating Area





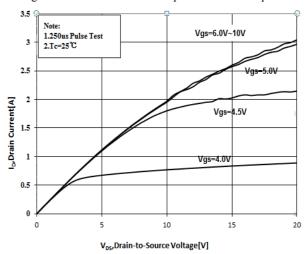


Figure 3 Maximum Continuous Drain Current vs Case Temperature

Figure 4 Typical Output Characteristics

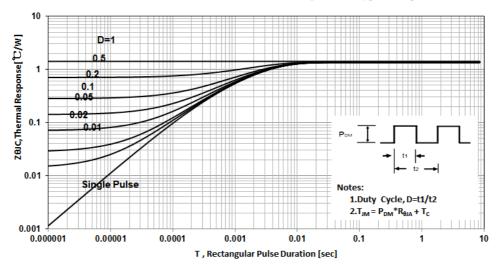


Figure 5 Maximum Effective Thermal Impedance, Junction to Case

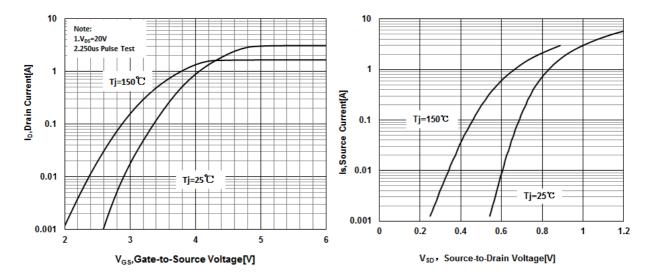


Figure 6 Typical Transfer Characteristics

Figure 7 Typical Body Diode Transfer Characteristics

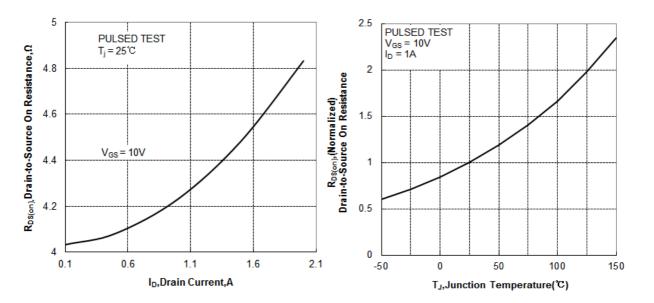


Figure 8 Typical Drain to Source ON Resistance vs Drain Current

Figure 9 Typical Drian to Source on Resistance vs Junction Temperature

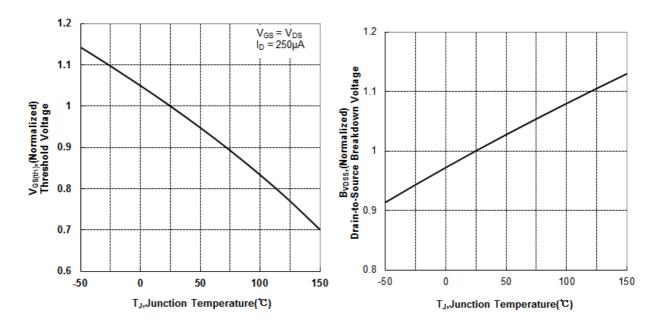


Figure 10 Typical Theshold Voltage vs Junction Temperature

Figure 11 Typical Breakdown Voltage vs Junction Temperature

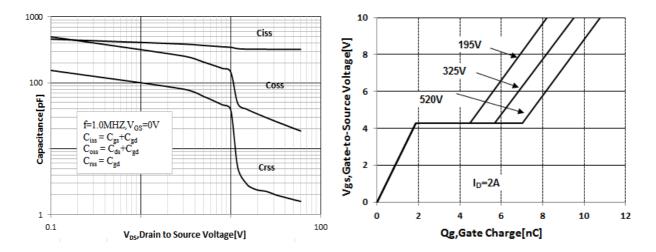


Figure 12 Typical Capacitance vs Drain to Source Voltage

Figure 13 Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform

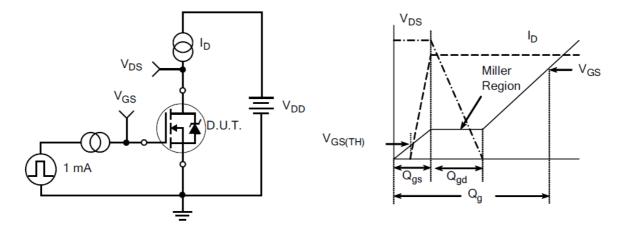


Figure 14. Gate Charge Test Circuit

Figure 15. Gate Charge Waveforms

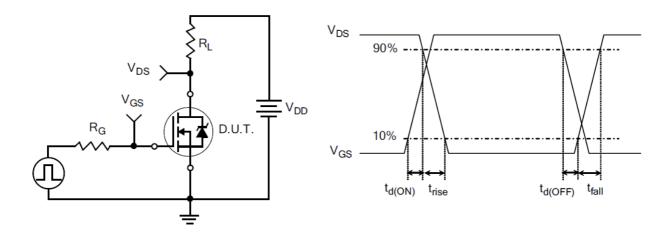


Figure 16. Resistive Switching Test Circuit

Figure 17. Resistive Switching Waveforms



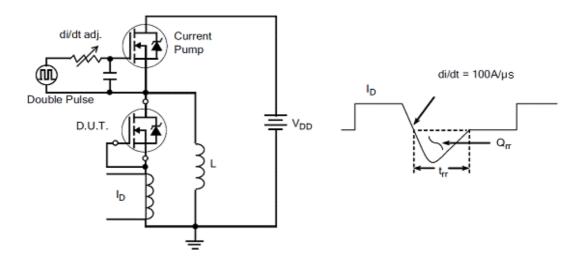


Figure 18. Diode Reverse Recovery Test Circuit

Figure 19. Diode Reverse Recovery Waveform

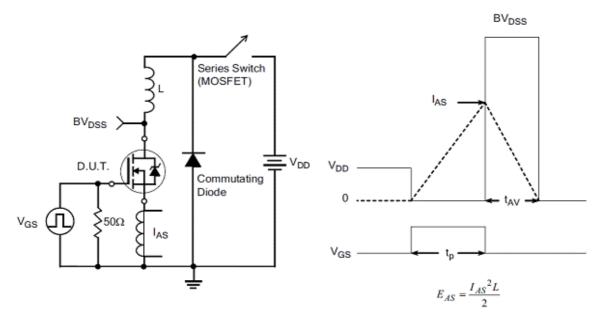
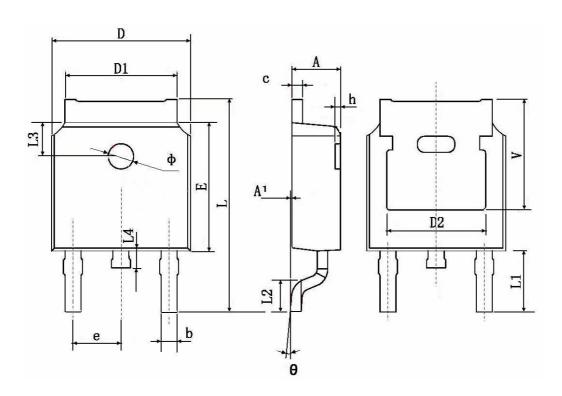


Figure 20. Unclamped Inductive Switching Test Circuit

Figure 21. Unclamped Inductive Switching Waveform

TO252-2L Package Information



O. wash ad	Dimensions	In Millimeters	Dimension	s In Inches	
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.48	3 TYP.	0.190	TYP.	
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114	TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.35	0 TYP.	0.211 TYP.		



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