

Description

The IRLR8726TRLPBF uses advanced trench technology

to provide excellent $R_{\text{DS}(\text{ON})},$ low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} = 30V I_D =100 A

 $R_{DS(ON)} < 5m\Omega @ V_{GS}=10V$

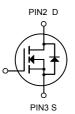
Application

Battery protection

Load switch Uninterruptible power supply



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N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
IRLR8726TRLPBF	TO252-2L	100N03DXXX YYYY	2500

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating 30 ±20 100 57		Units
VDS	Drain- Source Voltage			V
VGS	Gate-Source Voltage			V
I₀@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V ¹			А
I₀@Tc=100°C	Continuous Drain Current, V _{GS} @ 10V ¹			А
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	27	17	Α
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	23	14.5	Α
Ідм	Pulsed Drain Current ²	160 115.2 48 53		Α
EAS	Single Pulse Avalanche Energy ³			mJ
las	Avalanche Current			Α
P _D @T _C =25°C	Total Power Dissipation ⁴			W
P _D @T _A =25°C	Total Power Dissipation ⁴	6	2.4	W
Тѕтс	Storage Temperature Range	-55 to 175 -55 to 175 62 25 2.8		°C
TJ	Operating Junction Temperature Range			°C
R _θ ja	Thermal Resistance Junction-ambient (Steady State) ¹			°C/M
R _{θJA}	Thermal Resistance Junction-Ambient ¹ (t ≤10s)			°C/V
R _θ JC	Thermal Resistance Junction-Case ¹			°C/M

IRLR8726TRLPBF

N-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA				V	
∆BVbss/∆Tj	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA		0.028		V/°C	
Dravau		V _{GS} =10V , I _D =30A		3.8	5.5		
.Rds(on)	Static Drain-Source On- Resistance ²	V _{GS} =4.5V , I _D =15A		7.5	9	mΩ	
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.5	2.5	V	
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			-6.16		mV/°C	
lpss	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1	uA	
1055	Drain-Source Leakage Guirent	V _{DS} =24V,V _{GS} =0V, TJ=55℃			5		
lgss	Gate-Source Leakage Current	$V_{GS}=\pm20V$, $V_{DS}=0V$			±100	nA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =30A		22		S	
Rg	Gate Resistance V _{DS} =0V , V _{GS} = f=1MHz			1.7	3.4	Ω	
Qg	Total Gate Charge (4.5V)			20		nC	
Qgs	Gate-Source Charge	V _{DS} =15V , V _{GS} =4.5V , In=15A		7.6			
Q _{gd}	Gate-Drain Charge			7.2			
Td(on)	Turn-On Delay Time			7.8		ns	
Tr	Rise Time	V _{DD} =15V , V _{GS} =10V ,		15			
Td(off)	Turn-Off Delay Time	-R _G =3.3		37.3			
T _f	Fall Time	_I _D =15A		10.6			
C _{iss}	Input Capacitance			2295			
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V ,		267		pF	
Crss	Reverse Transfer Capacitance	_f=1MHz		210			
ls	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force			80	Α	
lsм	Pulsed Source Current ^{2,5}	Current			160	Α	
Vsd	Diode Forward Voltage ²	V _{GS} =0V , Is=1A , TJ=25°C			1	V	
trr	Reverse Recovery Time	IF=30A , dl/dt=100A/µs ,		14		nS	
Qrr	Q _{rr} Reverse Recovery Charge T _J =25°C			5		nC	

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2. The data tested by pulsed , pulse width . The EAS data shows $\ensuremath{\mathsf{Max}}\xspace$. rating .

3.The test cond \leq 300us , duty cycle ition is V_DD=25 \leq V,V 2%GS =10V,L=0.1mH,I_AS=53.8A

4.The power dissipation is limited by 175°C junction temperature

5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.



IRLR8726TRLPBF N-Channel Enhancement Mode MOSFET

Typical Characteristics

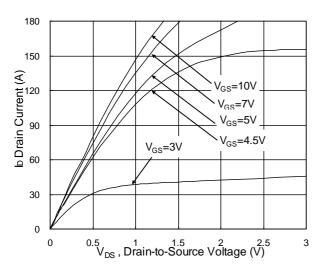


Fig.1 Typical Output Characteristics

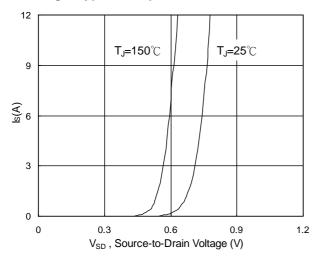


Fig.3 Forward Characteristics of Reverse

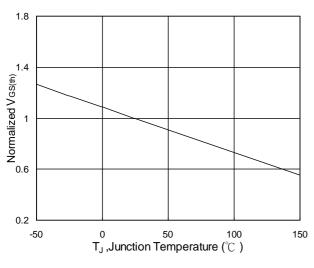


Fig.5 Normalized $V_{\text{GS(th)}}\,vs.\,T_{\text{J}}$

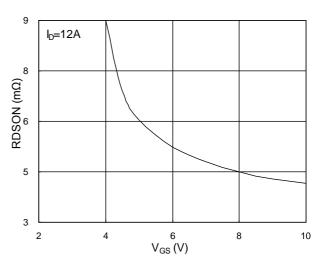


Fig.2 On-Resistance vs. G-S Voltage

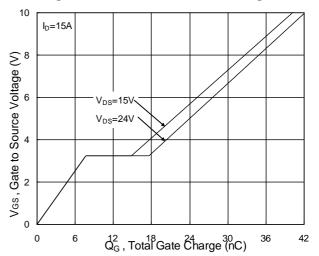


Fig.4 Gate-Charge Characteristics

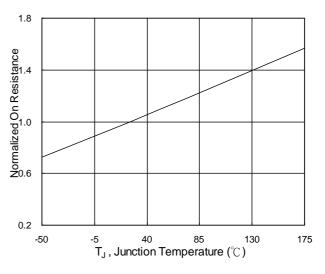


Fig.6 Normalized R_{DSON} vs. T_{J}



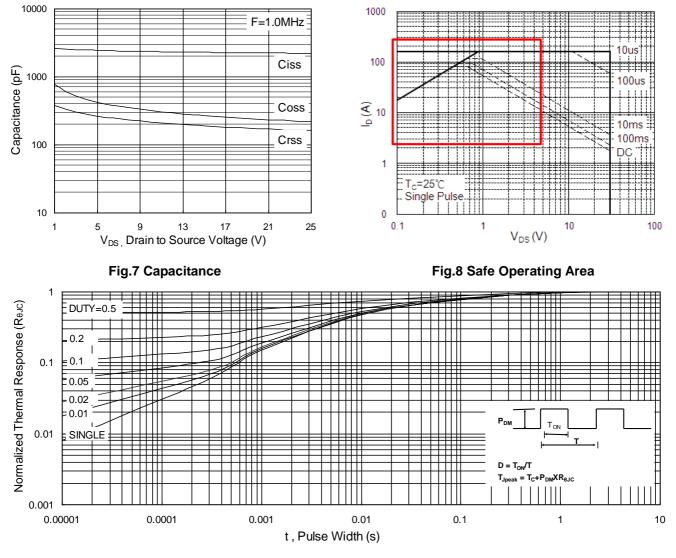


Fig.9 Normalized Maximum Transient Thermal Impedance

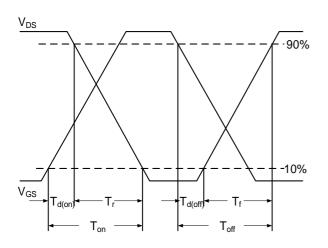


Fig.10 Switching Time Waveform

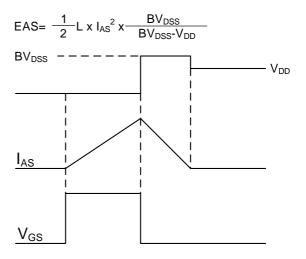
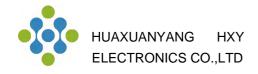
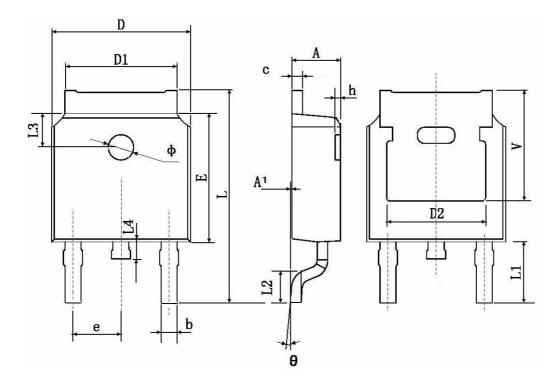


Fig.11 Unclamped Inductive Switching Waveform



TO252-2L Package Information



Ourseland	Dimensions In Millimeters		Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.483 TYP.		0.190 TYP.			
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900 TYP.		0.114 TYP.			
L2	1.400	1.700	0.055	0.067		
L3	1.600 TYP.		0.063 TYP.			
L4	0.600	1.000	0.024	0.039		
Φ	1.100	1.300	0.043	0.051		
θ	0°	8°	0 °	8°		
h	0.000	0.300	0.000	0.012		
V	V 5.350 TYP.			0.211 TYP.		



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