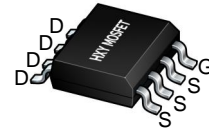




## Description

The AO4421 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



SOP-8

## General Features

$V_{DS} = -60V$   $I_D = -6A$

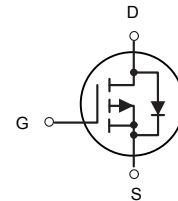
$R_{DS(ON)} < 63m\Omega @ V_{GS}=10V$

## Application

Battery protection

Load switch

Uninterruptible power supply



P-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AO4421	SOP-8	4421 XXXX	3000

## Absolute Maximum Ratings (Tc=25°C unless otherwise noted )

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	+20	V
$I_D @ T_A=25^\circ C$	Drain Current <sup>3</sup> , $V_{GS} @ 10V$	-6	A
$I_D @ T_A=70^\circ C$	Drain Current <sup>3</sup> , $V_{GS} @ 10V$	-4.8	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-40	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	3.1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	40	°C/W



**Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μ A, V <sub>GS</sub> =0V	-60			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V			-1	uA	
		V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			-5		
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250 μ A	-1		-3	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6.2A		53	63	mΩ	
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-6.2A T <sub>J</sub> =125°C		75	87		
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A		55			
On state drain current	I <sub>D(ON)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-40			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-6.2A		18		S	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-30V, f=1MHz		2417	2900	pF	
Output Capacitance	C <sub>oss</sub>			179			
Reverse Transfer Capacitance	C <sub>rss</sub>			120			
Gate resistance	R <sub>g</sub>		V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		1.9		2.3
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-30V, I <sub>D</sub> =-6.2A		46.5	55	nC	
Total Gate Charge (4.5V)				22.7			
Gate Source Charge			Q <sub>gs</sub>		9.1		
Gate Drain Charge			Q <sub>gd</sub>		9.2		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-30V, R <sub>L</sub> =4.7Ω, R <sub>GEN</sub> =3Ω		9.8		ns	
Turn-On Rise Time	t <sub>r</sub>			6.1			
Turn-Off DelayTime	t <sub>d(off)</sub>			44			
Turn-Off Fall Time	t <sub>f</sub>			12.7			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-6.2A, di/dt=100A/us		34	42	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			47			
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-4.2	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1	V	

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.



### Typical Characteristics

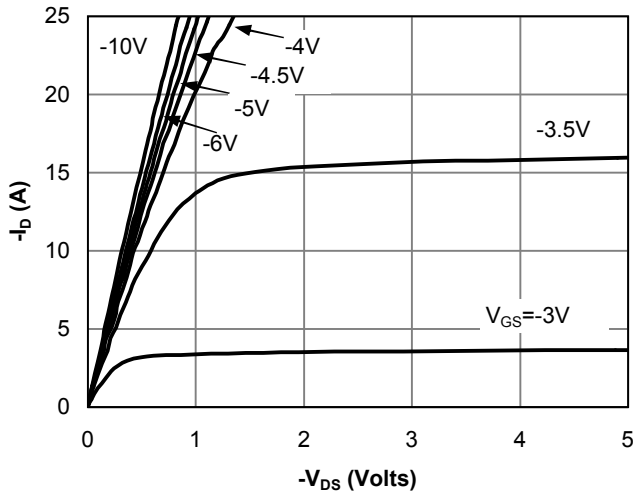


Fig 1: On-Region Characteristics

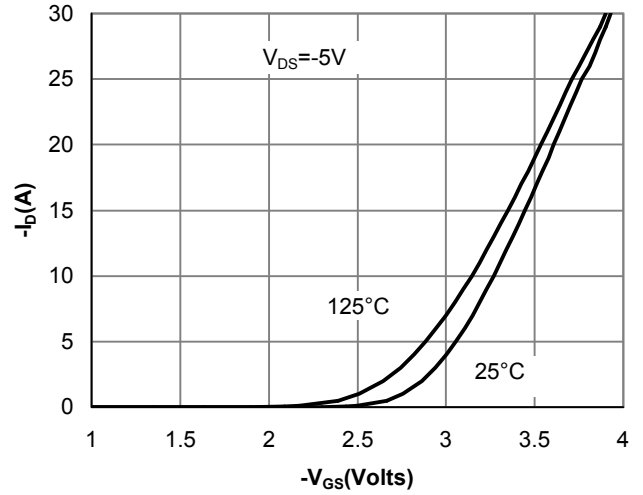


Figure 2: Transfer Characteristics

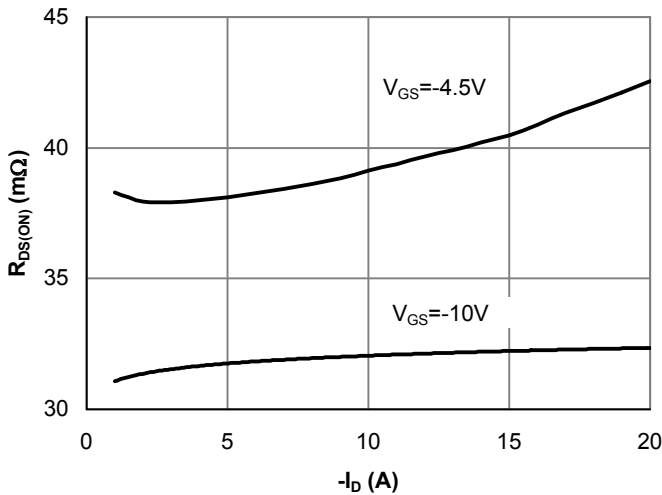


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

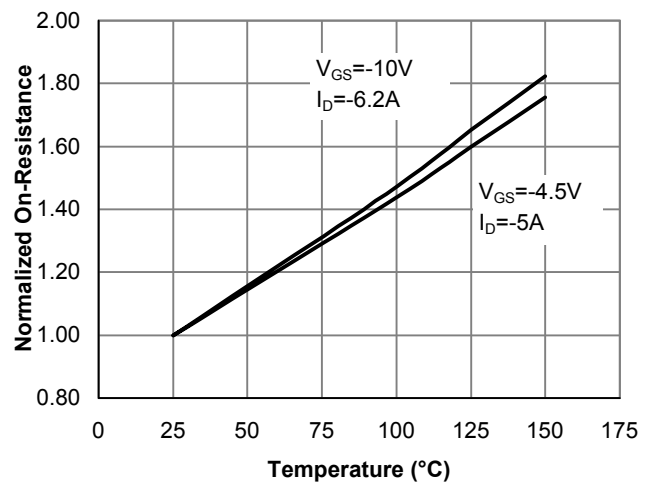


Figure 4: On-Resistance vs. Junction Temperature

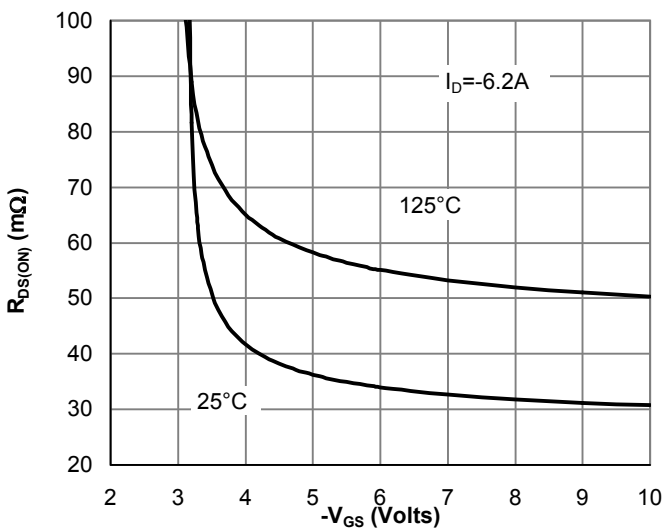


Figure 5: On-Resistance vs. Gate-Source Voltage

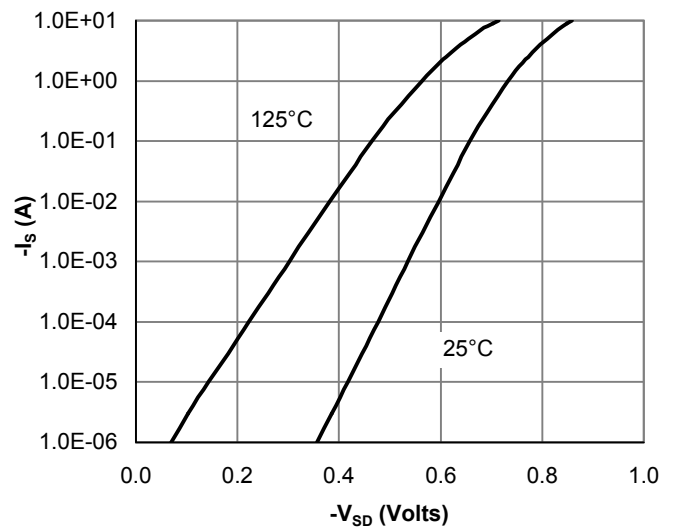


Figure 6: Body-Diode Characteristics



### Typical Characteristics

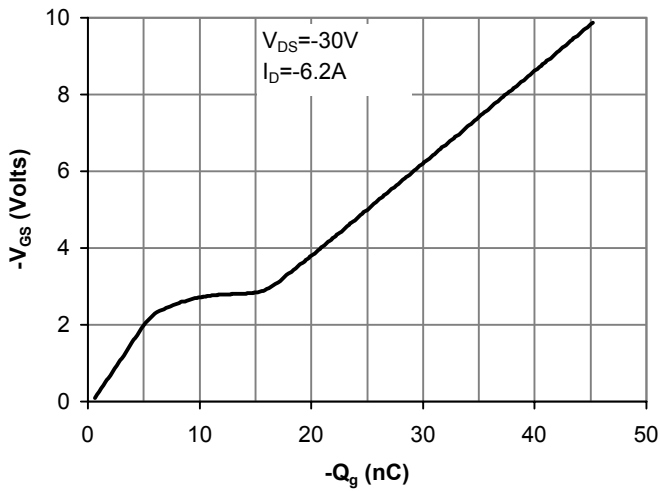


Figure 7: Gate-Charge Characteristics

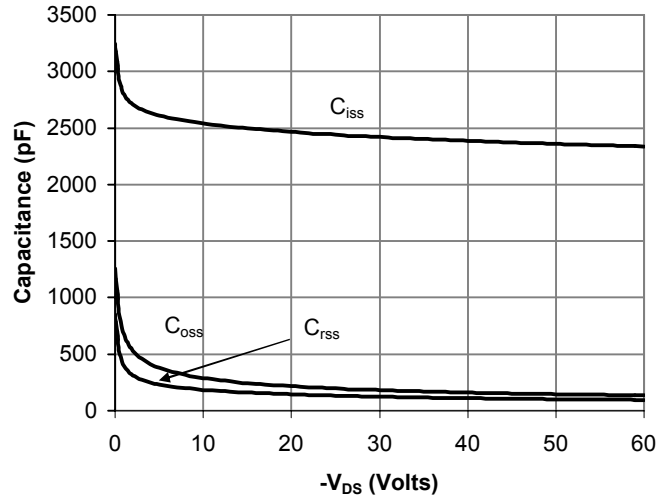


Figure 8: Capacitance Characteristics

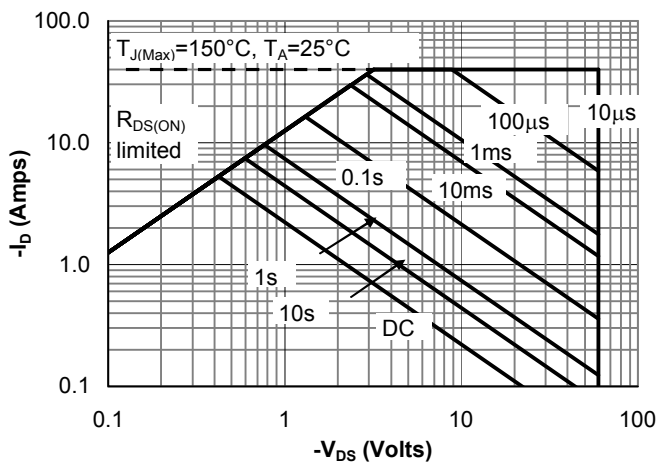


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

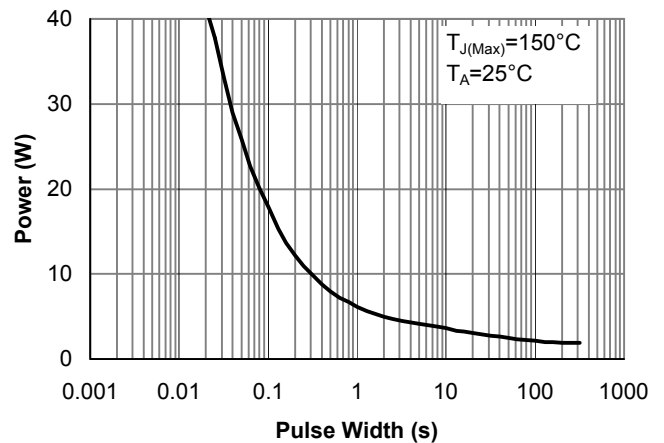


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

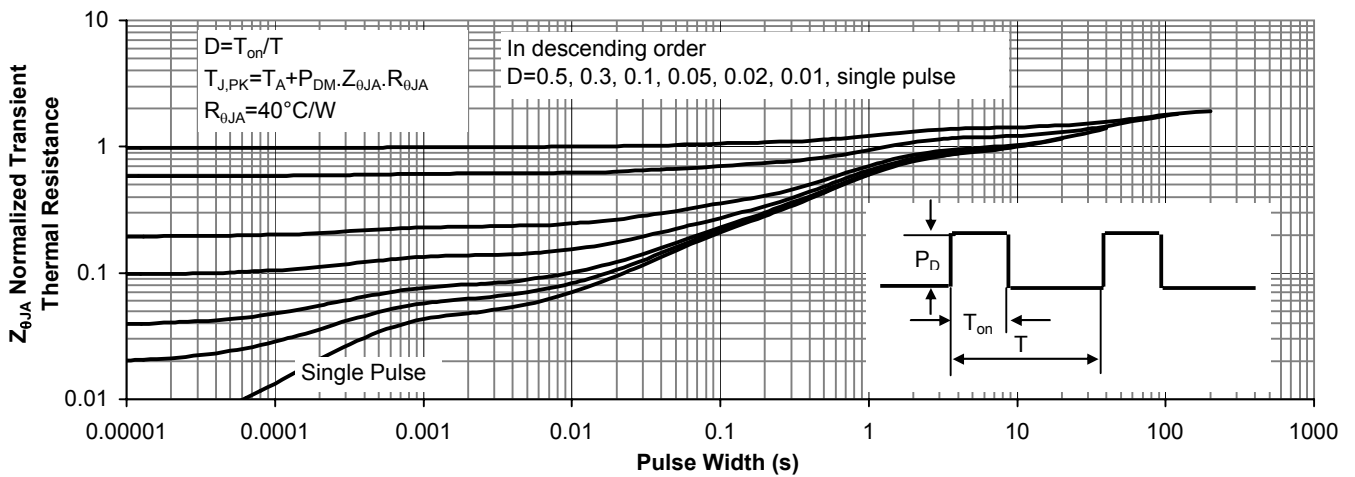
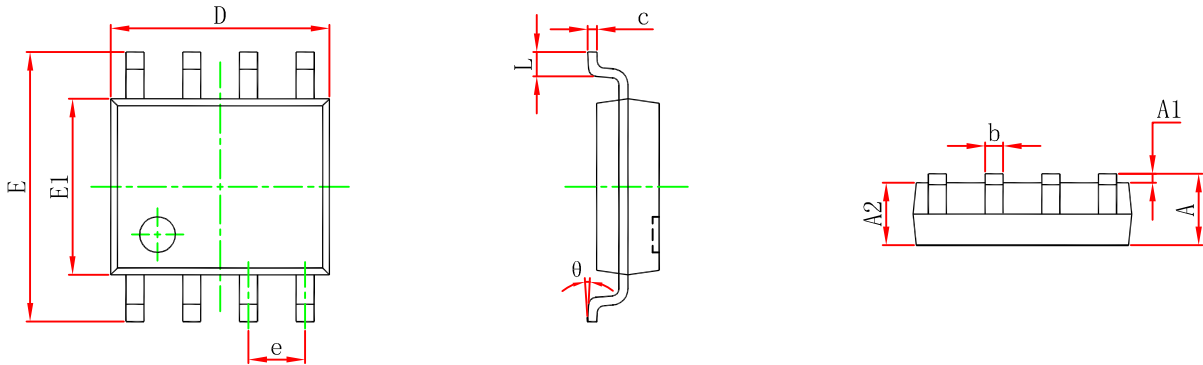


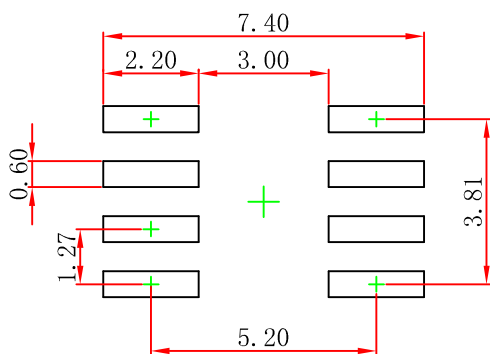
Figure 11: Normalized Maximum Transient Thermal Impedance



### SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



- Note:
1. Controlling dimension: in millimeters.
  2. General tolerance:  $\pm 0.05\text{mm}$ .
  3. The pad layout is for reference purposes only.



### Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.