

## 3-Electrode arrester

Features	Applications				
Extremely small size	Splitter				
Extremely fast response time	PCI Cards				
Excellent SMD handing	Moden				
Stable performance over life	Line cards				
<ul> <li>Very low capacitance</li> </ul>					
High insulation resistance					
RoHS-compatible					
<ul> <li>UL-identification,</li> </ul>					

**Electrical specifications** 

Part Number	DC spark-over Voltage	Max. Impulse Breakdown Voltage	Discharge Current (8/20us)	AC discharge Current	Impulse Life (10/1000us)	Minimum Insulation Resistance		Max. Capacitance 1MHz
	100V/S	1KV/us	10 times	50Hz,1S	100A			
	%	V	KA	Α	Times	Test Voltage DC(V)	(GΩ)	(Pf)

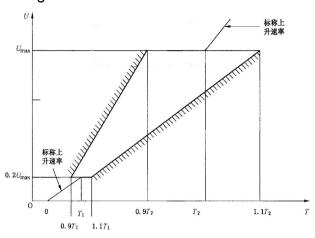
	.20		_	_	100			
3SPC090F	±30	600	5	5	100	50	1	1

Glow Voltage at 10mA~60V
Arc Voltage at 1A~10V
Glow to Arc transition Current~0.3A
Weight~0.88g
Operation and storage temperature40~90°C
Climatic category (IEC 60068-1)
Surface treatment



### 3-Electrode arrester

## DC breakdown voltage



## 8/20us, Test wave

T1=1.25T=8us±20%

T2=20us±20%

# 10/700us, Test Wave

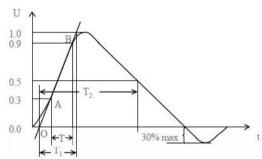
T1=1.67T=10us±20%

T2=700us±20%

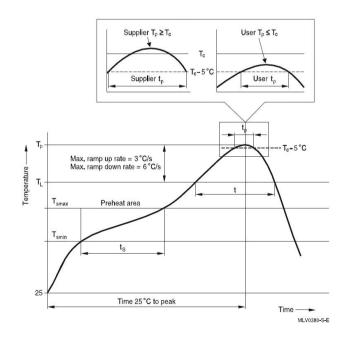
### 10/1000us, Test Wave

T1=1.67T=10us±20%

T2=1000us±20%



# Recommended wave soldering profile



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	T <sub>smin</sub> T <sub>smax</sub> t <sub>smin</sub> to t <sub>smax</sub>	100 °C 150 °C 60 120 s	150 °C 200 °C 60 180 s
Average ramp-up rate	T <sub>smax</sub> to T <sub>p</sub>	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T <sub>L</sub>	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T <sub>p</sub> , T <sub>C</sub>	220 235 °C **	245 260 °C **
Time (t <sub>p</sub> ) ** within 5 °C of the specified classification temperature (T <sub>C</sub> )		20 s ***	30 s ***
Average ramp-down rate	T <sub>p</sub> to T <sub>smax</sub>	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

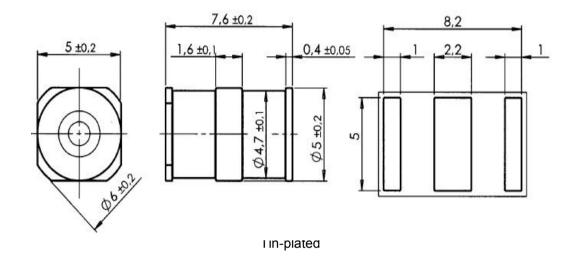
- Tolerance for peak profile temperature  $(\mathsf{T}_\mathsf{p})$  is defined as a supplier minimum and a user maximum.
- \*\* = For details please refer to JEDEC J-STD-020D.
- \*\*\* = Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.





- 1) Sampling size in accordance to AQL(C=0)
- 2) DC spark-over voltage ±30% after load
- 3) Tests according to ITU-T Rec. K. 12 and IEC61643-1

#### **Dimensions**



Part Number Code

<b>*</b>	3	R	1 5 0		В	
		$\downarrow$		$\downarrow$		
JuX ing	3R s	eries		DC Voltage	B:5*7.6	

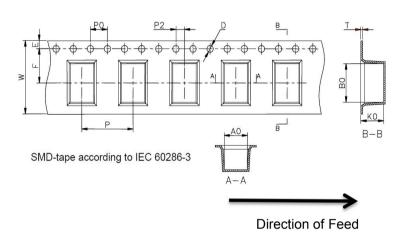


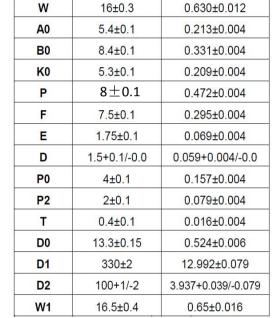
Inches



## Packaging

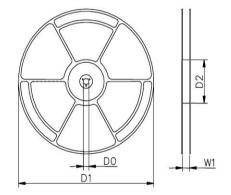
#### 1000PCS/reel





Millimeters

Symbol



# Cautions and warnings

- Surge arresters must not be operated directly in power supply networks
- Surge arresters may become hot in case of longer periods of current stress (danger of burning).
- If the contacts of the surge arrester are defective, current stress can lead to the formation of sparks and loud noises.
- Surge arresters may be used only within their specified values. In case of overload, the head contacts may fail or the component may be destroyed.
- Damaged surge arresters must not be re-used.