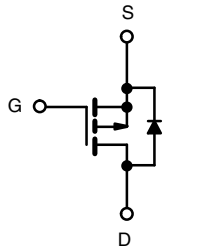


## P-Channel 60 V (D-S) 175 °C MOSFET

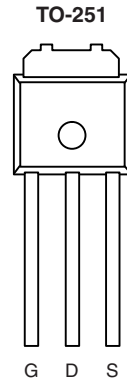
| PRODUCT SUMMARY                                 |        |
|---|--------|
| $V_{DS}$ (V)                                    | - 60   |
| $R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = - 10$ V  | 0.0135 |
| $R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = - 4.5$ V | 0.017  |
| $I_D$ (A)                                       | - 50   |
| Configuration                                   | Single |

### FEATURES

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_g$  and UIS Tested



P-Channel MOSFET



Drain Connected to Drain-Tab

Top View

| ABSOLUTE MAXIMUM RATINGS ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted) |                |                                   |                  |    |
|---|----------------|-----------------------------------|------------------|----|
| PARAMETER   | SYMBOL         | LIMIT                             | UNIT             |    |
| Drain-Source Voltage  | $V_{DS}$       | - 60                              | V                |    |
| Gate-Source Voltage   | $V_{GS}$       | $\pm 20$                          |                  |    |
| Continuous Drain Current <sup>a</sup>   | $I_D$          | $T_C = 25\text{ }^\circ\text{C}$  | - 50             |    |
|   |                | $T_C = 125\text{ }^\circ\text{C}$ | - 38             |    |
| Continuous Source Current (Diode Conduction) <sup>a</sup>                             | $I_S$          | - 50                              | A                |    |
| Pulsed Drain Current <sup>b</sup>   | $I_{DM}$       | - 200                             |                  |    |
| Single Pulse Avalanche Current  | $I_{AS}$       | - 52                              |                  |    |
| Single Pulse Avalanche Energy   | $E_{AS}$       | L = 0.1 mH                        | 135              | mJ |
| Maximum Power Dissipation <sup>b</sup>  |                |                                   | $P_D$            |    |
|   |                | $T_C = 125\text{ }^\circ\text{C}$ |                  | 45 |
| Operating Junction and Storage Temperature Range                                      | $T_J, T_{stg}$ | - 55 to + 175                     | $^\circ\text{C}$ |    |

| THERMAL RESISTANCE RATINGS |            |       |                    |
|----------------------------|------------|-------|--------------------|
| PARAMETER                  | SYMBOL     | LIMIT | UNIT               |
| Junction-to-Ambient        | $R_{thJA}$ | 50    | $^\circ\text{C/W}$ |
| Junction-to-Case (Drain)   |            |       |                    |

### Notes

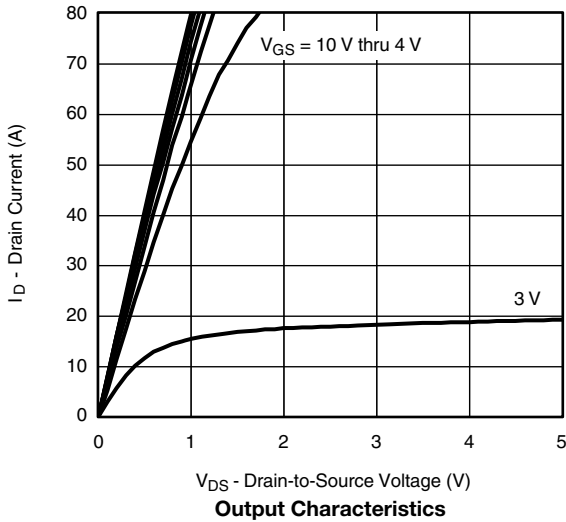
- Package limited.
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- When mounted on 1" square PCB (FR-4 material).

| <b>SPECIFICATIONS</b> ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted) |              |   |  |      |        |           |               |
|--|--------------|---|--|------|--------|-----------|---------------|
| PARAMETER  | SYMBOL       | TEST CONDITIONS   |  | MIN. | TYP.   | MAX.      | UNIT          |
| <b>Static</b>  |              |   |  |      |        |           |               |
| Drain-Source Breakdown Voltage   | $V_{DS}$     | $V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$  |  | -60  | -      | -         | V             |
| Gate-Source Threshold Voltage  | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$  |  | -1.5 | -      | -2.5      |               |
| Gate-Source Leakage  | $I_{GSS}$    | $V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$   |  | -    | -      | $\pm 100$ | nA            |
| Zero Gate Voltage Drain Current  | $I_{DSS}$    | $V_{GS} = 0\text{ V}$   | $V_{DS} = -60\text{ V}$                                  | -    | -      | -1        | $\mu\text{A}$ |
|  |              | $V_{GS} = 0\text{ V}$   | $V_{DS} = -60\text{ V}, T_J = 125\text{ }^\circ\text{C}$ | -    | -      | -50       |               |
|  |              | $V_{GS} = 0\text{ V}$   | $V_{DS} = -60\text{ V}, T_J = 175\text{ }^\circ\text{C}$ | -    | -      | -150      |               |
| On-State Drain Current <sup>a</sup>  | $I_{D(on)}$  | $V_{GS} = -10\text{ V}$   | $V_{DS} \geq -5\text{ V}$                                | -50  | -      | -         | A             |
| Drain-Source On-State Resistance <sup>a</sup>                                      | $R_{DS(on)}$ | $V_{GS} = -10\text{ V}$   | $I_D = -17\text{ A}$                                     | -    | 0.0135 | -         | $\Omega$      |
|  |              | $V_{GS} = -10\text{ V}$   | $I_D = -50\text{ A}, T_J = 125\text{ }^\circ\text{C}$    | -    | 0.026  | -         |               |
|  |              | $V_{GS} = -10\text{ V}$   | $I_D = -50\text{ A}, T_J = 175\text{ }^\circ\text{C}$    | -    | 0.032  | -         |               |
|  |              | $V_{GS} = -4.5\text{ V}$  | $I_D = -14\text{ A}$                                     | -    | 0.017  | -         |               |
| Forward Transconductance <sup>a</sup>  | $g_{fs}$     | $V_{DS} = -15\text{ V}, I_D = -17\text{ A}$   |  | -    | 50     | -         | S             |
| <b>Dynamic<sup>b</sup></b>   |              |   |  |      |        |           |               |
| Input Capacitance  | $C_{iss}$    | $V_{GS} = 0\text{ V}$   | $V_{DS} = -25\text{ V}, f = 1\text{ MHz}$                | -    | 4730   | 5910      | $\text{pF}$   |
| Output Capacitance   | $C_{oss}$    |   |  | -    | 485    | 606       |               |
| Reverse Transfer Capacitance   | $C_{rss}$    |   |  | -    | 330    | 410       |               |
| Total Gate Charge <sup>c</sup>   | $Q_g$        | $V_{GS} = -10\text{ V}$   | $V_{DS} = -30\text{ V}, I_D = -50\text{ A}$              | -    | 98     | 150       | nC            |
| Gate-Source Charge <sup>c</sup>  | $Q_{gs}$     |   |  | -    | 15     | 23        |               |
| Gate-Drain Charge <sup>c</sup>   | $Q_{gd}$     |   |  | -    | 21     | 32        |               |
| Gate Resistance  | $R_g$        | f = 1 MHz   |  | 1.47 | 2.9    | 4.42      | $\Omega$      |
| Turn-On Delay Time <sup>c</sup>  | $t_{d(on)}$  | $V_{DD} = -30\text{ V}, R_L = 0.6\text{ }\Omega$<br>$I_D \cong -50\text{ A}, V_{GEN} = -10\text{ V}, R_g = 6.0\text{ }\Omega$ |  | -    | 15     | 18        | ns            |
| Rise Time <sup>c</sup>   | $t_r$        |   |  | -    | 12     | 16        |               |
| Turn-Off Delay Time <sup>c</sup>   | $t_{d(off)}$ |   |  | -    | 112    | 125       |               |
| Fall Time <sup>c</sup>   | $t_f$        |   |  | -    | 39     | 48        |               |
| <b>Source-Drain Diode Ratings and Characteristics<sup>b</sup></b>                  |              |   |  |      |        |           |               |
| Pulsed Current <sup>a</sup>  | $I_{SM}$     |   |  | -    | -      | -200      | A             |
| Forward Voltage  | $V_{SD}$     | $I_F = -50\text{ A}, V_{GS} = 0\text{ V}$   |  | -    | -0.8   | -1.5      | V             |

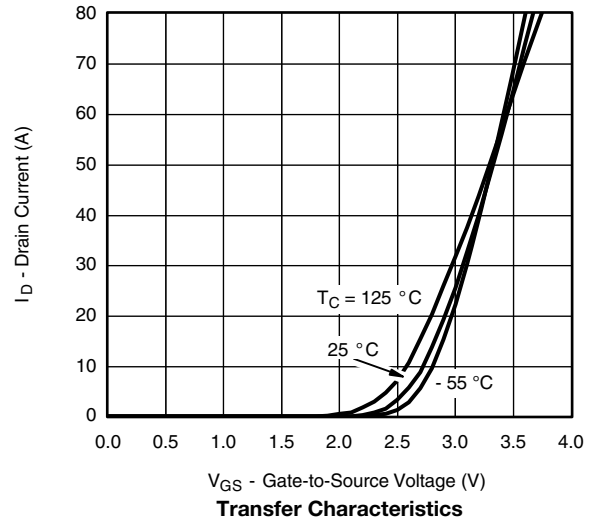
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

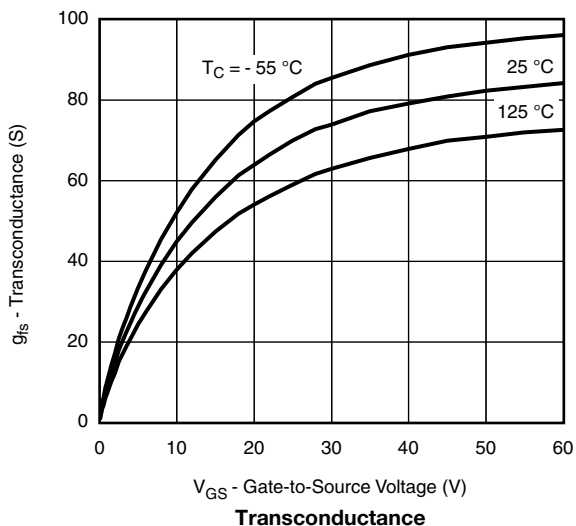
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



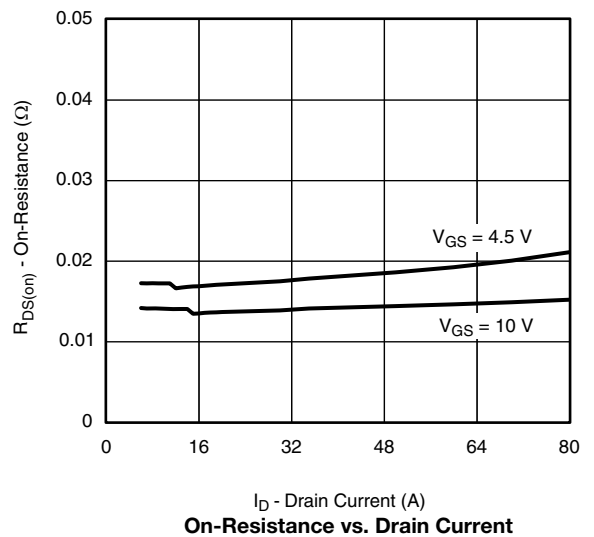
**Output Characteristics**



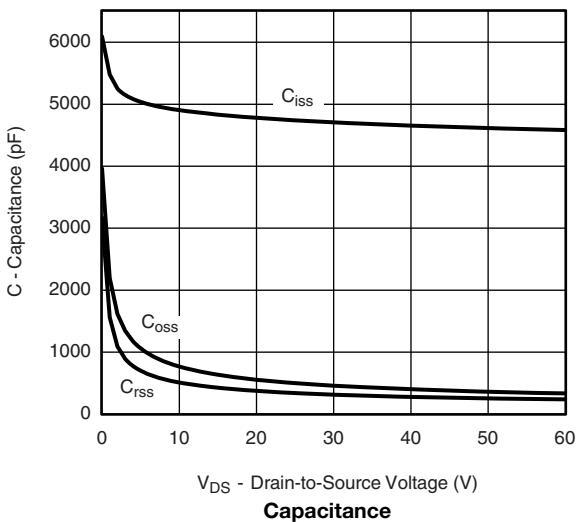
**Transfer Characteristics**



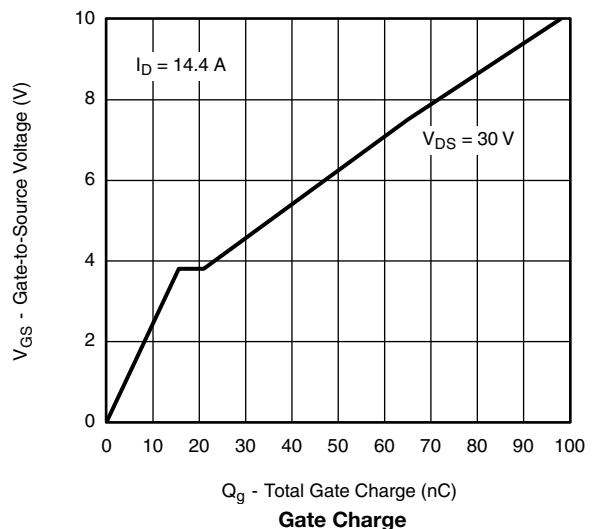
**Transconductance**



**On-Resistance vs. Drain Current**

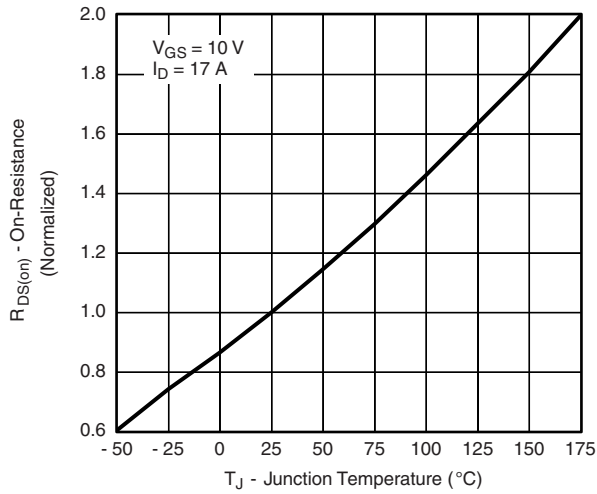


**Capacitance**

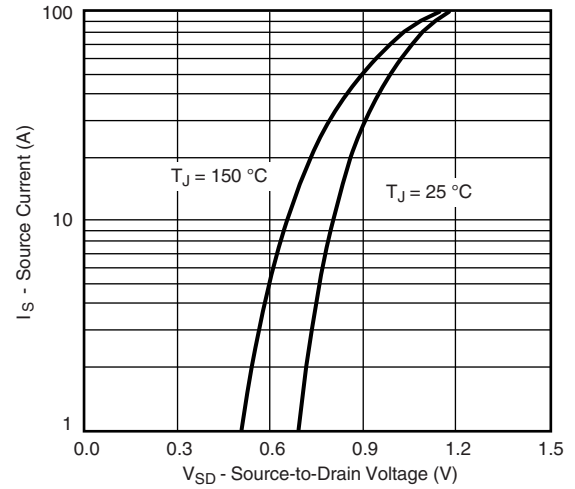


**Gate Charge**

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

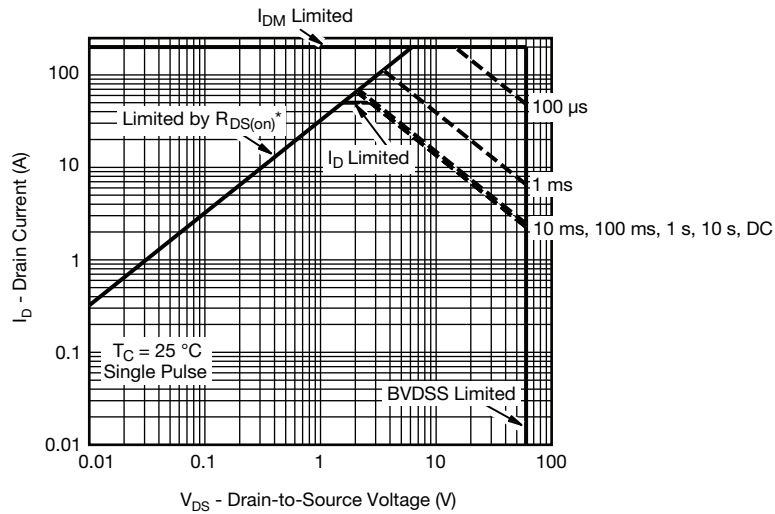


**On-Resistance vs. Junction Temperature**



**Source Drain Diode Forward Voltage**

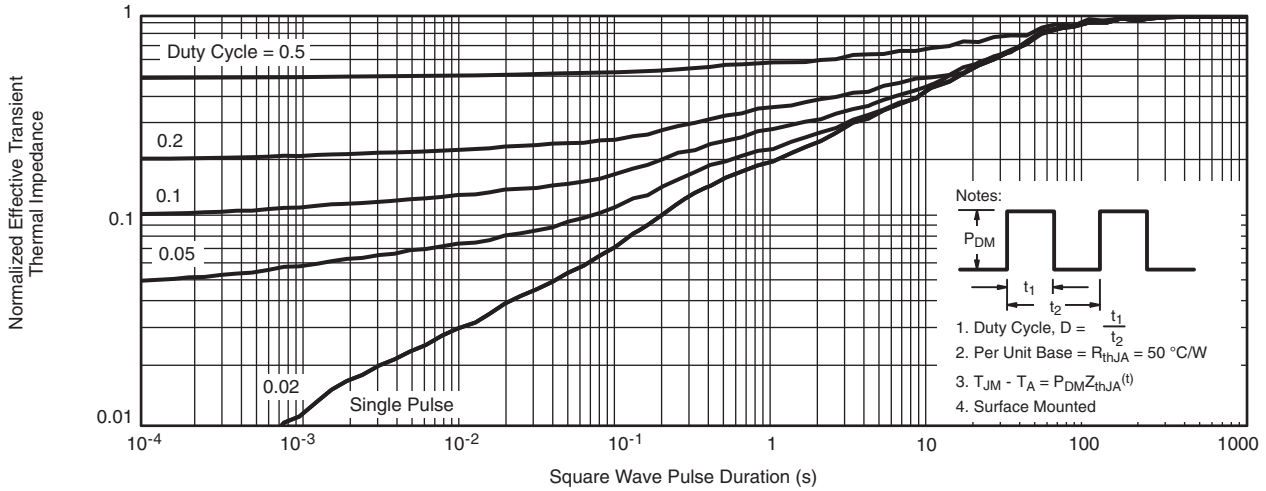
**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



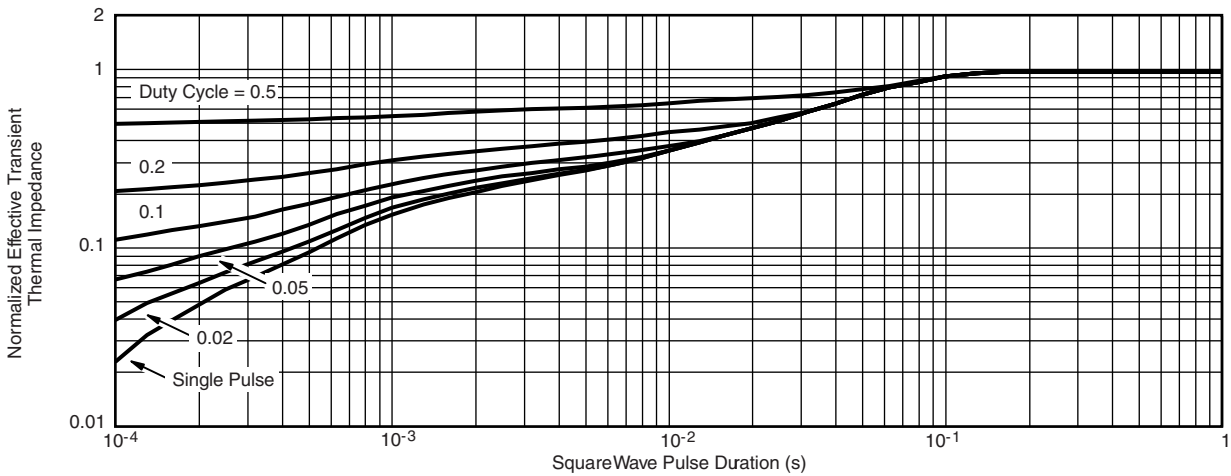
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area**

**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

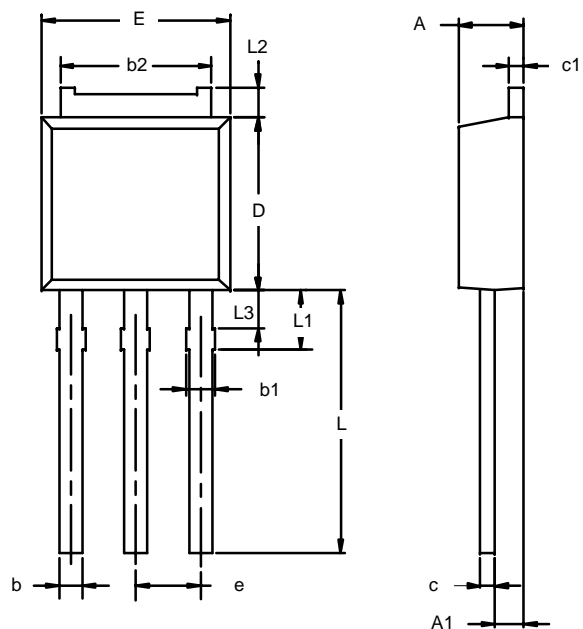


**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient ( $25\text{ }^\circ\text{C}$ )
  - Normalized Transient Thermal Impedance Junction to Case ( $25\text{ }^\circ\text{C}$ )
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

**TO-251AA**



Note: Dimension L3 is for reference only.

| Dim       | MILLIMETERS |      | INCHES    |       |
|-----------|-------------|------|-----------|-------|
|           | Min         | Max  | Min       | Max   |
| <b>A</b>  | 2.21        | 2.38 | 0.087     | 0.094 |
| <b>A1</b> | 0.89        | 1.14 | 0.035     | 0.045 |
| <b>b</b>  | 0.71        | 0.89 | 0.028     | 0.035 |
| <b>b1</b> | 0.76        | 1.14 | 0.030     | 0.045 |
| <b>b2</b> | 5.23        | 5.43 | 0.206     | 0.214 |
| <b>c</b>  | 0.46        | 0.58 | 0.018     | 0.023 |
| <b>c1</b> | 0.46        | 0.58 | 0.018     | 0.023 |
| <b>D</b>  | 5.97        | 6.22 | 0.235     | 0.245 |
| <b>E</b>  | 6.48        | 6.73 | 0.255     | 0.265 |
| <b>e</b>  | 2.28 BSC    |      | 0.090 BSC |       |
| <b>L</b>  | 3.89        | 9.53 | 0.153     | 0.375 |
| <b>L1</b> | 1.91        | 2.28 | 0.075     | 0.090 |
| <b>L2</b> | 0.89        | 1.27 | 0.035     | 0.050 |
| <b>L3</b> | 1.15        | 1.52 | 0.045     | 0.060 |

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