

Features

- $V_{DSS} = -20V$
- $I_D = -2.3A$
- $R_{DS(on)}@V_{GS} = -4.5V < 112m\Omega$
- $R_{DS(on)}@V_{GS} = -2.5V < 142m\Omega$
- Trench Power LV MOSFET technology
- High density cell design for low $R_{DS(ON)}$
- High Speed switching

Applications

- Battery protection
- Load switch
- Power management

Mechanical Data

- Case: SOT-23
Molding compound meets UL 94V-0 flammability rating, RoHS-compliant, halogen-free
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

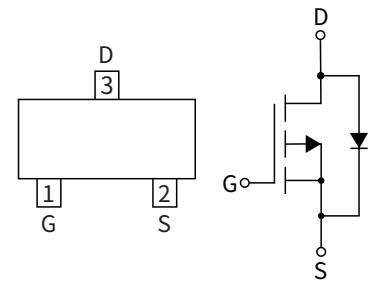
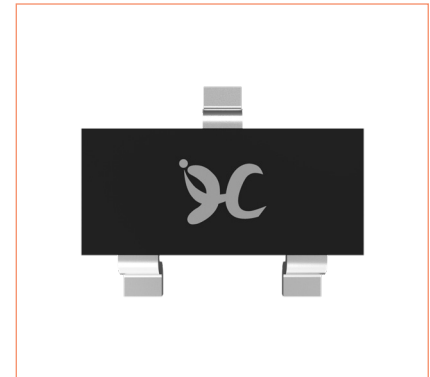
Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	
Drain-source Voltage	V_{RRM}	V	-20	
Gate-source Voltage	V_{RMS}	V	± 8	
Drain Current	I_d	A	Ta=25°C	-2.3
			Ta=70°C	-3.5
Pulsed Drain Current	I_{DM}	A	-10	
Total Power Dissipation	P_d	W	Ta=25°C	0.7
			Ta=70°C	0.4
Storage temperature	T_{stg}	°C	-55 ~ +150	
Junction temperature	T_j	°C	-55 ~ +150	
Typical Thermal Resistance	$R_{\theta J-A}$	°C / W	312	

Ordering Information

PACKAGE	PACKAGE CODE	UNIT WEIGHT(g)	REEL(pcs)	BOX(pcs)	CARTON(pcs)	DELIVERY MODE
SOT-23	R1	0.008	3000	30000	120000	7"

SOT-23



▶ Static Parameter Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	V	-20	—	—
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-20V, V_{GS}=0V$	μA	—	—	-1
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$	nA	—	—	± 100
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	V	-0.4	-0.7	-1.0
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=-2.8A$	m Ω	—	90	112
		$V_{GS}=-2.5V, I_C=-2A$		—	110	142
Diode Forward Voltage	V_{SD}	$I_S=0.7A$	V	—	-0.8	-1.2

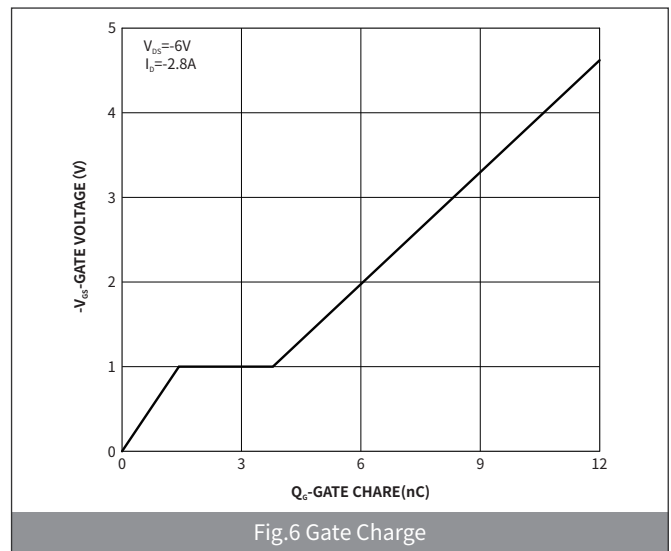
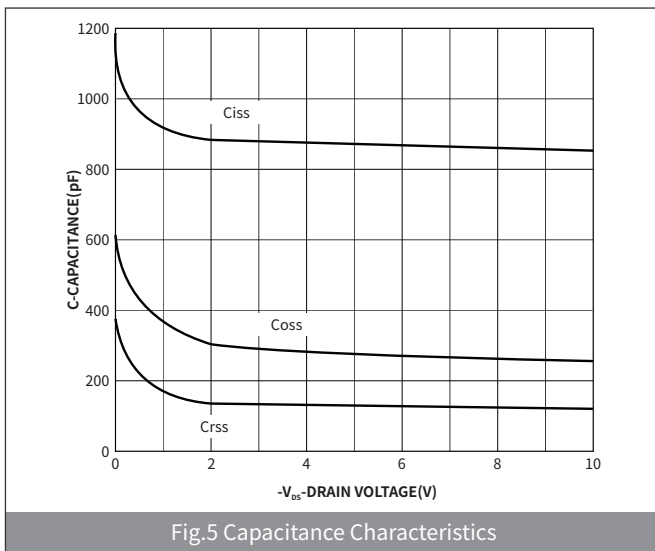
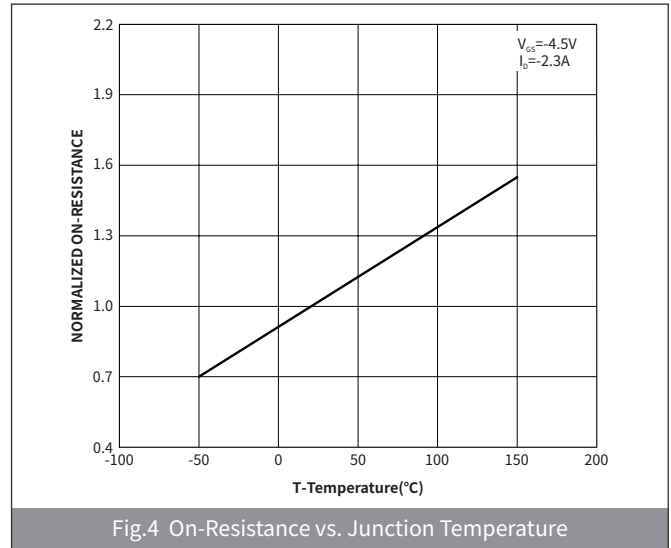
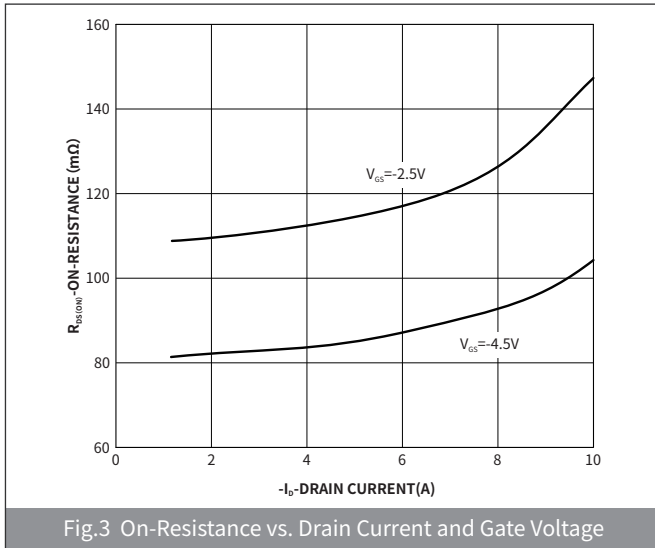
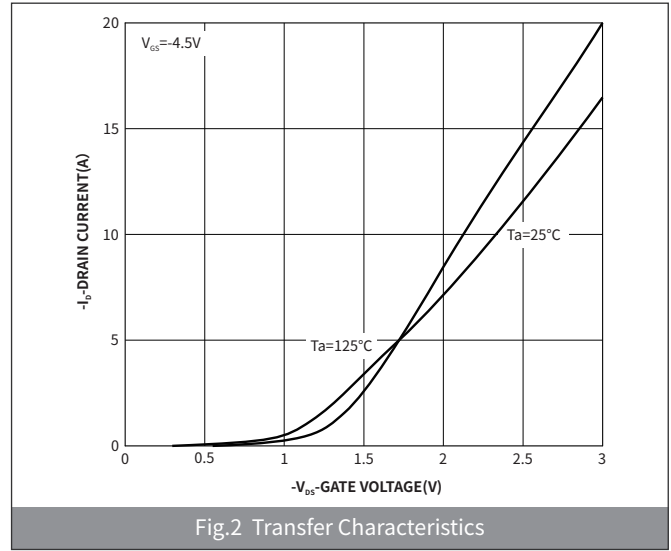
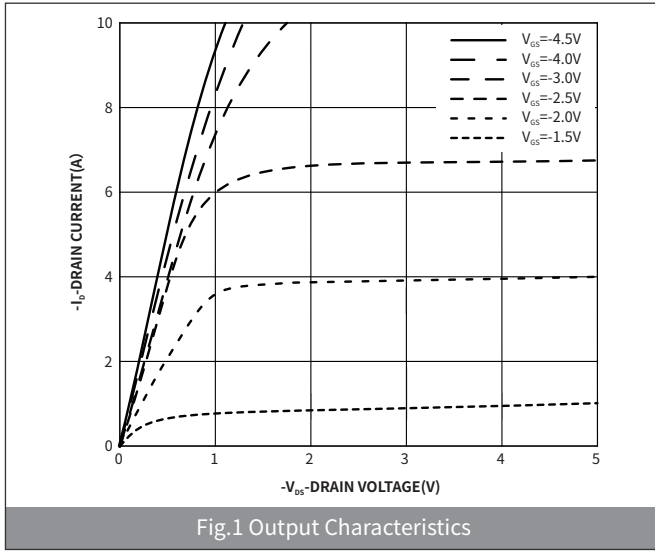
▶ Dynamic Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Input Capacitance	C_{iss}	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	pF	—	405	—
Output Capacitance	C_{oss}			—	75	—
Reverse Transfer Capacitance	C_{rss}			—	55	—

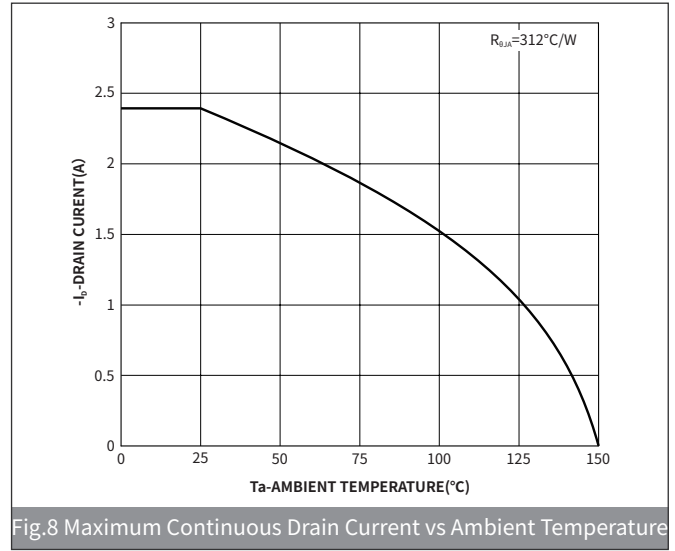
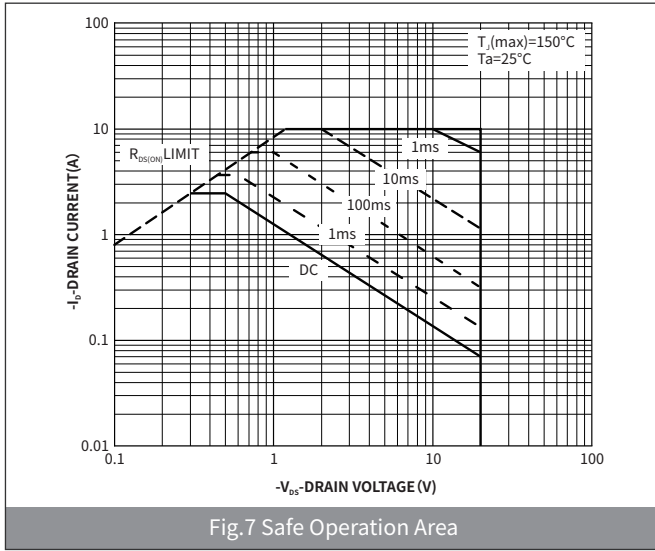
▶ Switching Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Total Gate Charge	Q_g	$V_{GS}=-4.5V, V_{DS}=-10V, I_D=-2.3A$	nC	—	5.5	10
				—	3.3	6
Gate-Source Charge	Q_{gs}	$V_{GS}=-2.5V, V_{DS}=-10V, I_D=-2.3A$		—	0.7	—
Gate-Drain Charge	Q_{gd}			—	1.3	—
Reverse Recovery Time	t_{rr}	$I_F=-2.3A, di/dt=100A/us$	ns	—	13.1	—
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-4.5V, V_{DS}=-10V, I_D=-1A$ $R_{GEN}=1\Omega$	ns	—	11	20
Turn-on Rise Time	t_r			—	35	60
Turn-off Delay Time	$t_{D(off)}$			—	30	50
Turn-off fall Time	t_f			—	10	20

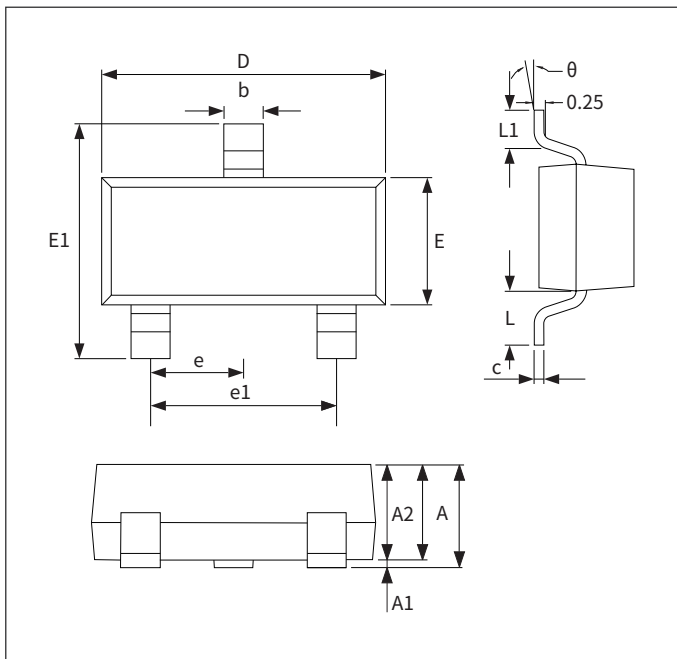
► Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



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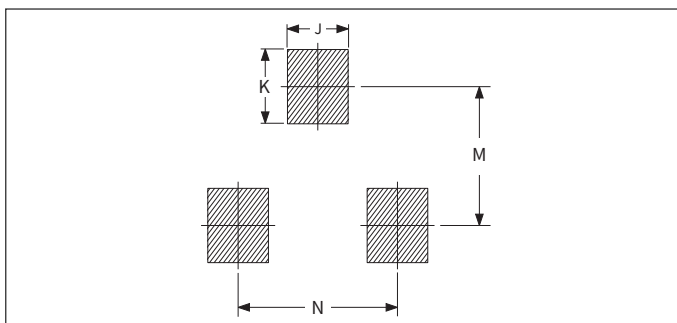


► Package Outline Dimensions (SOT-23)



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.15	0.035	0.045
A1	-	0.10	-	0.004
A2	0.90	1.05	0.035	0.041
b	0.30	0.50	0.012	0.020
c	0.10	0.20	0.004	0.008
D	2.80	3.00	0.110	0.118
E	1.20	1.40	0.047	0.055
E1	2.25	2.55	0.089	0.100
e	0.950TYP		0.037TYP	
e1	1.80	2.00	0.071	0.079
L	0.550REF		0.022REF	
L1	0.30	0.50	0.012	0.020
θ	-	8°	-	8°

► Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
J	0.80	-	0.031	-
K	-	0.90	-	0.035
M	2.00	-	0.078	-
N	-	1.90	-	0.074