



Features

- ★ Advanced Trench MOSFET Technology
- ★ Reliable and Rugged
- ★ Green Device Available

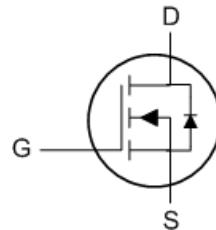
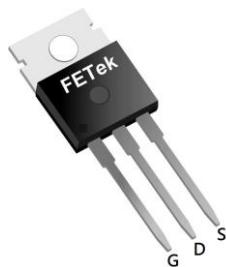
Product Summary

BVDSS	RDS(ON)	ID
100V	47mΩ	37A

Applications

- ★ Hard Switching and High Speed circuit.
- ★ Synchronous Rectification in SMPS
- ★ DC/DCC in Telecoms and Industrial

TO220 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current ¹	37	A
I _D @T _C =100°C	Continuous Drain Current ¹	23	A
I _D @T _A =25°C	Continuous Drain Current ¹	4.2	A
I _D @T _A =70°C	Continuous Drain Current ¹	3.3	A
I _{DM}	Pulsed Drain Current ²	110	A
EAS	Single Pulse Avalanche Energy ³	168.7	mJ
I _{AS}	Avalanche Current	15	A
P _D @T _C =25°C	Total Power Dissipation ⁴	87	W
P _D @T _A =25°C	Total Power Dissipation ⁴	2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.0	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

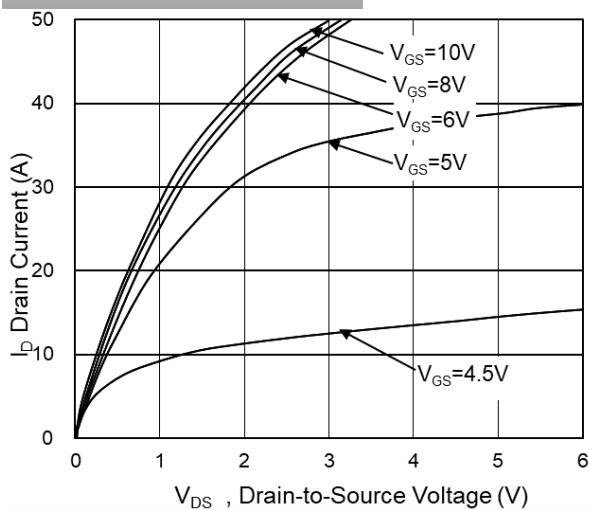
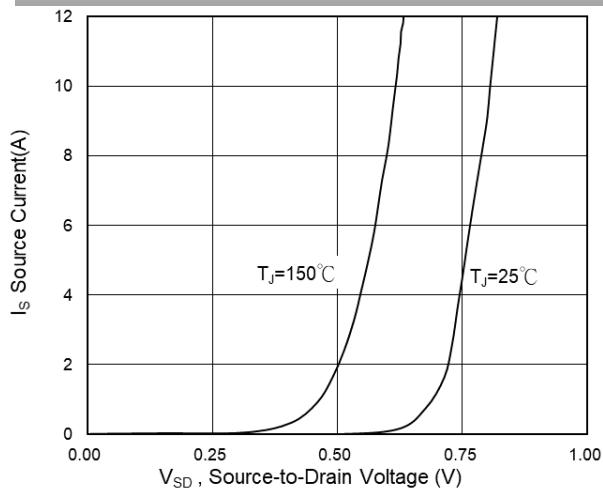
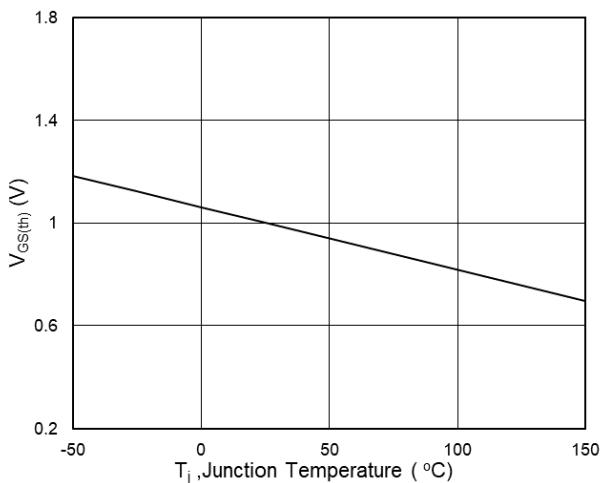
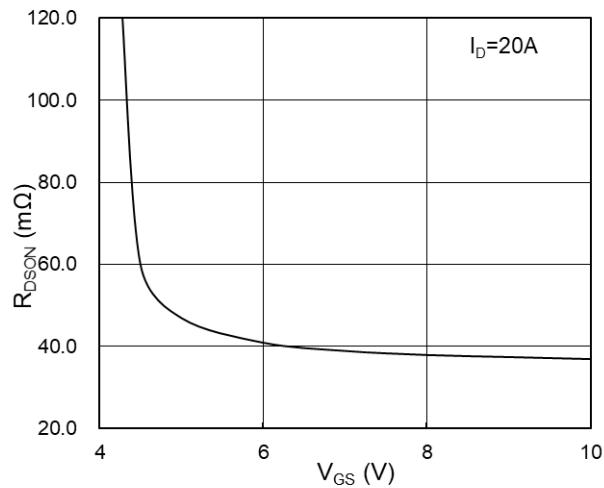
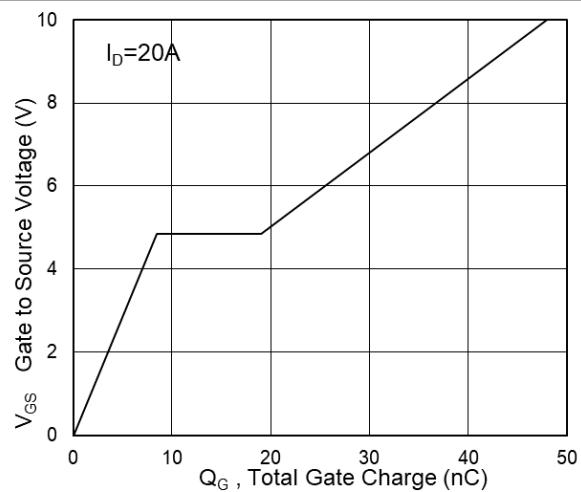
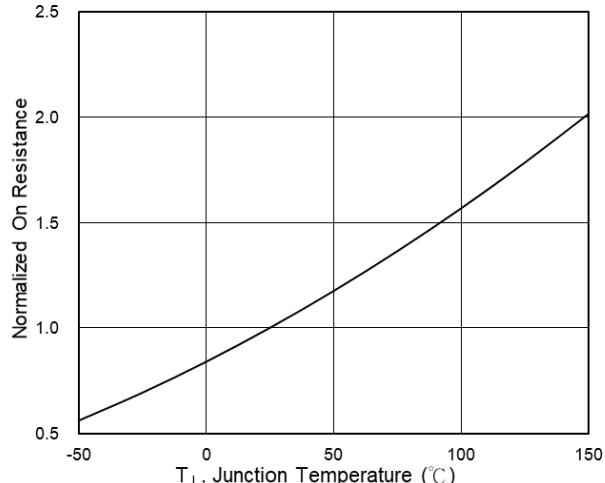
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	---	47	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	2	3	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	10	μA
		$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=20\text{A}$	---	28.7	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.6	---	Ω
Q_g	Total Gate Charge (10V)	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	48	---	nC
Q_{gs}	Gate-Source Charge		---	8.5	---	
Q_{gd}	Gate-Drain Charge		---	10.5	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_g=3.3\Omega$	---	10	---	ns
T_r	Rise Time		---	42	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	50	---	
T_f	Fall Time		---	9.5	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3848	---	pF
C_{oss}	Output Capacitance		---	137	---	
C_{rss}	Reverse Transfer Capacitance		---	82	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	37	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	100	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	30	---	nS
Q_{rr}	Reverse Recovery Charge		---	37	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=27\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.3 Source Drain Forward Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs T_J

Fig.2 On-Resistance vs G-S Voltage

Fig.4 Gate-Charge Characteristics

Fig.6 Normalized $R_{DS(on)}$ vs T_J

