

ROF

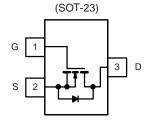
COMPLIANT

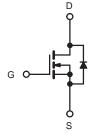
# N-Channel 20V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a, g</sup>	Q <sub>g</sub> (Typ.)			
20	0.014 at V <sub>GS</sub> = 4.5V	8	9nC			
	0.018 at V <sub>GS</sub> = 2.5V	7	900			

### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Gen III Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC





N-Channel MOSFET

Parameter Drain-Source Voltage Gate-Source Voltage		Symbol	Limit	Unit	
		V <sub>DS</sub>	20	V	
		V <sub>GS</sub>	± 12	V	
	T <sub>C</sub> = 25 °C		8 <sup>a, g</sup>		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C		5.5 <sup>g</sup>		
$Continuous Drain Current (1) = 150^{\circ} C)$	T <sub>A</sub> = 25 °C	I <sub>D</sub>	6 <sup>b, c</sup>	Α	
	T <sub>A</sub> = 70 °C		3.5 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	30 <sup>g</sup>		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	14		
Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	11.25	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		3 <sup>a, g</sup>	A	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.5 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		3.2		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.8	W	
	T <sub>A</sub> = 25 °C	<sup>r</sup> D	2.2 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		1.0 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	0°C	
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>			260		

### THERMAL RESISTANCE RATINGS

THENMAL RESISTANCE NA	mas				
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 10 s	R <sub>thJA</sub>	29	36	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	3.6	4.5	0/11

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

- d. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A
- solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection. e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

f. Maximum under Steady State conditions is 81 °C/W.

g. Package limited.

<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				_		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		22		- mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 - 200 μΛ		- 5.0		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.5		1.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zara Cata Valtaga Drain Current	la a a	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	12			A
Durain Courses On Otate Desistenced		V <sub>GS</sub> = 4.5V,I <sub>D</sub> = 10 A		0.014		0
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 V$ , $I_{D} = 7 A$		0.018		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A		26		S
Dynamic <sup>b</sup>			•	•	•	
Input Capacitance	C <sub>iss</sub>			865		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		316		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			131		
Tetel Octo Oberra	0	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		15	23	
Total Gate Charge	Q <sub>g</sub>		10			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 10 A		2.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			2.3		
Gate Resistance	Rg	f = 1 MHz	0.2	0.9	1.8	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			15	30	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_{L}$ = 2 $\Omega$		11	22	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong\text{5}$ A, $\text{V}_\text{GEN}$ = 4.5 V, $\text{R}_\text{g}$ = 1 $\Omega$		16	30	-
Fall Time	t <sub>f</sub>			8	16	
Turn-On Delay Time	t <sub>d(on)</sub>			10	20	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_{L}$ = 2 $\Omega$		8	16	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 5 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		16	30	1
Fall Time	t <sub>f</sub>			7	14	1
Drain-Source Body Diode Characteristi	cs				•	
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C		8		
Pulse Diode Forward Current	I <sub>SM</sub>			30		A
Body Diode Voltage	V <sub>SD</sub>	$I_{S} = 3 \text{ A}, V_{GS} = 0 \text{ V}$		0.77	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			14	28	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	)		4.5	9	nC
Reverse Recovery Fall Time	ta	I <sub>F</sub> = 10 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		5.5		
Reverse Recovery Rise Time	t <sub>b</sub>			8.5		ns

Notes:

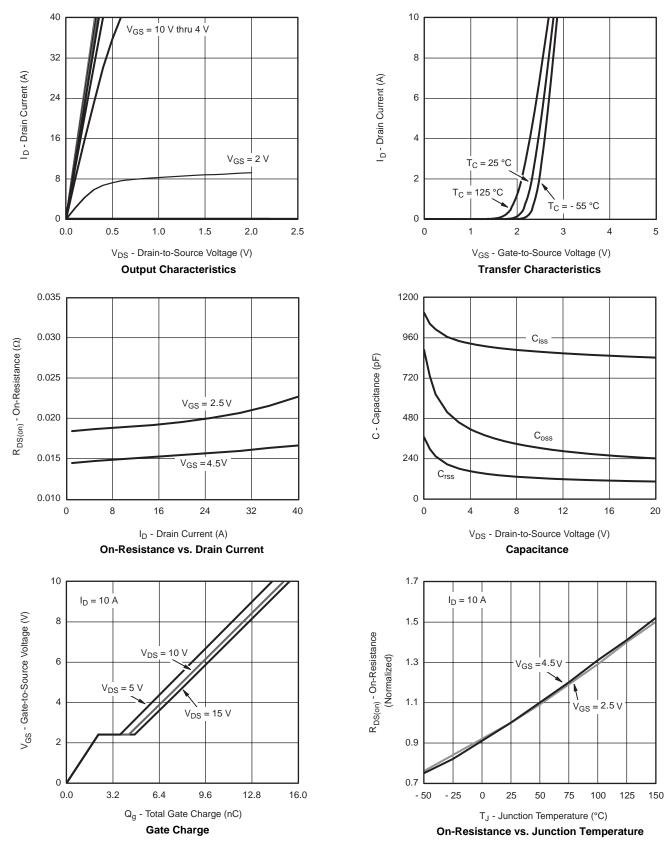
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

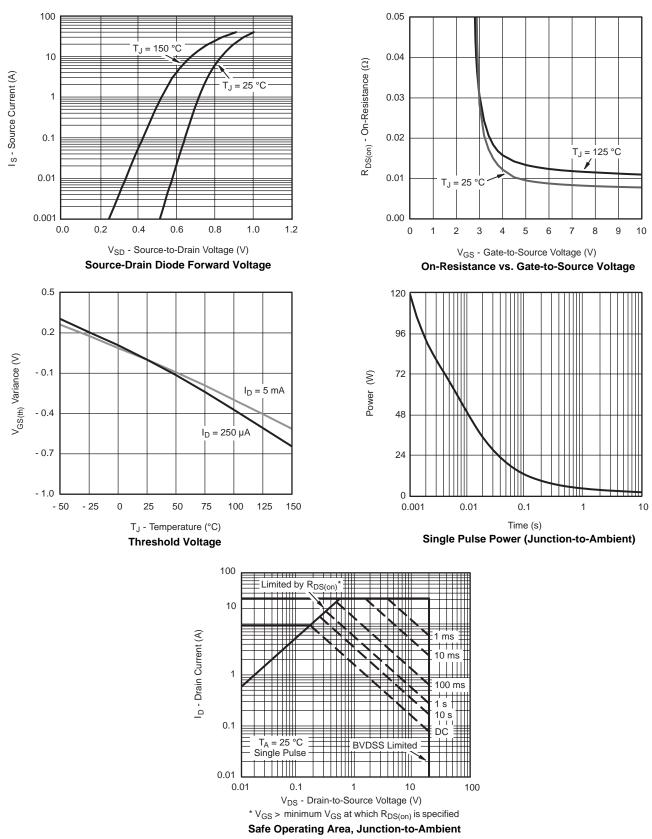
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







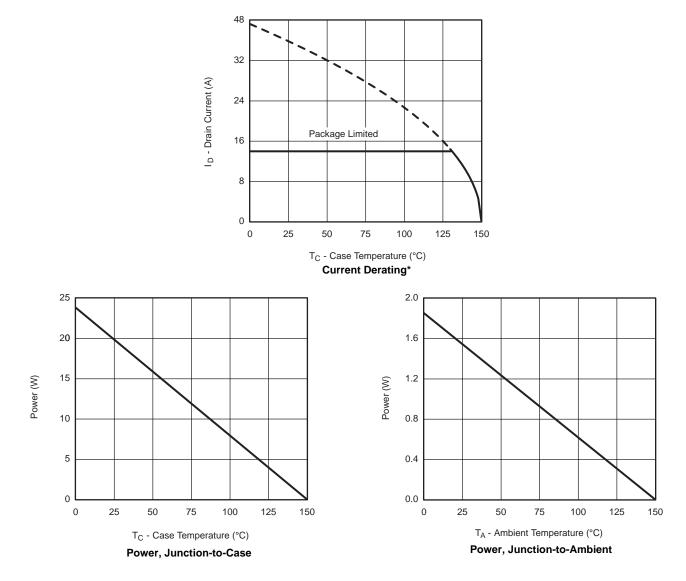




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



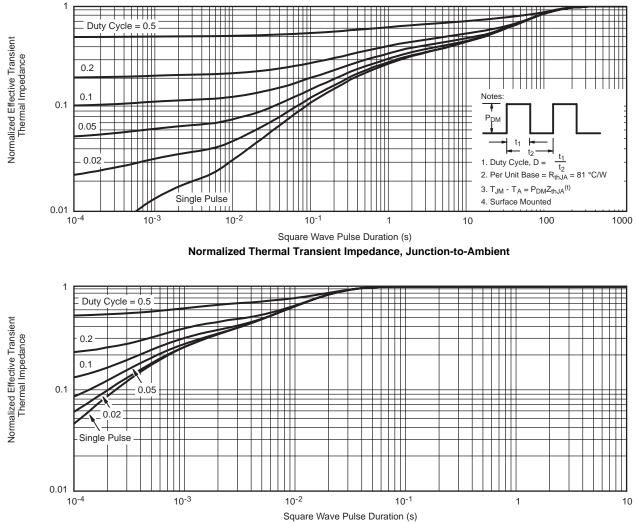
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



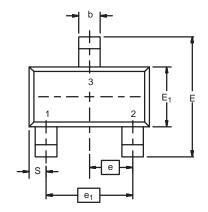
# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



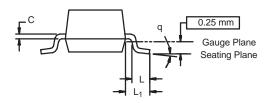
Normalized Thermal Transient Impedance, Junction-to-Case



## SOT-23 : 3-LEAD



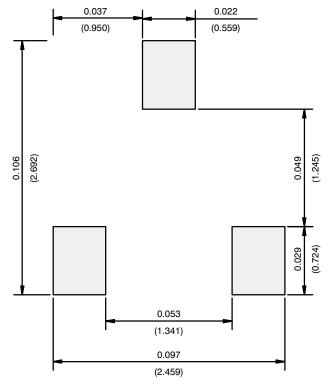




Dim -	MILLIN	IETERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
C	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020	0.020 Ref	
q	3°	8°	3°	8°	



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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