

N-Channel 20 V (D-S) MOSFET

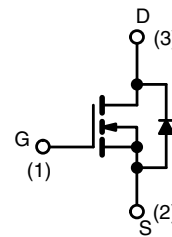
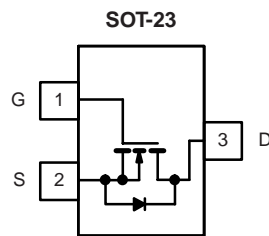
PRODUCT SUMMARY	
V _{DS} (V)	20
R _{DS(on)} max. (Ω) at V _{GS} = 4.5V	0.020
R _{DS(on)} max. (Ω) at V _{GS} = 2.5V	0.025
Q _g typ. (nC)	4.0
I _D (A) ^{a, e}	6
Configuration	Single

FEATURES

- TrenchFET® power MOSFET
- Low on-resistance
- 100 % R_g tested
- Material categorization:
for definitions of compliance please see



RoHS
COMPLIANT



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DS}	20	V
Gate-source voltage	V _{GS}	± 12	
Continuous drain current (T _J = 150 °C)	I _D	T _C = 25 °C	6 ^e
		T _C = 70 °C	5 ^e
		T _A = 25 °C	5.0 ^{b, c}
		T _A = 70 °C	4.4 ^{b, c}
Pulsed drain current (t = 300 μs)	I _{DM}	26	A
Continuous source-drain diode current	I _S	T _C = 25 °C	
		T _A = 25 °C	1.1 ^{b, c}
Maximum power dissipation	P _D	T _C = 25 °C	2.8
		T _C = 70 °C	1.6
		T _A = 25 °C	1.3 ^{b, c}
		T _A = 70 °C	0.8 ^{b, c}
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C
Soldering recommendations (peak temperature)		260	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient ^{b, d}	R _{thJA}	75	100	°C/W	
Maximum junction-to-foot (drain)	R _{thJF}	40	50		

Notes

- Based on T_C = 25 °C
- Surface mounted on 1" x 1" FR4 board
- t = 5 s
- Maximum under steady state conditions is 166 °C/W
- Package limited

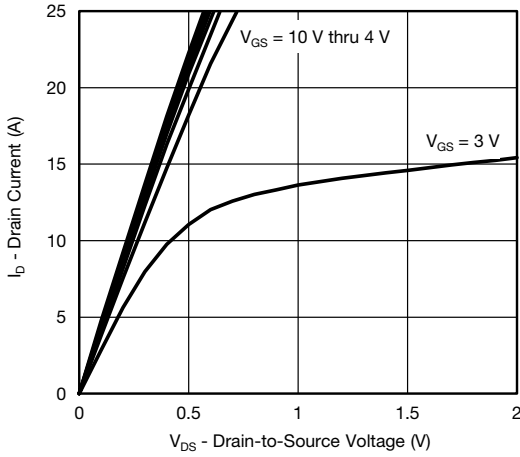
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	20	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	30	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-4.8	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.5	-	1.5	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 20V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 20V, V _{GS} = 0 V, T _J = 70 °C	-	-	10	
On-state drain current ^a	I _{D(on)}	V _{DS} ≤ 5 V, V _{GS} = 10 V	20	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5V, I _D = 5.5 A	-	0.020	-	Ω
		V _{GS} = 2.5 V, I _D = 5 A	-	0.025	-	
Forward transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5.5 A	-	24	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	-	900	-	pF
Output capacitance	C _{oss}		-	100	-	
Reverse transfer capacitance	C _{rss}		-	42	-	
Total gate charge	Q _g	V _{DS} = 10 V, V _{GS} = 10 V, I _D = 5.5 A	-	8.2	13	nC
		V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 5.5 A	-	4.2	7	
Gate-source charge	Q _{gs}		-	1.4	-	
Gate-drain charge	Q _{gd}		-	1.4	-	
Gate resistance	R _g	f = 1 MHz	2.5	12.6	25.2	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 10 V, R _L = 3.4 Ω I _D ≅ 4.4 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	6	12	ns
Rise time	t _r		-	20	30	
Turn-off delay time	t _{d(off)}		-	14	21	
Fall time	t _f		-	10	20	
Turn-on delay time	t _{d(on)}	V _{DD} = 10 V, R _L = 3.4 Ω I _D ≅ 4.4 A, V _{GEN} = 10 V, R _g = 1 Ω	-	3	6	
Rise time	t _r		-	11	20	
Turn-off delay time	t _{d(off)}		-	20	30	
Fall time	t _f		-	7	14	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	7	-	A
Pulse diode forward current	I _{SM}		-	-	25	
Body diode voltage	V _{SD}	I _S = 4.4 A, V _{GS} = 0 V	-	0.82	1.2	V
Body diode reverse recovery time	t _{rr}	I _F = 4.4 A, di/dt = 100 A/μs, T _J = 25 °C	-	13	20	ns
Body diode reverse recovery charge	Q _{rr}		-	6	12	nC
Reverse recovery fall time	t _a		-	8	-	ns
Reverse recovery rise Time	t _b		-	5	-	

Notes

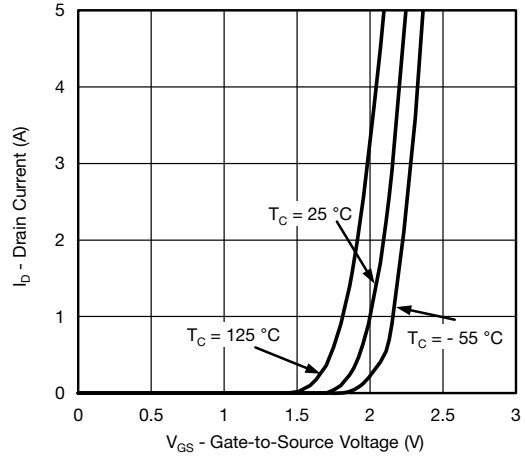
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

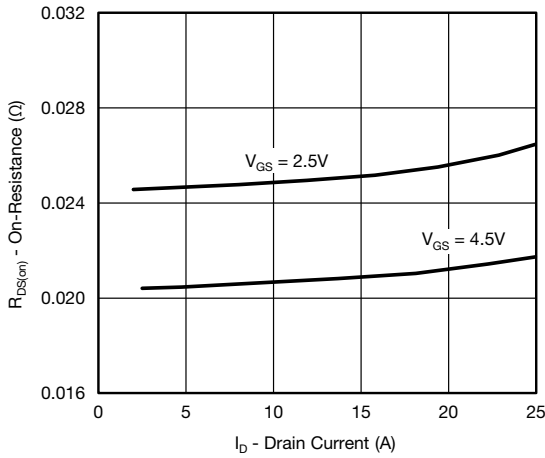
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



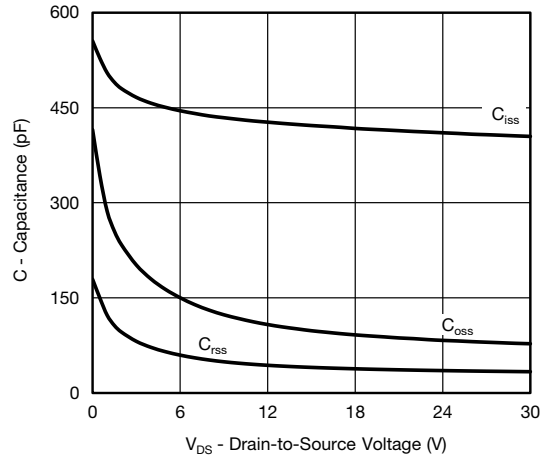
Output Characteristics



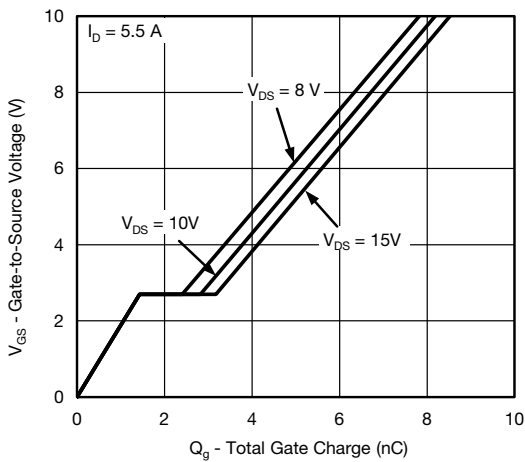
Transfer Characteristics



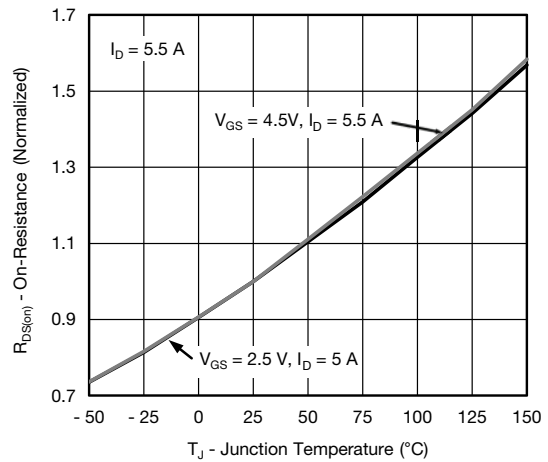
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

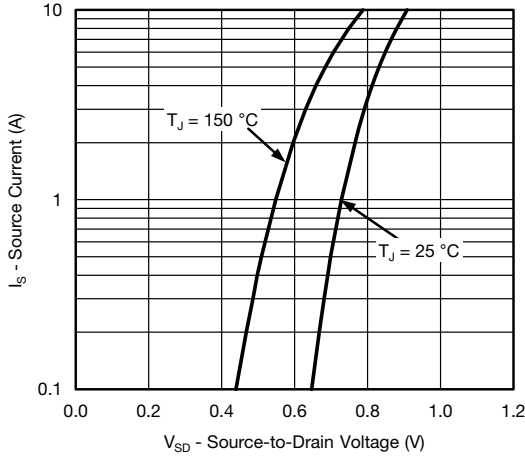


Gate Charge

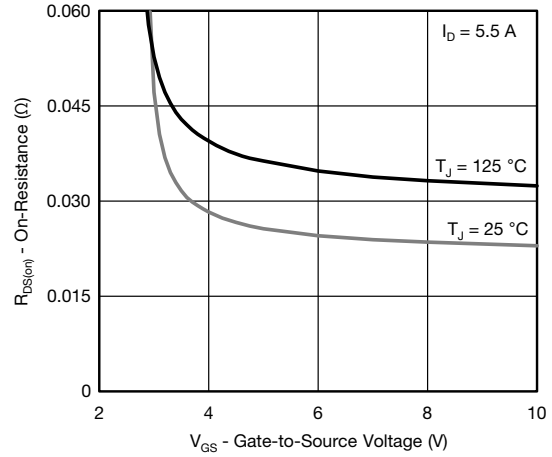


On-Resistance vs. Junction Temperature

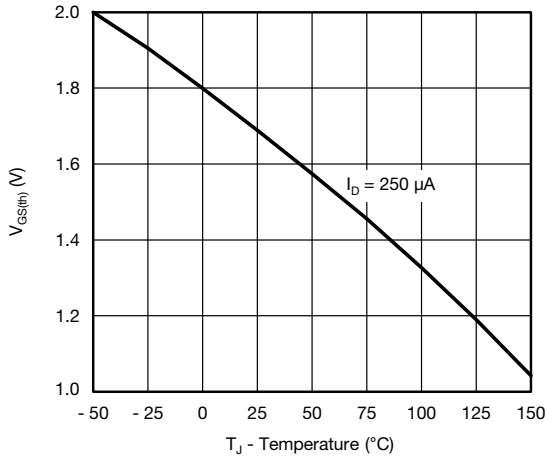
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



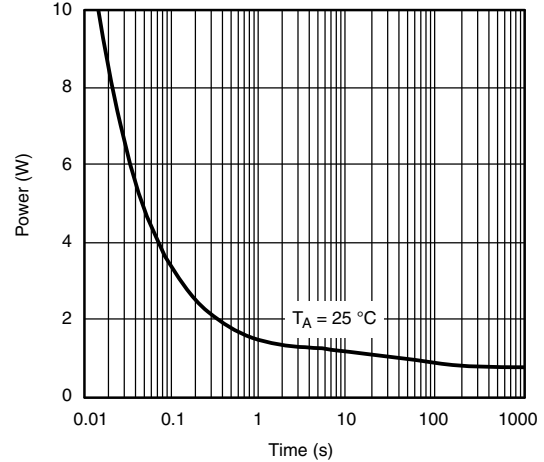
Source-Drain Diode Forward Voltage



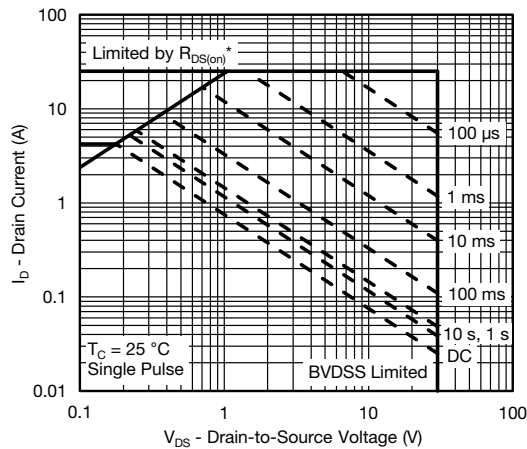
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

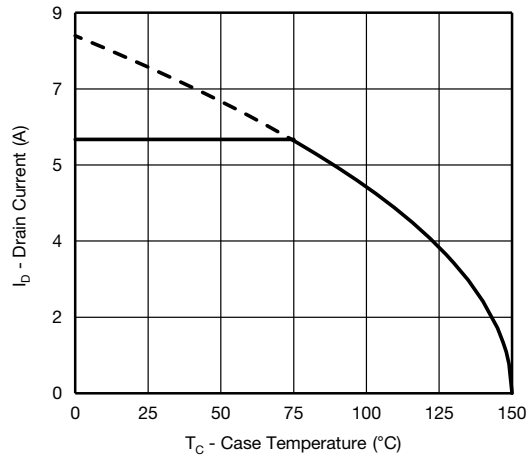


Single Pulse Power (Junction-to-Ambient)

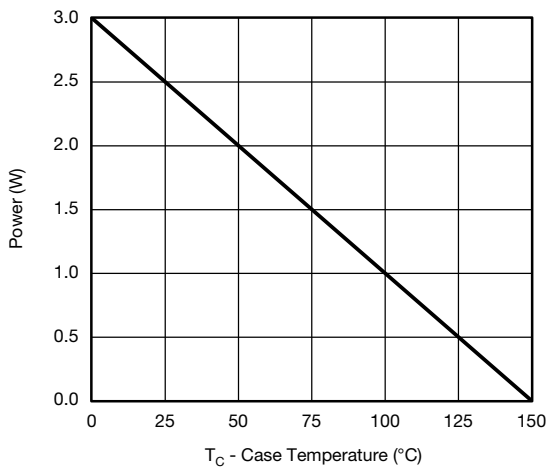


Safe Operating Area, Junction-to-Ambient

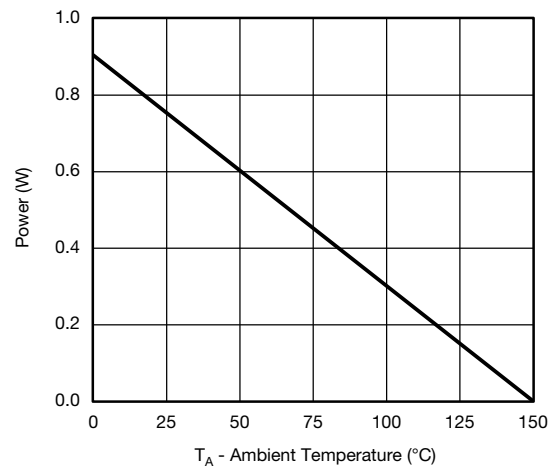
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating^a



Power Derating, Junction-to-Foot

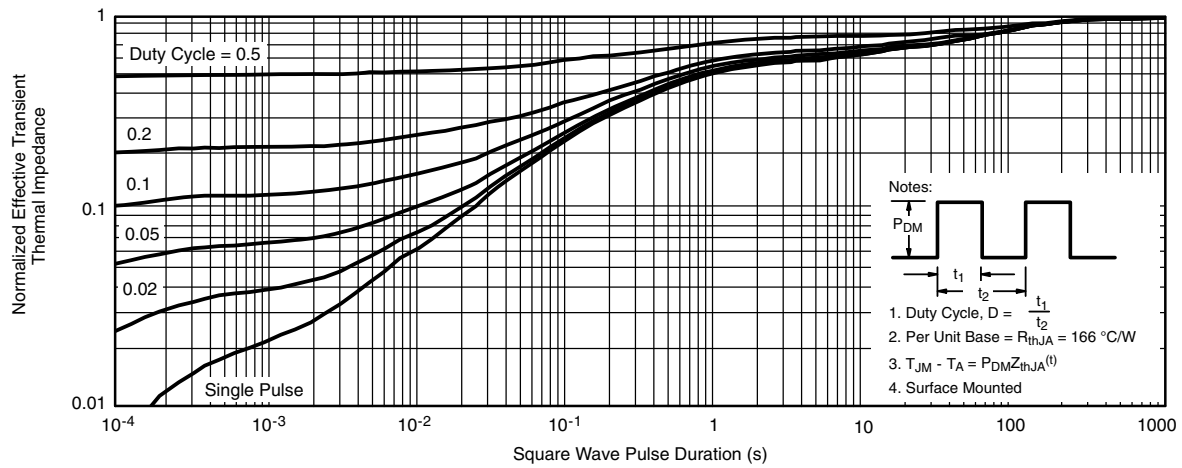


Power Derating, Junction-to-Ambient

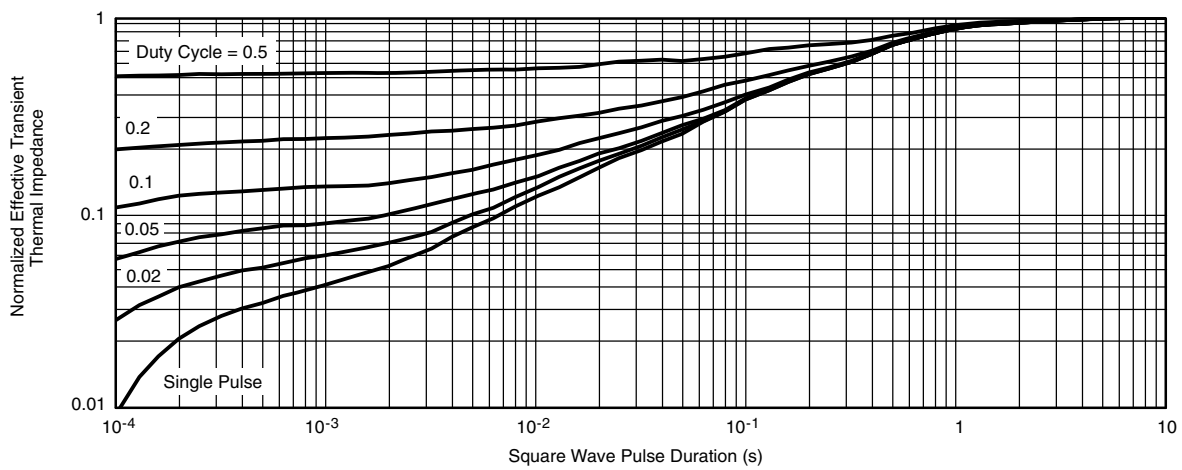
Note

- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

SOT-23 (TO-236): 3-LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°

ECN: S-03946-Rev. K, 09-Jul-01
DWG: 5479

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads
Dimensions in Inches/(mm)

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