

N-Channel 20V (D-S) MOSFET

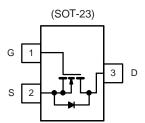
PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A) ^{a, g}	Q _g (Typ.)			
20	0.015 at V _{GS} = 4.5V	8	10nC			
20	0.018 at V _{GS} = 2.5V	7	TONC			

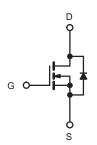
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Gen III Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V_{GS}	± 12	V	
	T _C = 25 °C		8 ^{a, g}		
Continuous Drain Current (T _{.1} = 150 °C)	$T_C = 70 ^{\circ}C$	I _D	5.5 ^g		
Continuous Diam Current (1) = 130 C)	T _A = 25 °C	'D	6 ^{b, c}	A	
	T _A = 70 °C		3.5 ^{b, c}		
Pulsed Drain Current		I _{DM}	30 ^g		
Avalanche Current	L = 0.1 mH	I _{AS}	14		
Avalanche Energy		E _{AS}	11.25	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C	I _s	3 ^{a, g}	А	
Continuous Course Brain Blode Current	T _A = 25 °C	'8	1.5 ^{b, c}	, ,	
	T _C = 25 °C		3.2		
Maximum Power Dissipation	$T_C = 70 ^{\circ}C$	P _D	1.8	w	
Maximum Fower Dissipation	T _A = 25 °C	' b	2.2 ^{b, c}	• • • • • • • • • • • • • • • • • • • •	
	T _A = 70 °C		1.0 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R_{thJA}	29	36	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	3.6	4.5	0/11	

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 81 °C/W.
- g. Package limited.



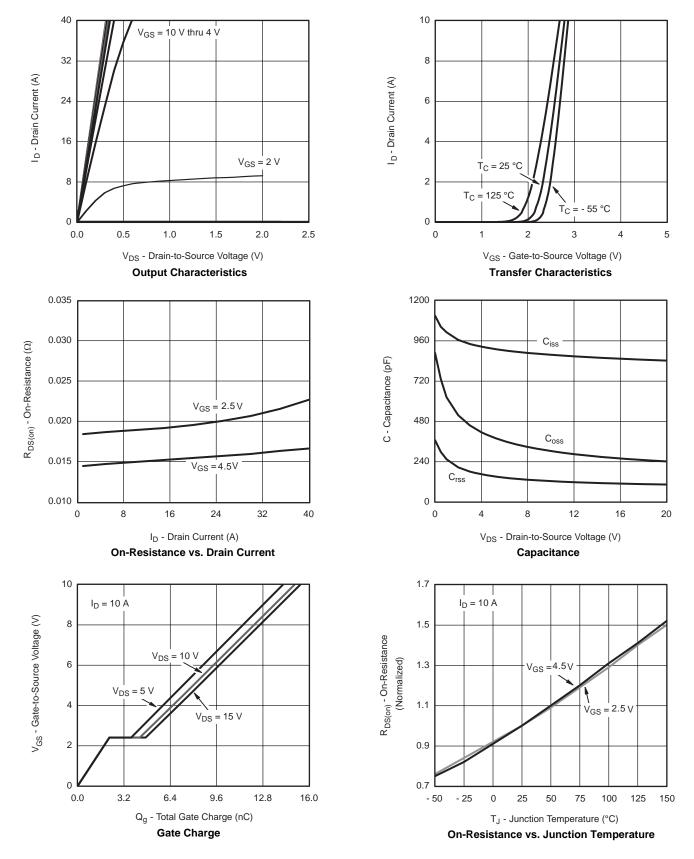
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		22		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.0		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	0.5		1.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7. 0. 1/1. 5. 0		V _{DS} = 20 V, V _{GS} = 0 V			1	μА
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	12			Α
D : 0		$V_{GS} = 4.5V, I_{D} = 10 A$		0.015		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5V$, $I_{D} = 7 A$		0.018		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 10 A		26		S
Dynamic ^b						
Input Capacitance	C _{iss}			865		
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		316		pF
Reverse Transfer Capacitance	C _{rss}			131		
· · · · · · · · · · · · · · · · · · ·		V _{DS} = 10 V, V _{GS} = 10 V, I _D = 10 A		15	23	nC
Total Gate Charge	Q _g			10		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		2.5		
Gate-Drain Charge	Q _{gd}	20 00 0		2.3		
Gate Resistance	R _g	f = 1 MHz	0.2	0.9	1.8	Ω
Turn-On Delay Time	t _{d(on)}			15	30	
Rise Time	t _r	$V_{DD} = 10 \text{ V, R}_{1} = 2 \Omega$		11	22	- - -
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_q = 1 \Omega$		16	30	
Fall Time	t _f	· ·		8	16	
Turn-On Delay Time	t _{d(on)}			10	20	ns
Rise Time	t _r	$V_{DD} = 10 \text{ V, } R_1 = 2 \Omega$		8	16	- - -
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 1 \Omega$		16	30	
Fall Time	t _f	Ç		7	14	
Drain-Source Body Diode Characteristi	cs			I.		
Continuous Source-Drain Diode Current Is		T _C = 25 °C		8		
Pulse Diode Forward Current	I _{SM}			30		A
Body Diode Voltage	V _{SD}	I _S = 3 A, V _{GS} = 0 V		0.77	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			14	28	ns
Body Diode Reverse Recovery Charge	Q _{rr}			4.5	9	nC
Reverse Recovery Fall Time	t _a	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		5.5		<u> </u>
Reverse Recovery Rise Time	t _b			8.5		ns

Notes:

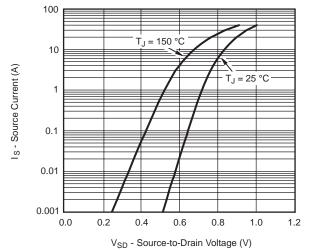
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

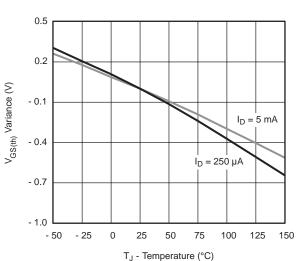








Source-Drain Diode Forward Voltage

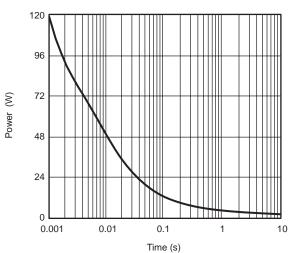


Threshold Voltage

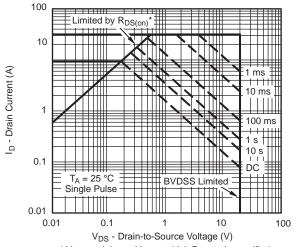
0.05 0.04 0.03 0.02 0.01 0.00 0 1 2 3 4 5 6 7 8 9 10

V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



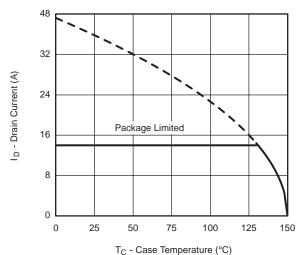
Single Pulse Power (Junction-to-Ambient)



* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

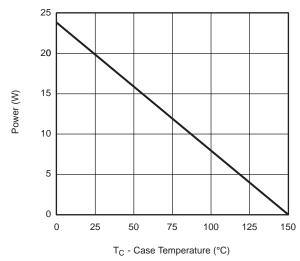
Safe Operating Area, Junction-to-Ambient

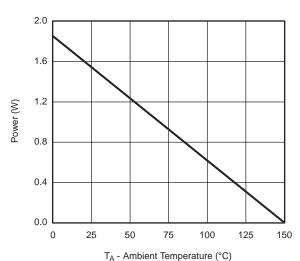




Current Deretine*

Current Derating*



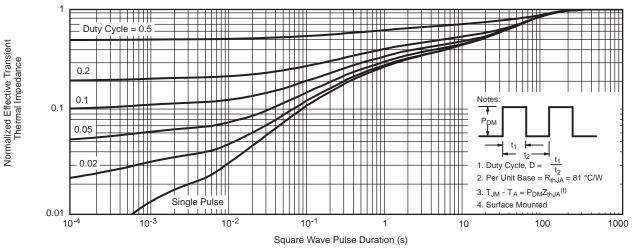


Power, Junction-to-Case

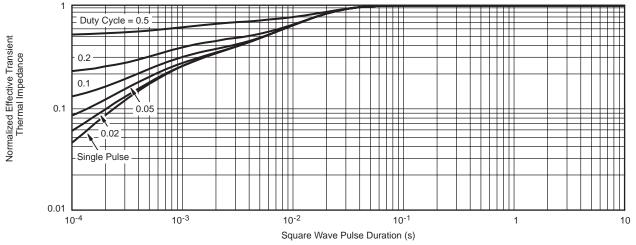
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





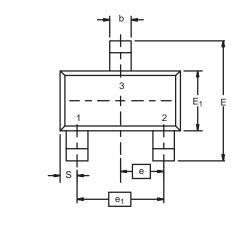
Normalized Thermal Transient Impedance, Junction-to-Ambient

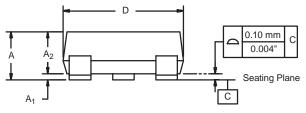


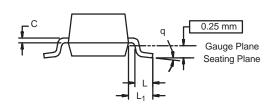
Normalized Thermal Transient Impedance, Junction-to-Case



SOT-23: 3-LEAD





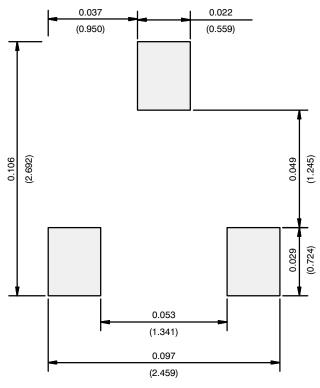


Dim	MILLIM	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01	•			

DWG: 5479



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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