Murata Power Solutions

iLGA
inspectable LGA

Typical unit

FEATURES

- **i**LGA inspectable Land Grid Array
- 2.9-14Vdc input voltage range
- Programmable output voltage from 0.9-5.5Vdc
- Drives up to 200 µF ceramic capacitive loads
- High power conversion efficiency at 90%
- Outstanding thermal derating performance
- Short circuit and over current protection
- On/Off control
- UL/IEC 60950-1 safety
- RoHS hazardous substance compliance

PRODUCT OVERVIEW

The OKL-T/1-W12 series are non-isolated Point-of-Load (PoL) DC/DC power converters for embedded applications. Featuring inspectable Land Grid Array (**i**LGA) format, the OKL-T/1-W12 measures only 0.488 x 0.488 x 0.18 inches max. (12.4 x 12.4 x 4.57 mm max.).

The wide input range is 2.9 to 14 Volts DC. The maximum output current is 1 Amp. Based on fixed-frequency synchronous buck converter switching topology, the high power conversion efficient Point of Load (PoL) module features programmable output voltage and On/Off control. These converters also include under voltage lock out (UVLO), output short circuit protection, and over-current protections.

These units are designed to meet all standard UL/IEC 60950-1 safety certifications (2nd Edition) and RoHS hazardous substance compliance.
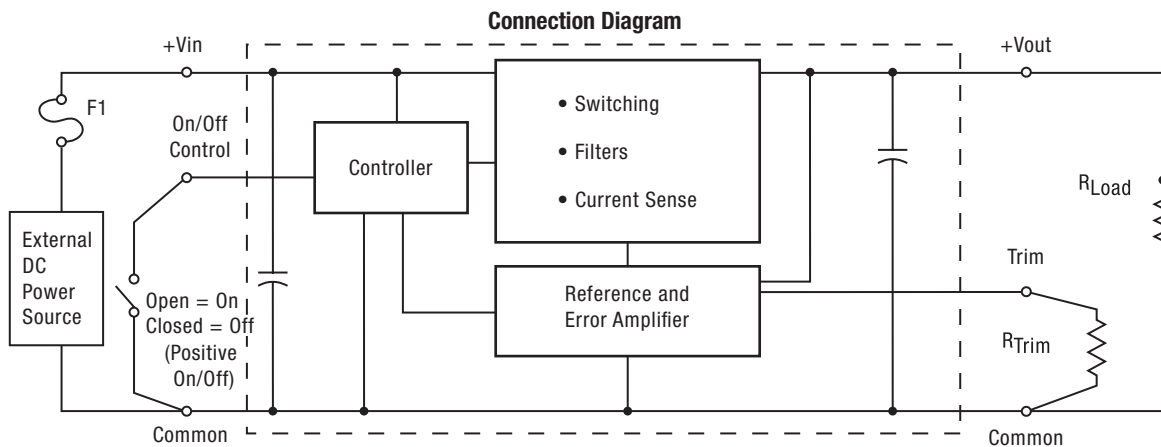
Connection Diagram

Figure 1. OKL-T/1-W12

Note: Murata Power Solutions strongly recommends an external input fuse, F1.
See specifications.

RoHS COMPLIANT

25
For full details go to
www.murata-ps.com/rohs

www.murata-ps.com/support

PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE

| Model Number | Output | | | | | | Input | | | | Efficiency | | On/Off Logic | Package - Pinout P83 |
| | Vout (Volts) ① | Iout (Amps, max.) | Power (Watts) | R/N (mV p-p) Max. ④ | Regulation (max.) | | Vin nom. (Volts) | Range (Volts) | Iin, no load (mA) ④ | Iin, full load (Amps) ② | Min. | Typ. | | Case C83 inches (mm) |
					Line	Load								
OKL-T/1-W12P-C	0.9-5.5	1	5	10	±0.25%	±0.25%	12	2.9-14	10	0.46	88.7%	90%	Pos.	0.488x0.488x0.18 max 12.4x12.4x4.57 max
OKL-T/1-W12N-C	0.9-5.5	1	5	10	±0.25%	±0.25%	12	2.9-14	10	0.46	88.7%	90%	Neg.	0.488x0.488x0.18 max 12.4x12.4x4.57 max

Discontinued (OKL-T/1-W12P-C)
In Production (OKL-T/1-W12N-C)

① The output range is limited by Vin. (Vin x 0.8) ≥ Vout.

② All specifications are at nominal line voltage, Vout=nominal (5V for W12 models) and full load, +25 deg.C. unless otherwise noted.

Output capacitors are 10 µF ceramic. Input cap is 22 µF. See detailed specifications.

I/O caps are necessary for our test equipment and may not be needed for your application.

③ Use adequate ground plane and copper thickness adjacent to the converter.

④ Ripple and Noise (R/N) and no-load input current are shown at Vout=1V. See specs for details.

PART NUMBER STRUCTURE

OK L - T / 1 - W12 N - C

Non-isolated PoL

LGA Surface Mount

Trimmable Output Voltage Range
0.591–5.5Vdc

Maximum Rated Output
Current in Amps

Input Voltage Range
2.9–14Vdc

On/Off Logic
P = Positive Logic
N = Negative Logic

RoHS Hazardous Substance Compliance
C = RoHS (does not claim EU RoHS exemption 7b – lead in solder)
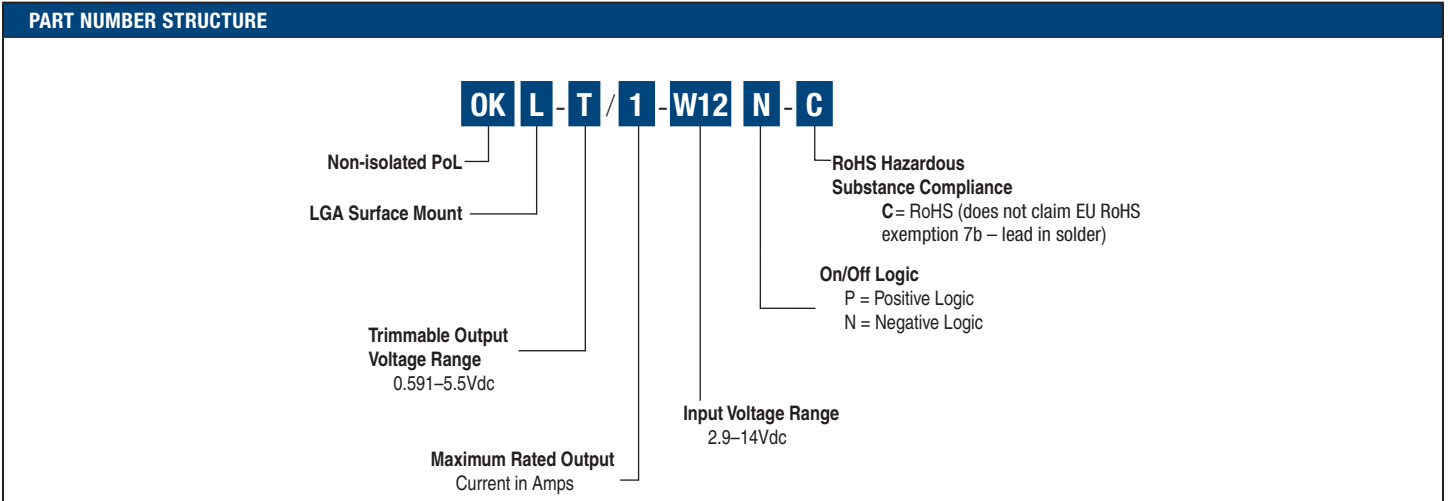
Product Label

Because of the small size of these products, the product label contains a character-reduced code to indicate the model number and manufacturing date code. Not all items on the label are always used. Please note that the label differs from the product photograph. Here is the layout of the label:

XXXXXX ← Product code
Mfg. date code → YMDX Rev. ← Revision level
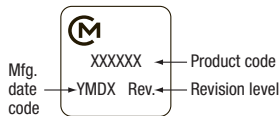
Figure 2. Label Artwork Layout

The label contains three rows of information:

First row – Murata Corporation mark
Second row – Model number product code (see table)
Third row – Manufacturing date code and revision level

Model Number	Product Code
OKL-T/1-W12P-C	L01101
OKL-T/1-W12N-C	L00101

The manufacturing date code is four characters:

First character – Last digit of manufacturing year, example 200**9**
Second character – Month code (1 through 9 = Jan-Sep; O, N, D = Oct, Nov, Dec)
Third character – Day code (1 through 9 = 1 to 9, 10 = 0 and 11 through 31 = A through Z)
Fourth character – Manufacturing information

Performance and Functional Specifications

See Note 1

Input	
Input Voltage Range	See Ordering Guide and Note 7.
Isolation	Not isolated
Start-Up Voltage	2.45 V
Undervoltage Shutdown (see Note 15)	2.4 V
Overvoltage Shutdown	None
Reflected (Back) Ripple Current (Note 2)	49 mA pk-pk
Internal Input Filter Type	Capacitive
Recommended External Fuse	2A
Reverse Polarity Protection	None, install external fuse
Input Current:	
Full Load Conditions	See Ordering Guide
Inrush Transient	0.16 A^2Sec.
Shutdown Mode (Off, UV, OT)	1 mA
Output in Short Circuit	5 mA
Low Line (Vin=Vmin)	0.78 A.
Remote On/Off Control (Note 5)	
Negative Logic	ON = Open pin or -0.2V to +0.3V. max. OFF = +1.2V min. to +Vin max.
Positive Logic	ON = Open pin (internally pulled up) or +1.2V to +Vin max. OFF = -0.3V to +0.8V. max. or ground
Current	0.250 mA

Output	
Output Power	5.15W max.
Output Voltage Range	See Ordering Guide
Minimum Loading	No minimum load
Accuracy (50% load, untrimmed)	±3 % of Vnominal
Voltage Output Range (Note 13)	See Ordering Guide
Overvoltage Protection (Note 16)	None
Temperature Coefficient	±0.02% per °C of Vout range
Ripple/Noise (20 MHz bandwidth)	See Ordering Guide and note 8
Line/Load Regulation	See Ordering Guide and note 10
Efficiency	See Ordering Guide
Maximum Capacitive Loading (Note 14)	
Cap-ESR=0.001 to 0.01 Ohms	200 µF
Cap-ESR >0.01 Ohms	1000 µF Min. cap = 0 µF
Current Limit Inception (Note 6) (98% of Vout setting, after warm up)	2.4 Amps
Short Circuit Mode	
Short Circuit Current Output	10 mA
Protection Method	Hiccup autorecovery upon overload removal. (Note 17)
Short Circuit Duration	Continuous, no damage (output shorted to ground)
Prebias Startup	Converter will start up if the external output voltage is less than Vnominal.

| Dynamic Characteristics	
Dynamic Load Response (50-100% load step, di/dt=1A/µSec)	200µSec max. to within ±2% of final value (Note 1)
Peak Deviation	150 mV
Start-Up Time (Vin on or On/Off to Vout regulated)	3.4 mSec for Vout=nominal (Vin On) 3.5 mSec for Vout=nominal (Remote On/Off)
Switching Frequency	800 KHz

Environmental	
Calculated MTBF (hours)	OKL-T/1-W12
Telecordia method (4a)	TBC
Calculated MTBF (hours)	
MIL-HDBK-217N2 method (4b)	TBC
Operating Temperature Range (Ambient, vertical mount) See derating curves	-40 to +85 °C. with derating (Note 9)
Storage Temperature Range	-55 to +125 °C.
Thermal Protection/Shutdown	None
MSL Rating	2
Relative Humidity	to 85%/+85 °C., non-condensing

Physical	
Outline Dimensions	See Mechanical Specifications
Weight	0.024 ounces (0.67 grams)
Plating Thickness	Gold overplate 1.18µ" (0.03µm) on Nickel subplate 118.1µ" (3.0µm)
Safety	Designed to meet UL/cUL 60950-1, CSA-C22.2 No. 60950-1, IEC 60950-1, 2nd Edition
Restriction of Hazardous Substances	RoHS (does not claim EU RoHS exemption 7b–lead in solder)

Absolute Maximum Ratings	
Input Voltage (Continuous or transient) On/Off Control	0 V. to +15 Volts max. 0 V. min. to +Vin max.
Input Reverse Polarity Protection	None, install external fuse
Output Current (Note 7)	Current-limited. Devices can withstand a sustained short circuit without damage. The outputs are not intended to accept appreciable reverse current.
Storage Temperature	-55 to +125 °C.
Lead Temperature	See soldering specifications

Absolute maximums are stress ratings. Exposure of devices to greater than any of any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended.

Specification Notes:

(1) Specifications are typical at +25 °C, Vin=nominal (+12V.), Vout=nominal (+5V), full load, external caps and natural convection unless otherwise indicated. Extended tests at full power must supply substantial forced airflow.

All models are tested and specified with external 10µF ceramic output capacitors and a 22 µF external input capacitor. All capacitors are low ESR types. These capacitors are necessary to accommodate our test equipment and may not be required to achieve specified performance in your applications. However, Murata Power Solutions recommends installation of these capacitors. All models are stable and regulate within spec under no-load conditions.

(2) Input Back Ripple Current is tested and specified over a 5 Hz to 20 MHz bandwidth. Input filtering is Cin=2 x 100 µF ceramic, Cbus=1000 µF electrolytic, Lbus=1 µH.

(3) Note that Maximum Power Derating curves indicate an average current at nominal input voltage. At higher temperatures and/or lower airflow, the DC/DC converter will tolerate brief full current outputs if the total RMS current over time does not exceed the Derating curve.

(4a) Mean Time Before Failure is calculated using the Telcordia (Belcore) SR-332 Method 1, Case 3, ground fixed conditions, Tpcboard=+25 °C, full output load, natural air convection.

(4b) Mean Time Before Failure is calculated using the MIL-HDBK-217N2 method, ground benign, +25ºC., full output load, natural convection.

(5) The On/Off Control Input should use either a switch or an open collector/open drain transistor referenced to -Input Common. A logic gate may also be used by applying appropriate external voltages which do not exceed +Vin.

(6) Short circuit shutdown begins when the output voltage degrades approximately 2% from the selected setting.

Specification Notes, Cont.:

(7) Please observe the voltage input and output specifications in the Voltage Range Graph on page 7.

(8) Output noise may be further reduced by adding an external filter. At zero output current, the output may contain low frequency components which exceed the ripple specification. The output may be operated indefinitely with no load. Due to the minimum duty cycle of the controller, operation below Vout=1.8V. with Vin at or above 12V. may cause pulse skipping. This results in higher output ripple which may require additional external output filtering.

(9) All models are fully operational and meet published specifications, including "cold start" at –40˚ C.

(10) Regulation specifications describe the deviation as the line input voltage or output load current is varied from a nominal midpoint value to either extreme.

(11) Other input or output voltage ranges will be reviewed under scheduled quantity special order.

(12) Maximum PC board temperature is measured with the sensor in the center of the converter.

(13) Do not exceed maximum power specifications when adjusting the output trim.

(14) The maximum output capacitive loads depend on the the Equivalent Series Resistance (ESR) of the external output capacitor and, to a lesser extent, the distance and series impedance to the load. Larger caps will reduce output noise but may change the transient response. Newer ceramic caps with very low ESR may require lower capacitor values to avoid instability. Thoroughly test your capacitors in the application. Please refer to the Output Capacitive Load Application Note.

(15) Do not allow the input voltage to degrade lower than the input undervoltage shutdown voltage at all times. Otherwise, you risk having the converter turn off. The undervoltage shutdown is not latching and will attempt to recover when the input is brought back into normal operating range.

(16) The outputs are not intended to sink appreciable reverse current.

(17) "Hiccup" overcurrent operation repeatedly attempts to restart the converter with a brief, full-current output. If the overcurrent condition still exists, the restart current will be removed and then tried again. This short current pulse prevents overheating and damaging the converter. Once the fault is removed, the converter immediately recovers normal operation.

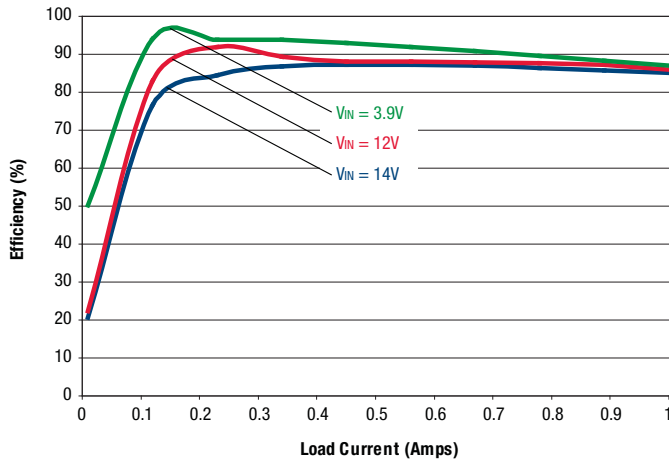## OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Efficiency vs. Line Voltage and Load Current @Ta = +25 °C
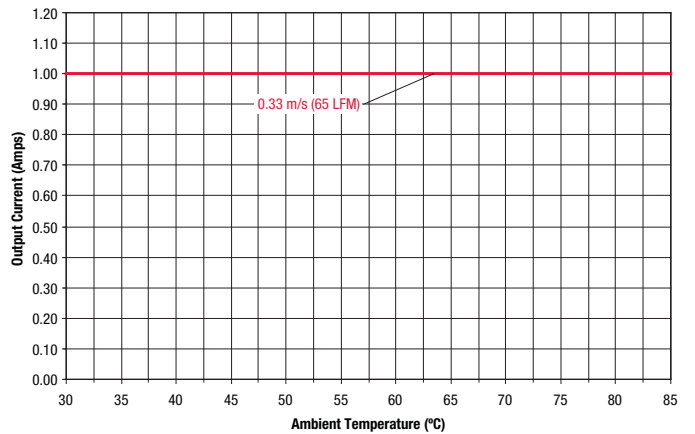(Vout = 5V)
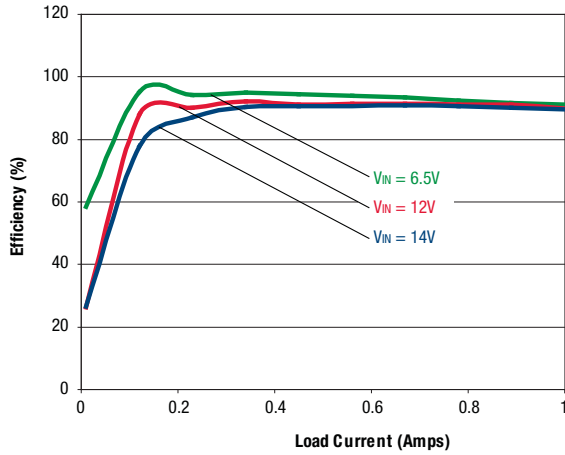
Maximum Current Temperature Derating at Sea Level
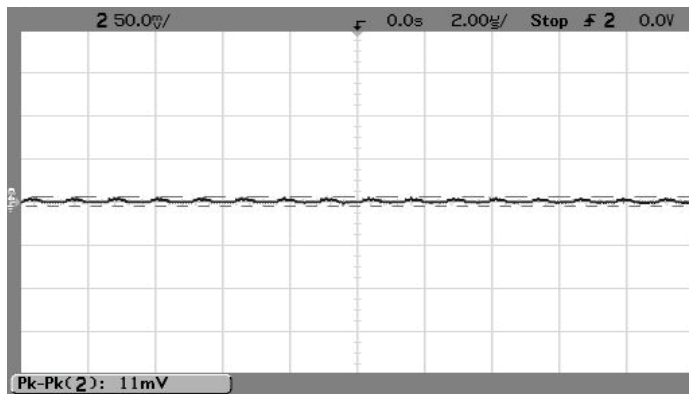(Vin = 6V, Vout = 5V, airflow is from pad 10 to pad 1)
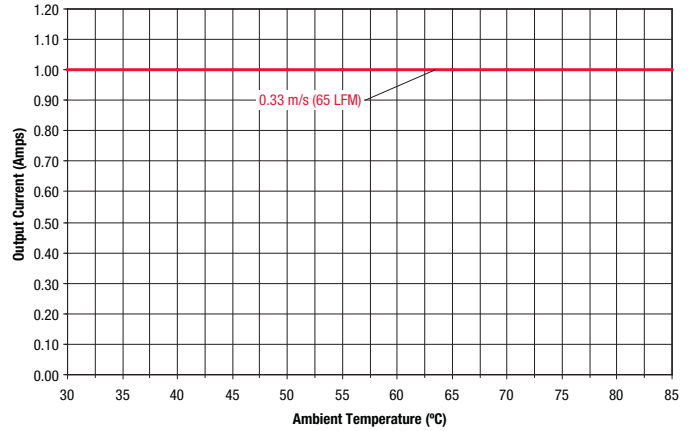
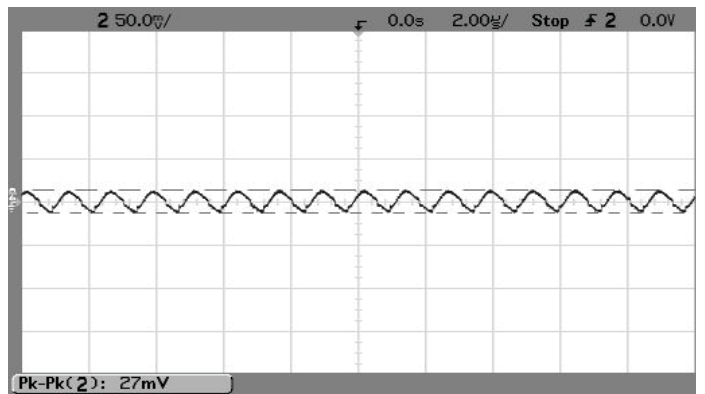Efficiency vs. Line Voltage and Load Current @Ta = +25 °C
(Vout = 3.3V)

Maximum Current Temperature Derating at Sea Level
(Vin = 6.5V to 16V, airflow is from pad 10 to pad 1)

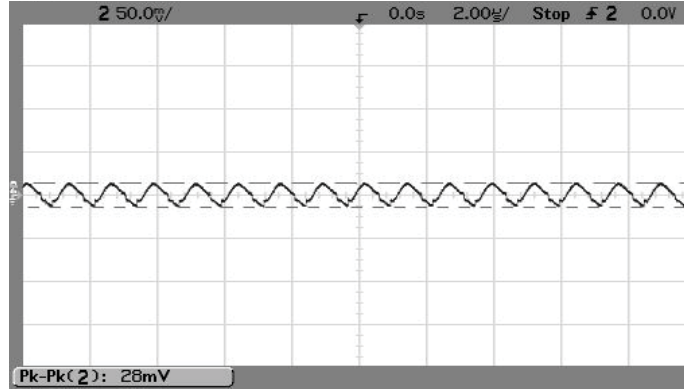Output Ripple and Noise (Vin=4.5V, Vout=3.3V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

Output Ripple and Noise (Vin=12V, Vout=3.3V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

Murata Power Solutions

OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Output Ripple and Noise (Vin=14V, Vout=3.3V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

Step Load Transient Response (Vin=4.5V, Vout=3.3V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.)Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

Step Load Transient Response (Vin=4.5V, Vout=3.3V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
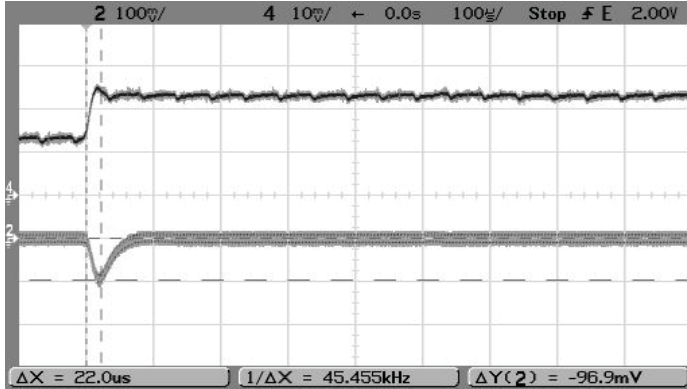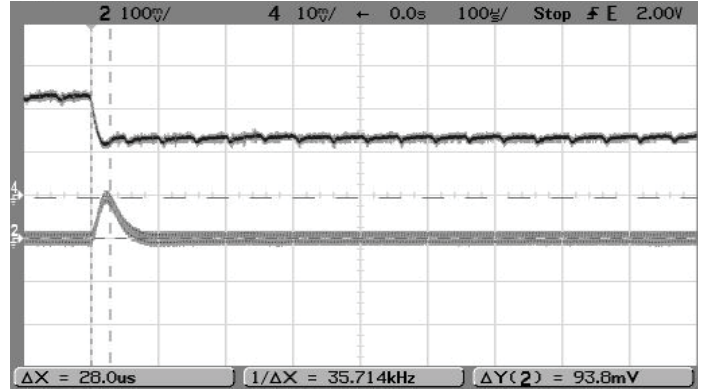

Step Load Transient Response (Vin=12V, Vout=3.3V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

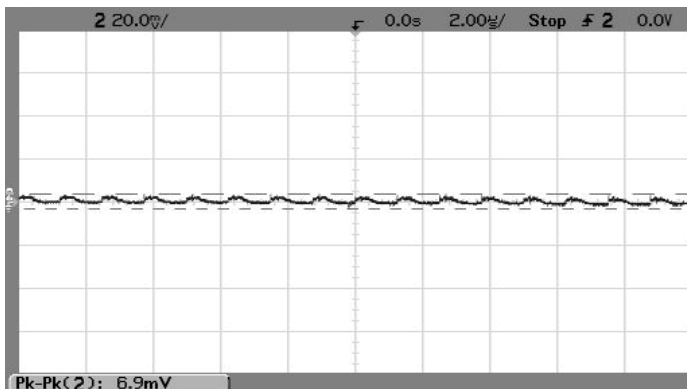
Step Load Transient Response (Vin=12V, Vout=3.3V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

www.murata-ps.com/support

Murata Power Solutions

OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Step Load Transient Response (Vin=14V, Vout=3.3V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

Step Load Transient Response (Vin=14V, Vout=3.3V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
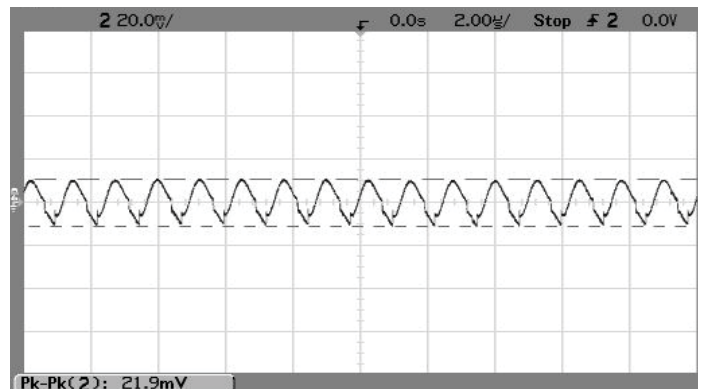

Efficiency vs. Line Voltage and Load Current @Ta = +25 °C (Vout = 2.5V)
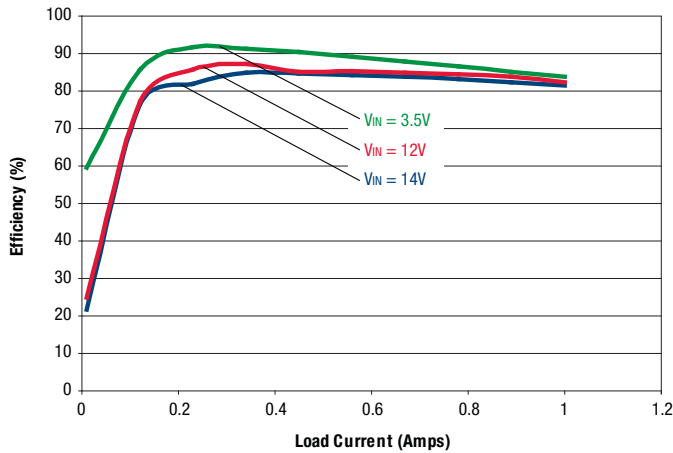
Maximum Current Temperature Derating at Sea Level (Vin = 4V, Vout = 2.5V)
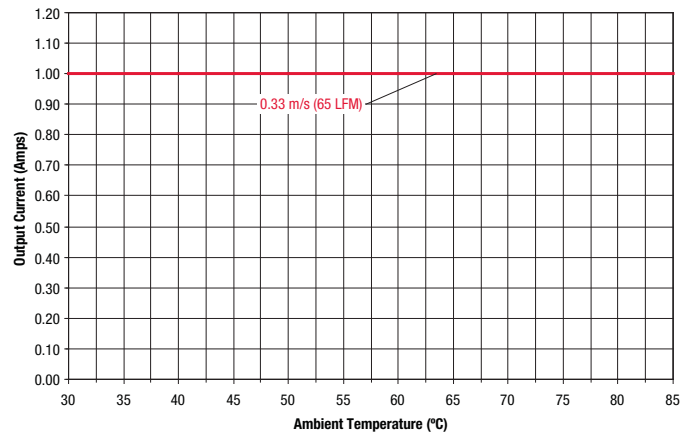
Output Ripple and Noise (Vin=3.5V, Vout=2.5V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

Output Ripple and Noise (Vin=12V, Vout=2.5V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

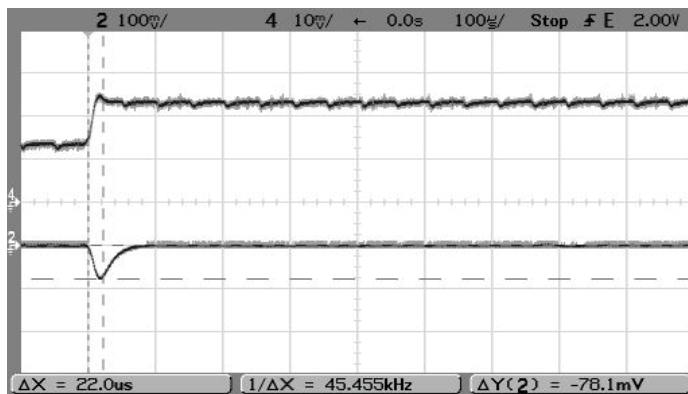**www.murata-ps.com/support**

Murata Power Solutions
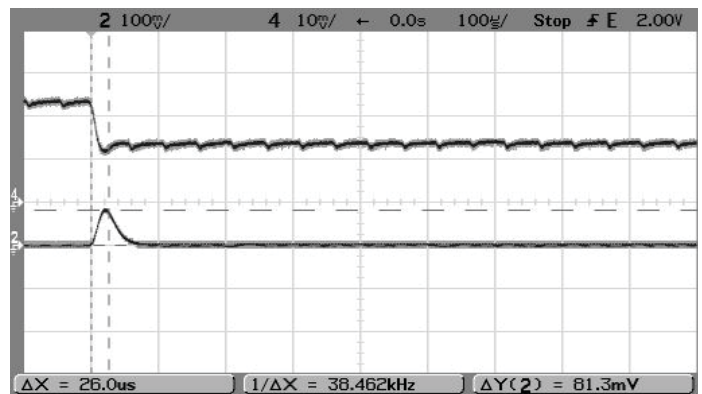
OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Output Ripple and Noise (Vin=14V, Vout=2.5V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)

Step Load Transient Response (Vin=3.5V, Vout=2.5V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

Step Load Transient Response (Vin=3.5V, Vout=2.5V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
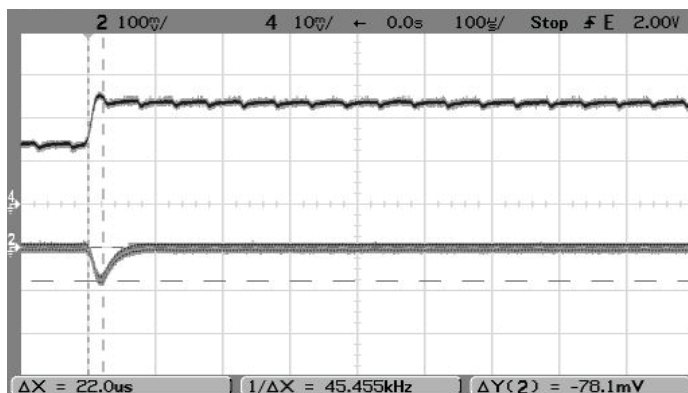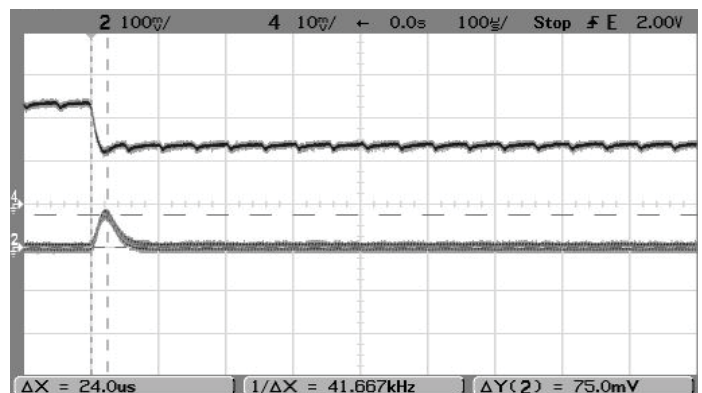

Step Load Transient Response (Vin=12V, Vout=2.5V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
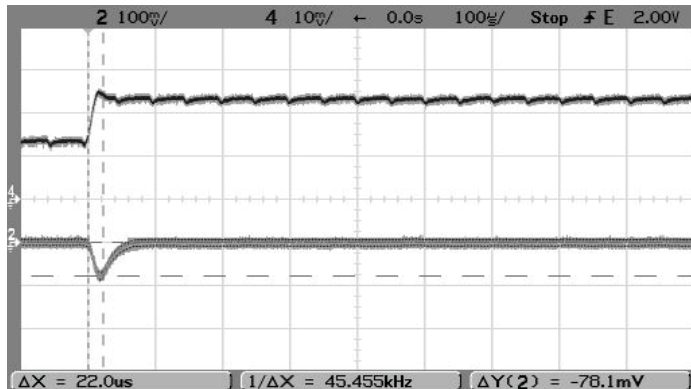

Step Load Transient Response (Vin=12V, Vout=2.5V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
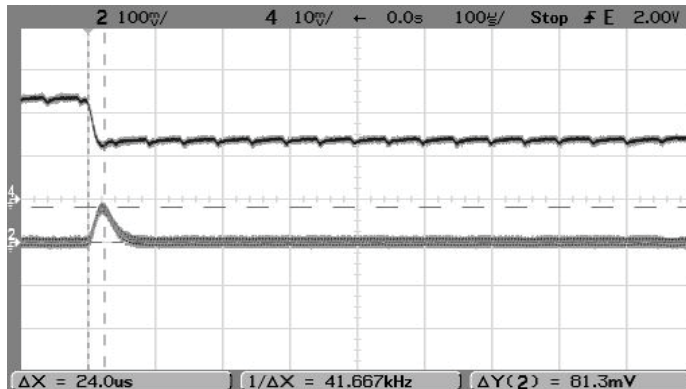

www.murata-ps.com/support

muRata Ps Murata Power Solutions

OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Step Load Transient Response (Vin=14V, Vout=2.5V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

ΔX = 22.0us   1/ΔX = 45.455kHz   ΔY(2) = -78.1mV

Step Load Transient Response (Vin=14V, Vout=2.5V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

ΔX = 24.0us   1/ΔX = 41.667kHz   ΔY(2) = 81.3mV

Efficiency vs. Line Voltage and Load Current @Ta = +25 °C
(Vout = 1.8V)

Maximum Current Temperature Derating at Sea Level
(Vin = 4V, Vout = 1.8V)
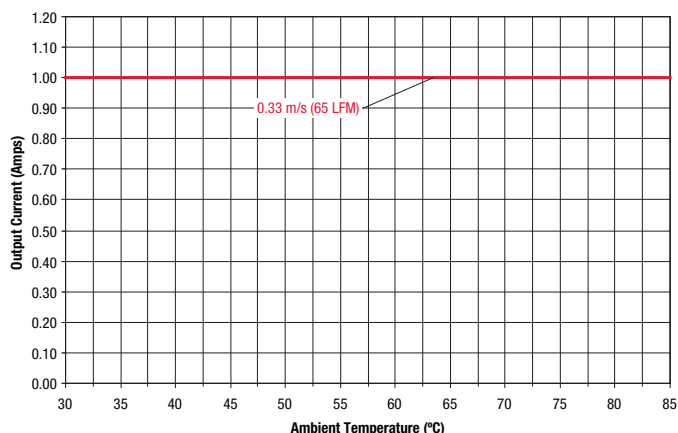
Efficiency vs. Line Voltage and Load Current @Ta = +25 °C
(Vout = 1.5V)

Murata Power Solutions

OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Output Ripple and Noise (Vin=2.6V, Vout=0.9V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)
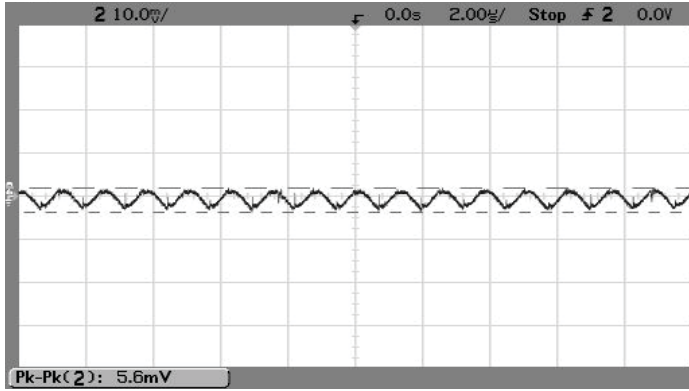
Output Ripple and Noise (Vin=12V, Vout=0.9V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)
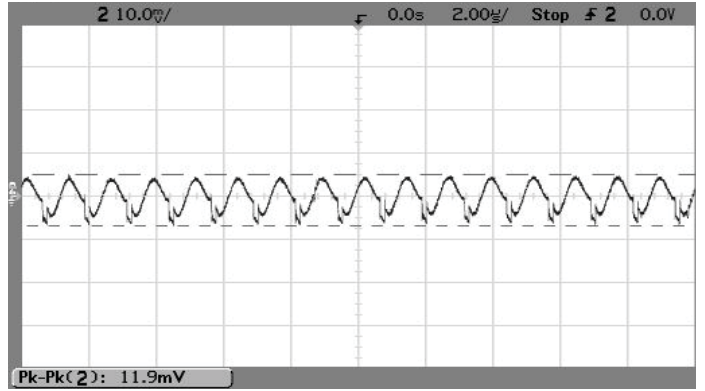
Output Ripple and Noise (Vin=14V, Vout=0.9V, Iout=1A, Cload=0, Ta=+25°C., ScopeBW=100MHz)
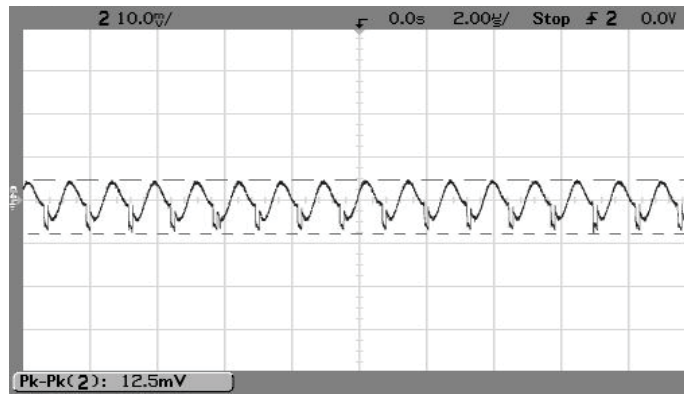
Step Load Transient Response (Vin=2.6V, Vout=0.9V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
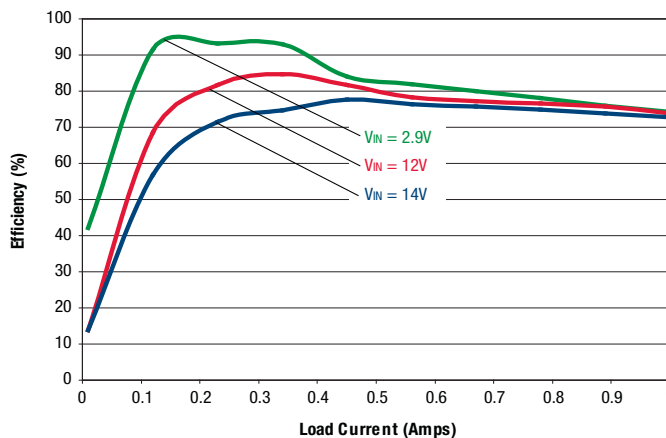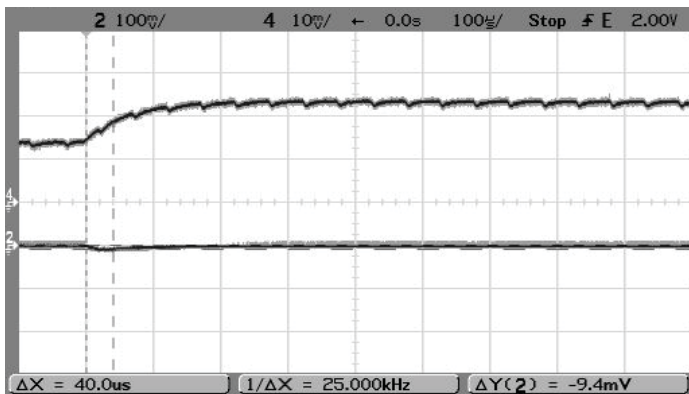

Step Load Transient Response (Vin=2.6V, Vout=0.9V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

OKL-T/1-W12 PERFORMANCE DATA AND OSCILLOGRAMS

Step Load Transient Response (Vin=12V, Vout=0.9V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
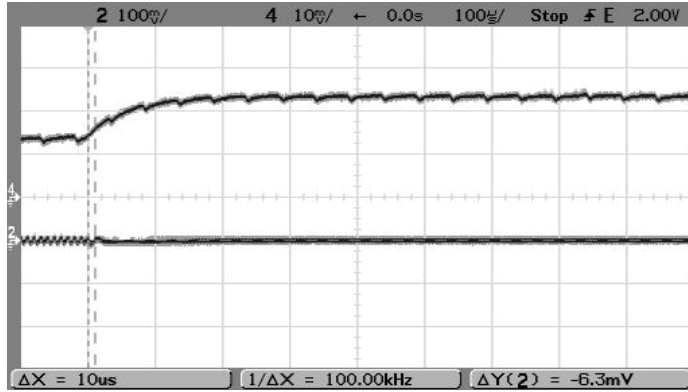
Step Load Transient Response (Vin=12V, Vout=0.9V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.
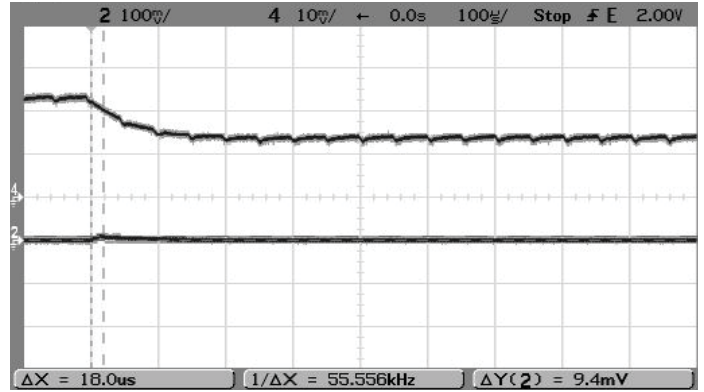
Step Load Transient Response (Vin=14V, Vout=0.9V, Cload=0, Iout=0.5A to 1A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

Step Load Transient Response (Vin=14V, Vout=0.9V, Cload=0, Iout=1A to 0.5A, Ta=+25°C.) Trace 2=Vout, 100 mV/div. Trace 4=Iout, 0.5A/div.

MECHANICAL SPECIFICATIONS

Top View

Side View

Bottom View
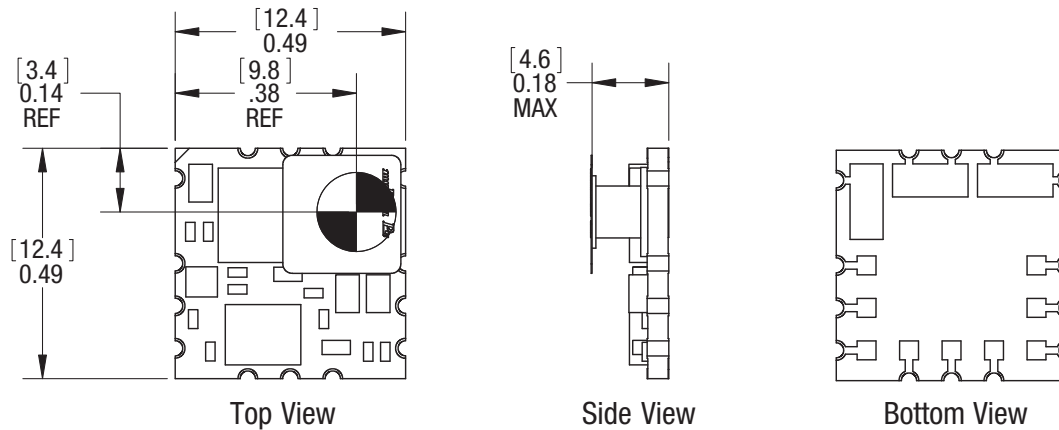
Recommended centroid location shown;
alternate locations may be selected by the end user.

SMT (iLGA Surface Mount Package) (MSL Rating 2)
Pinouts and Pin Locations
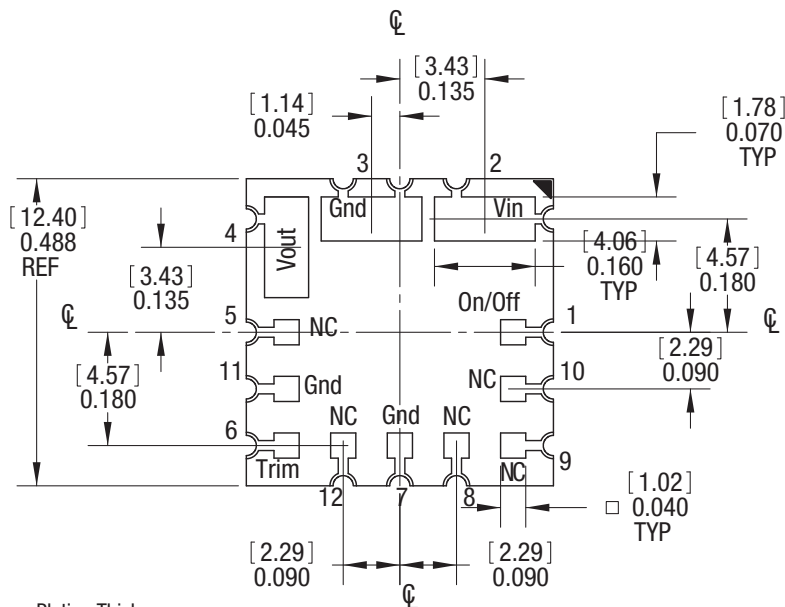
INPUT/OUTPUT CONNECTIONS	
Solder Pad	**Function**
1	On/Off Control *
2	Vin
3	Ground
4	Vout
5	No Connection **
6	Trim
7	Ground
8	No Connection**
9	No Connection **
10	No Connection**
11	Ground
12	No Connection**

* The Remote On/Off can be provided with either positive (P suffix) or negative (N suffix) logic.

** No Connection pins should not be connected to anything and should be left floating.
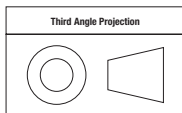
Plating Thickness:
Gold overplate 1.18µ'' (0.03µm)
on Nickel subplate 118.1µ'' (3.0µm)

Dimensions are in inches (mm shown for ref. only).

Third Angle Projection

Tolerances (unless otherwise specified):
.XX ± 0.02 (0.5)
.XXX ± 0.010 (0.25)
Angles ± 1˚

Components are shown for reference only.

Figure 3. OKL-T/1-W12 Mechanical Outline

MECHANICAL SPECIFICATIONS, Continued

Recommended Footprint
(Through the Board)
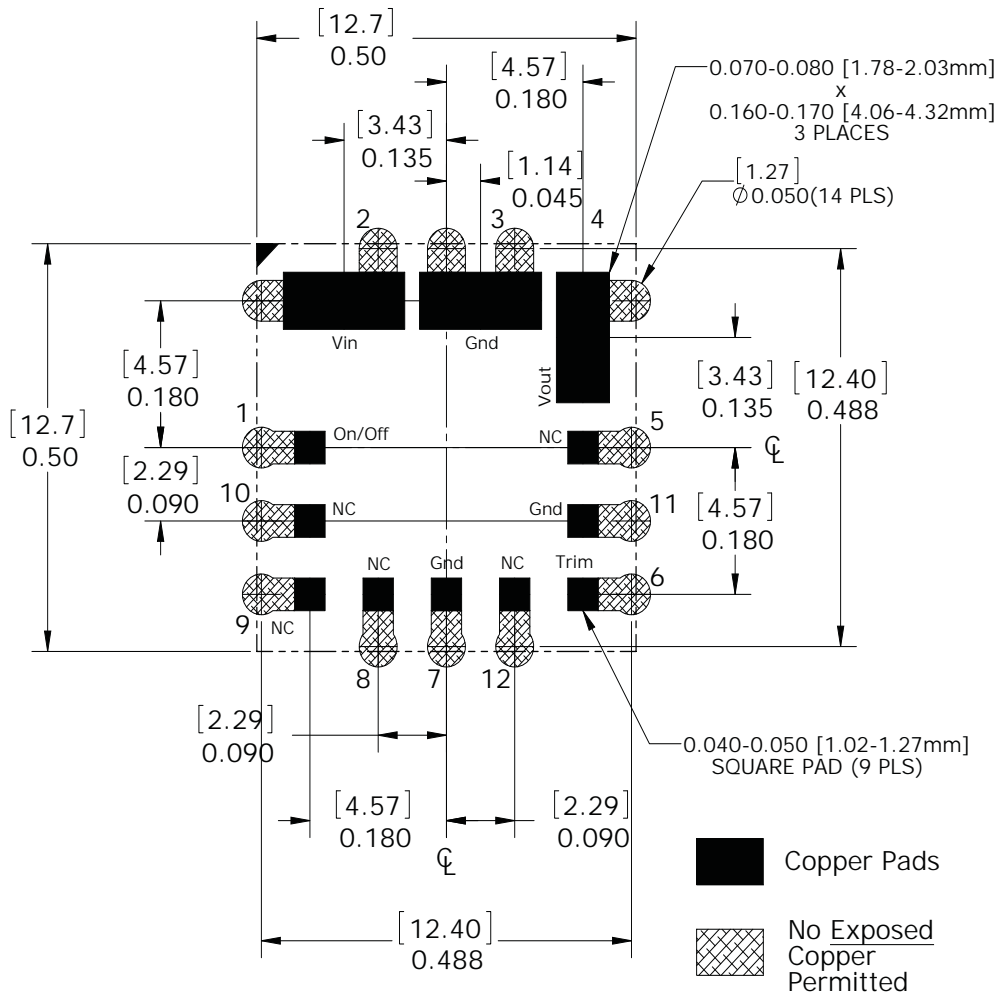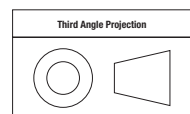
[12.7]
0.50

[4.57]
0.180

[3.43]
0.135

[1.14]
0.045

0.070-0.080 [1.78-2.03mm]
x
0.160-0.170 [4.06-4.32mm]
3 PLACES

[1.27]
⌀ 0.050(14 PLS)

2   3   4

Vin   Gnd

Vout

[4.57]
0.180

[12.7]
0.50

[2.29]
0.090

1   On/Off   NC   5

[3.43]
0.135

[12.40]
0.488

10   NC   Gnd   11

[4.57]
0.180

NC   Gnd   NC   Trim

9   NC   6

8   7   12

[2.29]
0.090

[4.57]
0.180

[2.29]
0.090

0.040-0.050 [1.02-1.27mm]
SQUARE PAD (9 PLS)

[12.40]
0.488

Copper Pads

No Exposed
Copper
Permitted

Dimensions are in inches (mm shown for ref. only).

Third Angle Projection

Tolerances (unless otherwise specified):
.XX ± 0.02 (0.5)
.XXX ± 0.010 (0.25)
Angles ± 1˚

Components are shown for reference only.

TAPE AND REEL INFORMATION (MSL RATING 2)

[330.20]
⌀13.000

FEED (UNWIND) DIRECTION →

[10.44]
0.411

[5.00]
0.197
REF

[5.00]
0.197
REF

[0.77]
0.030

[4.00]
0.157

[2.00]
0.079

[1.50]
⌀0.059 TYP

[1.75]
0.069

[24.00]
0.945

[13.41]
0.528

[4.00]
⌀0.157 TYP.

[16.00]
0.63 PITCH

Contents: 800 units per reel

Recommended centroid location shown; alternate locations may be selected by the end user.

TOP COVER TAPE

[5.00]
0.197
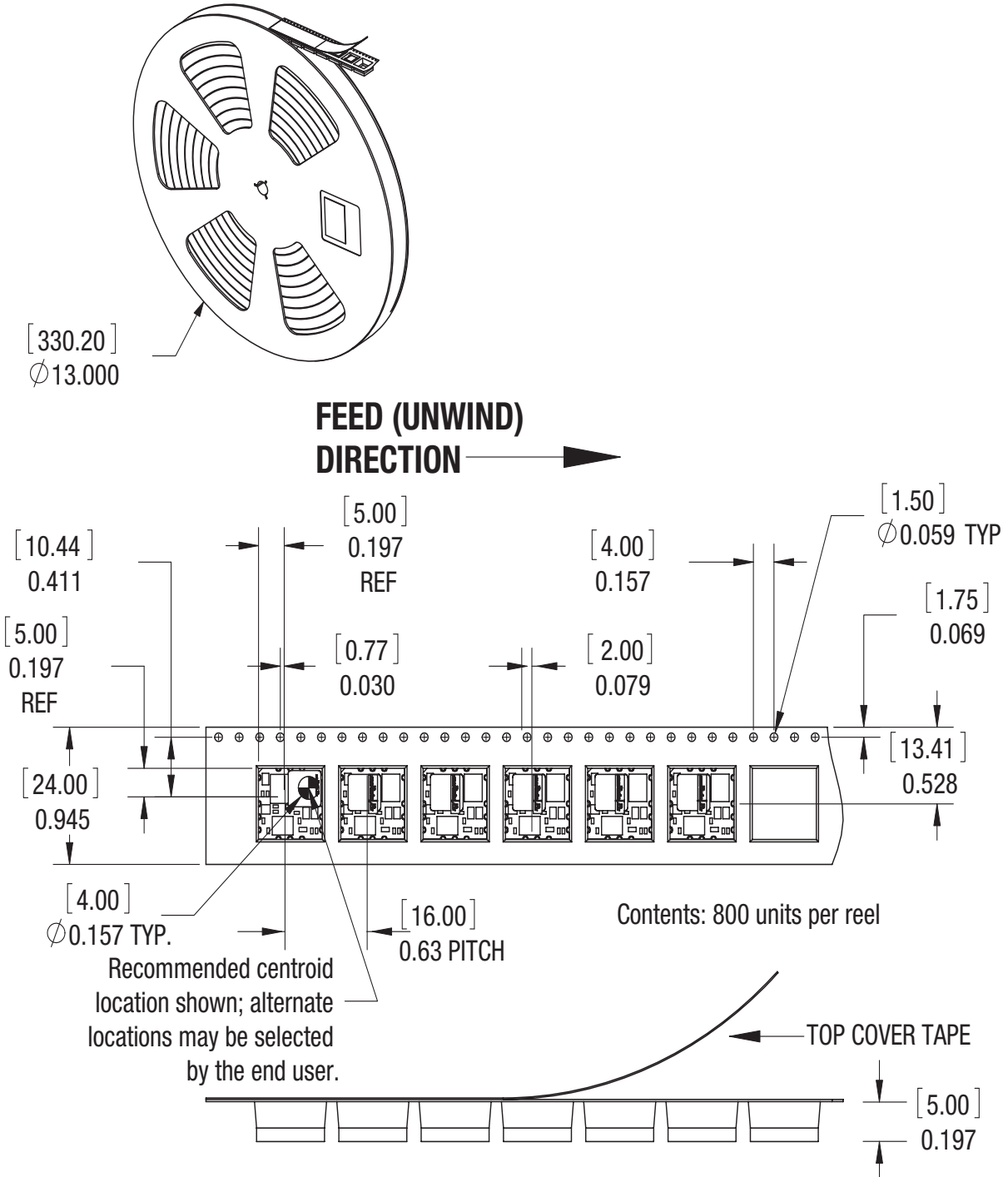
Dimensions in inches [mm]

Murata Power Solutions

TECHNICAL NOTES

Output Voltage Adjustment

The output voltage may be adjusted over a limited range by connecting an external trim resistor (Rtrim) between the Trim pin and Ground. The Rtrim resistor is recommended to have a ±0.5% accuracy (or better) with low temperature coefficient, ±100 ppm/°C or better. Mount the resistor close to the converter with very short leads or use a surface mount trim resistor.

In the tables below, the calculated resistance is given. Do not exceed the specified limits of the output voltage or the converter's maximum power rating when applying these resistors. Also, avoid high noise at the Trim input. However, to prevent instability, you should never connect any capacitors to Trim.

OKL-T/1-W12

Output Voltage	Calculated Rtrim (KΩ)
5.0 V.	2.18
3.3 V.	3.721
2.5 V.	5.576
1.8 V.	9.889
1.5 V.	14.793
1.2 V.	29.394
1.0 V.	85.238
0.895 V.	∞ (open)

Resistor Trim Equation, OKL-T/1-W12 models:

R_{TRIM} (kΩ) = 10 ÷ ((V_{out} ÷ 0.895) −1) where V_{out} = desired output voltage

Do not connect any additional components between the Vtrim pin and Vout or between the Trim and Sense pins. Use only the specified connections as recommended per this data sheet.

Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

The installer must observe all relevant safety standards and regulations. For safety agency approvals, install the converter in compliance with the end-user safety standard, i.e. IEC/EN/UL 60950-1.

Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, converters will not begin to regulate properly until the ramping-up input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage.

Users should be aware however of input sources near the Under-Voltage Shutdown whose voltage decays as input current is consumed (such as

capacitor inputs), the converter shuts off and then restarts as the external capacitor recharges. Such situations could oscillate. To prevent this, make sure the operating input voltage is well above the UV Shutdown voltage AT ALL TIMES.

Start-Up Time

Assuming that the output current is set at the rated maximum, the Vin to Vout Start-Up Time (see Specifications) is the time interval between the point when the ramping input voltage crosses the Start-Up Threshold and the fully loaded regulated output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, input voltage slew rate and final value of the input voltage as it appears at the converter.

These converters include a soft start circuit to moderate the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout regulated assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The interval is measured from the On command until the output enters and remains within its specified accuracy band. The specification assumes that the output is fully loaded at maximum rated current. Similar conditions apply to the On to Vout regulated specification such as external load capacitance and soft start circuitry.

Recommended Input Filtering

The user must assure that the input source has low AC impedance to provide dynamic stability and that the input supply has little or no inductive content, including long distributed wiring to a remote power supply. The converter will operate with no additional external capacitance if these conditions are met.

For best performance, we recommend installing a low-ESR capacitor immediately adjacent to the converter's input terminals. The capacitor should be a ceramic type such as the Murata GRM32 series or a polymer type. Initial suggested capacitor values are 10 to 22 µF, rated at twice the expected maximum input voltage. Make sure that the input terminals do not go below the undervoltage shutdown voltage at all times. More input bulk capacitance may be added in parallel (either electrolytic or tantalum) if needed.

Recommended Output Filtering

The converter will achieve its rated output ripple and noise with no additional external capacitor. However, the user may install more external output capacitance to reduce the ripple even further or for improved dynamic response. Again, use low-ESR ceramic (Murata GRM32 series) or polymer capacitors. Initial values of 10 to 47 µF may be tried, either single or multiple capacitors in parallel. Mount these close to the converter. Measure the output ripple under your load conditions.

Use only as much capacitance as required to achieve your ripple and noise objectives. Excessive capacitance can make step load recovery sluggish or possibly introduce instability. Do not exceed the maximum rated output capacitance listed in the specifications.

![Murata Power Solutions logo] **muRata** **Murata Power Solutions**

Input Ripple Current and Output Noise

All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. The Cbus and Lbus components simulate a typical DC voltage bus. Please note that the values of Cin, Lbus and Cbus will vary according to the specific converter model.
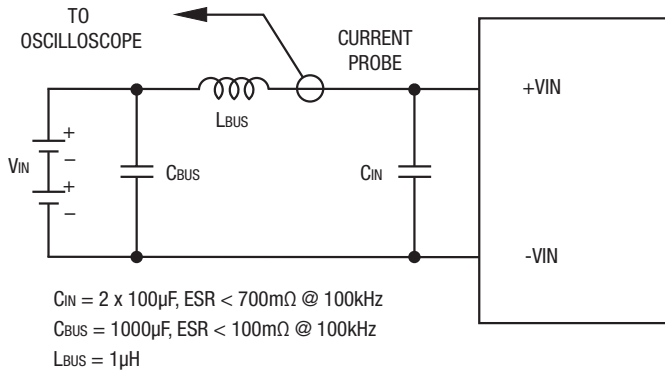

C_{IN} = 2 x 100μF, ESR < 700mΩ @ 100kHz
C_{BUS} = 1000μF, ESR < 100mΩ @ 100kHz
L_{BUS} = 1μH

Figure 4. Measuring Input Ripple Current

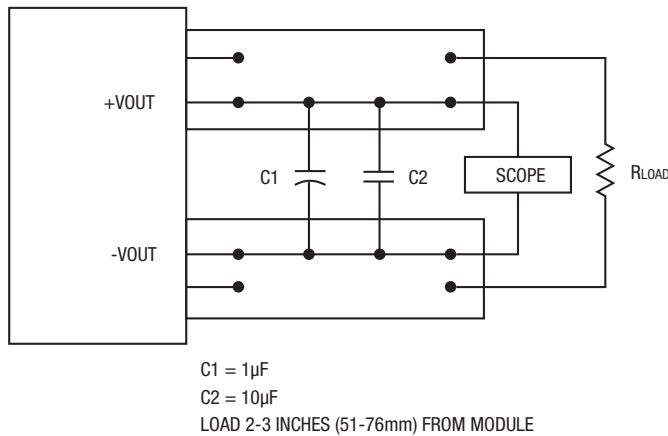C1 = 1μF
C2 = 10μF
LOAD 2-3 INCHES (51-76mm) FROM MODULE

Figure 5. Measuring Output Ripple and Noise (PARD)

Minimum Output Loading Requirements

All models regulate within specification and are stable under no load to full load conditions. Operation under no load might however slightly increase output ripple and noise.

Temperature Derating Curves

The graphs in this data sheet illustrate typical operation under a variety of conditions. The Derating curves show the maximum continuous ambient air temperature and decreasing maximum output current which is acceptable under increasing forced airflow measured in Linear Feet per Minute ("LFM"). Note that these are AVERAGE measurements. The converter will accept brief increases in current or reduced airflow as long as the average is not exceeded.

Note that the temperatures are of the ambient airflow, not the converter itself which is obviously running at higher temperature than the outside air. Also note that very low flow rates (below about 25 LFM) are similar to "natural convection", that is, not using fan-forced airflow.

Murata Power Solutions makes Characterization measurements in a closed cycle wind tunnel with calibrated airflow. We use both thermocouples and an infrared camera system to observe thermal performance.

CAUTION: These graphs are all collected at slightly above Sea Level altitude. Be sure to reduce the derating for higher density altitude.

Output Current Limiting

Current limiting inception is defined as the point at which full power falls below the rated tolerance. See the Performance/Functional Specifications. Note particularly that the output current may briefly rise above its rated value in normal operation as long as the average output power is not exceeded. This enhances reliability and continued operation of your application. If the output current is too high, the converter will enter the short circuit condition.

Output Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. Following a time-out period, the PWM will restart, causing the output voltage to begin ramping up to its appropriate value. If the short-circuit condition persists, another shutdown cycle will initiate. This rapid on/off cycling is called "hiccup mode". The hiccup cycling reduces the average output current, thereby preventing excessive internal temperatures and/or component damage. A short circuit can be tolerated indefinitely.

The "hiccup" system differs from older latching short circuit systems because you do not have to power down the converter to make it restart. The system will automatically restore operation as soon as the short circuit condition is removed.

Remote On/Off Control

The OKL Series power modules can be specified with either a positive or negative logic type. See Figures 7 and 8 for On/Off circuit control. In the positive logic on/off option the unit turns on during a logic high on the On/Off pin and turns off during a logic low. In a negative logic on/off option, the unit turns off during logic high and on during logic low. The On/Off signal should always be reference to ground. For positive or negative option, leaving then On/Off pin disconnected will turn the unit on when input voltage is present.

Positive—Units are enabled when the on/off pin is left open or is pulled high to +Vin. The On/Off circuit control is shown in figure 7. When the external transistor Q1 is in the off state, the internal PWM enable pin is pull high causing the unit to turn on. When Q1 is turn on, the On/Off pin is pulled low and the units is off. Rp should be around 20K ohms.

Negative—Units are enabled when the ON/Off is open or brought to within a low voltage (see specifications) with respect to –Vin. The unit is off when the ON/Off is pulled high with respect to –Vin (see specifications). The On/Off circuitry is shown in figure 8. The On/Off pin should be pulled high with an external pull-up resistor (20K ohms). When Q1 is in the off state, the On/Off pin is pulled high, transistor Q3 is turn on and the unit is off. To turn on the unit, Q1 is turn on, pulling the On/Off pin low and turning Q3 off resulting on the unit being on.

Dynamic control of the On/Off function should be able to sink the specified signal current when brought low and withstand appropriate voltage when brought high. Be aware too that there is a finite time in milliseconds (see specifications) between the time of On/Off Control activation and stable, regulated output. This time will vary slightly with output load type and current and input conditions.

Output Capacitive Load

These converters do not require external capacitance added to achieve rated specifications. Users should only consider adding capacitance to reduce switching noise and/or to handle spike current load steps. Install only enough capacitance to achieve noise objectives. Excess external capacitance may cause regulation problems, degraded transient response and possible oscillation or instability.

Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ therefore please thoroughly review these guidelines with your process engineers.

| Reflow Solder Operations for surface-mount products (SMT)	
For Sn/Ag/Cu based solders:	
Preheat Temperature	Less than 1 ºC. per second
Time over Liquidus	45 to 75 seconds
Maximum Peak Temperature	260 ºC.
Cooling Rate	Less than 3 ºC. per second
For Sn/Pb based solders:	
Preheat Temperature	Less than 1 ºC. per second
Time over Liquidus	60 to 75 seconds
Maximum Peak Temperature	235 ºC.
Cooling Rate	Less than 3 ºC. per second

BOM → Rp → 20K
BOM → Q1 → Q SMT MOS P 30V
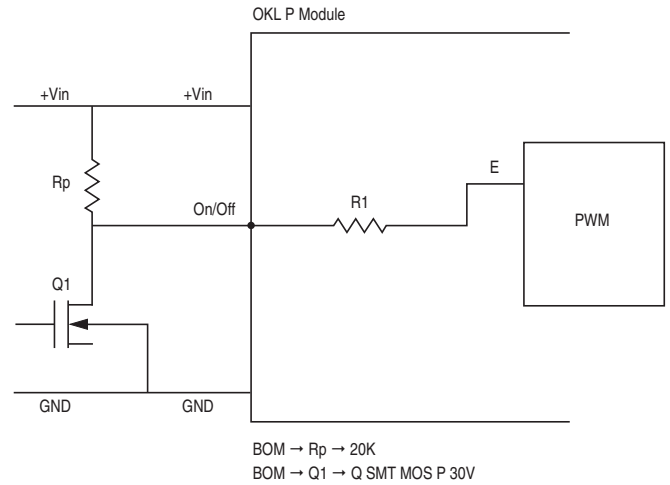
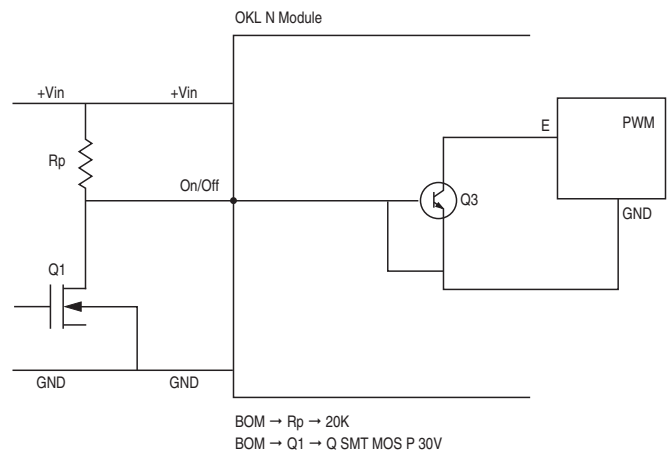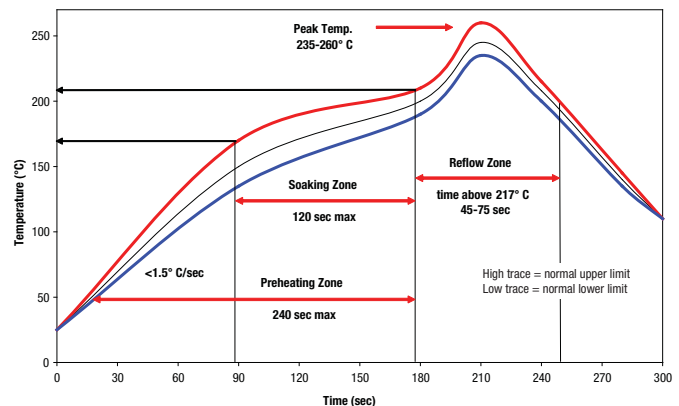Figure 6. On/Off Circuit Control for Using Positive On/Off Logic

BOM → Rp → 20K
BOM → Q1 → Q SMT MOS P 30V

Figure 7. On/Off Circuit Control for Using Negative On/Off Logic

Recommended Lead-free Solder Reflow Profile
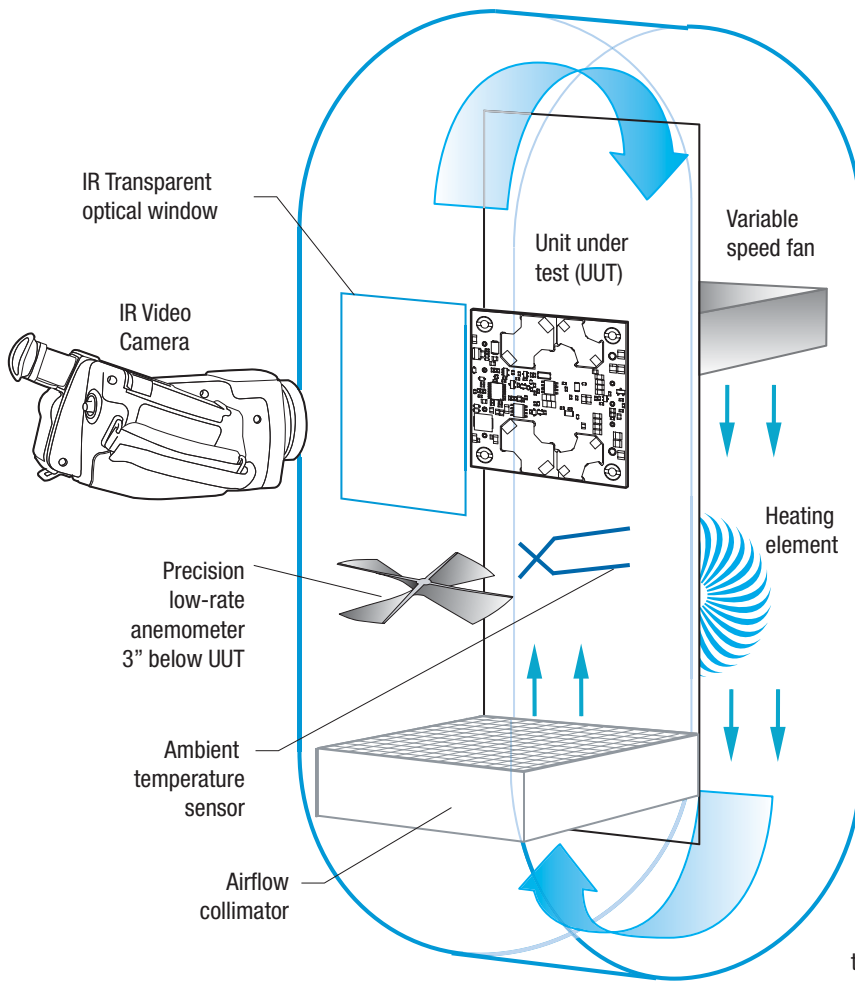
Murata Power Solutions

Figure 8. Vertical Wind Tunnel

Vertical Wind Tunnel

Murata Power Solutions employs a computer controlled custom-designed closed loop vertical wind tunnel, infrared video camera system, and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges, and adjustable heating element.

The IR camera monitors the thermal performance of the Unit Under Test (UUT) under static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths.

Both through-hole and surface mount converters are soldered down to a host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of adjustable airflow, adjustable ambient heat, and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The collimator reduces the amount of turbulence adjacent to the UUT by minimizing airflow turbulence. Such turbulence influences the effective heat transfer characteristics and gives false readings. Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges, and no-contact IR camera mean that power supplies are tested in real-world conditions.

This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy: Refer to: http://www.murata-ps.com/requirements/

www.murata-ps.com/support