

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ Max $T_C = +25^\circ C$
60V	17m $\Omega$ @ $V_{GS} = 10V$	46.9A
	24m $\Omega$ @ $V_{GS} = 4.5V$	38.3A

## Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

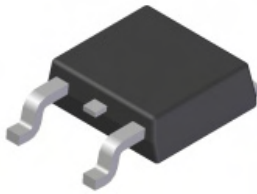
- Power Management Functions
- DC-DC Converters
- Backlighting

## Features

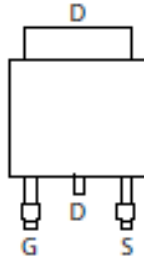
- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Low  $R_{DS(ON)}$  – Ensures On State Losses Are Minimized
- Excellent  $Q_{gd} \times R_{DS(ON)}$  Product (FOM)
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

## Mechanical Data

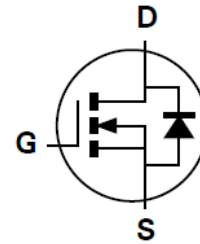
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208  $\text{e3}$
- Weight: 0.33 grams (Approximate)



Top View



Pin Out Top View



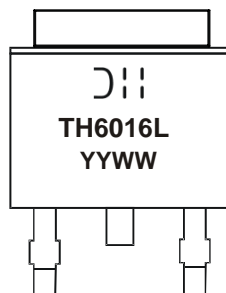
Equivalent Circuit

## Ordering Information (Note 5)

Part Number	Case	Packaging
DMTH6016LK3Q-13	TO252 (DPAK)	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Please refer to <https://www.diodes.com/quality/>.
  5. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



**DII** = Manufacturer's Marking  
**TH6016L** = Product Type Marking Code  
**YYWW** = Date Code Marking  
**YY** = Last Two Digits of Year (ex: 18 = 2018)  
**WW** = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	I <sub>D</sub>	T <sub>A</sub> = +25°C T <sub>A</sub> = +100°C	10.8 7.6	A
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V		T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	46.9 33.2	A
Maximum Continuous Body Diode Forward Current (Note 7)	I <sub>S</sub>	50	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	70	A	
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	15.3	A	
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	11.7	mJ	

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P <sub>D</sub>	3.2	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	47	°C/W
Total Power Dissipation (Note 7)	P <sub>D</sub>	60	W
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	2.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	11	17	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A
		—	16	24	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6A
Diode Forward Voltage	V <sub>SD</sub>	—	0.9	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	864	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	282	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	27.1	—		
Gate Resistance	R <sub>g</sub>	—	1.35	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	17	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 10A
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	8.4	—		
Gate-Source Charge	Q <sub>gs</sub>	—	3.1	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	4.3	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	3.4	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, R <sub>G</sub> = 6Ω, I <sub>D</sub> = 10A
Turn-On Rise Time	t <sub>R</sub>	—	5.2	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	12.9	—		
Turn-Off Fall Time	t <sub>F</sub>	—	6.8	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	22	—	ns	I <sub>F</sub> = 10A, di/dt = 400A/µs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	11.1	—	nC	

- Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.  
7. Device mounted on infinite heat sink and measured by thermal couple attached on bottom heat sink of package.  
8. Short duration pulse test used to minimize self-heating effect.  
9. Guaranteed by design. Not subject to product testing.

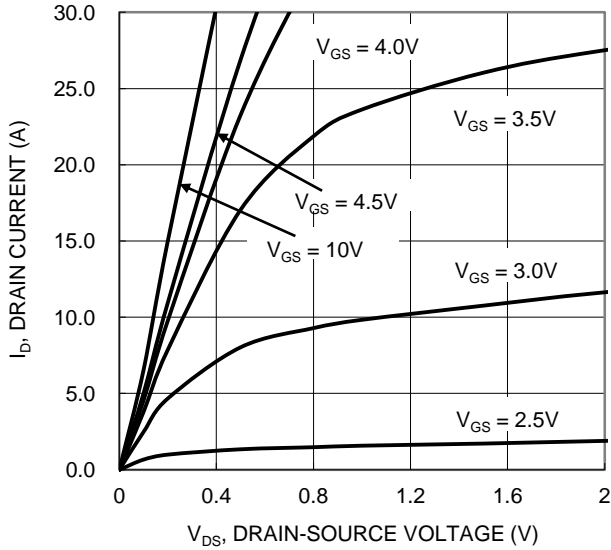


Figure 1. Typical Output Characteristic

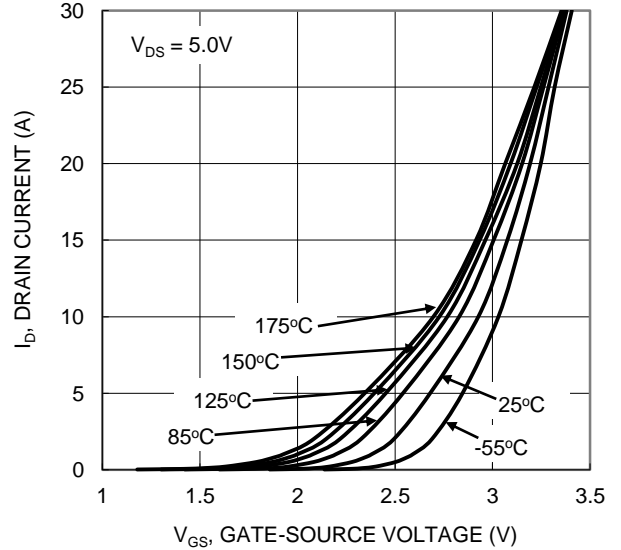


Figure 2. Typical Transfer Characteristic

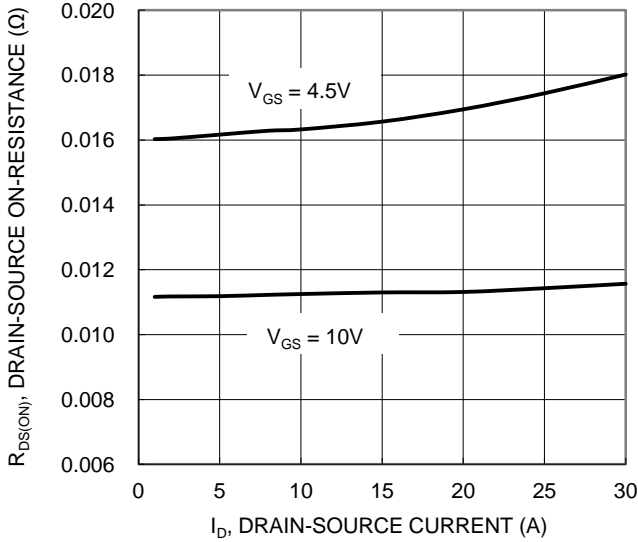


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

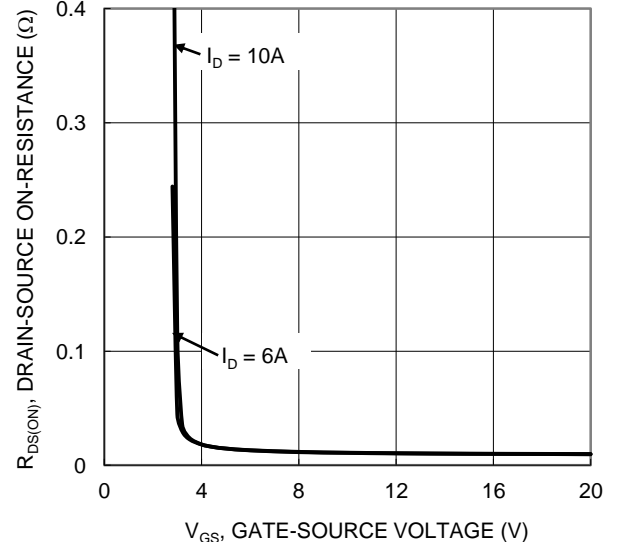


Figure 4. Typical Transfer Characteristic

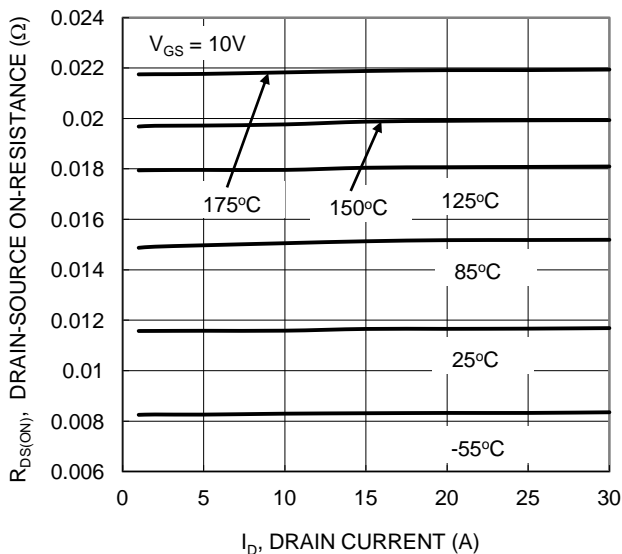


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

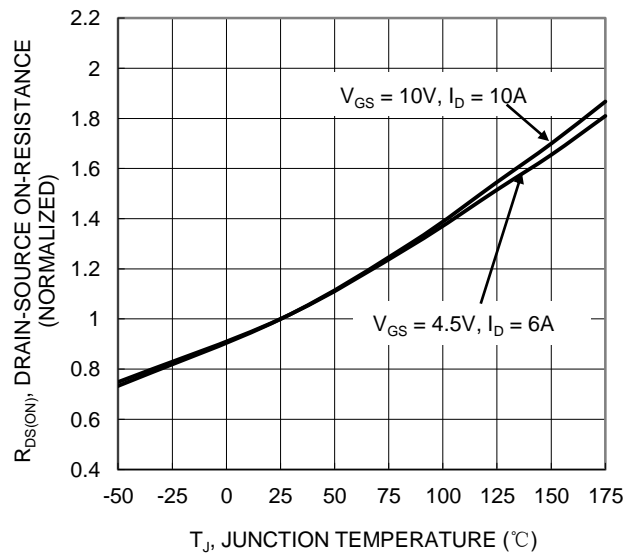


Figure 6. On-Resistance Variation with Temperature

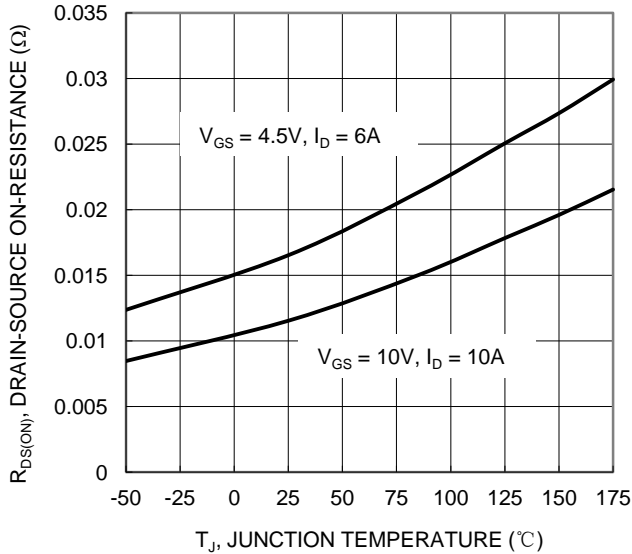


Figure 7. On-Resistance Variation with Temperature

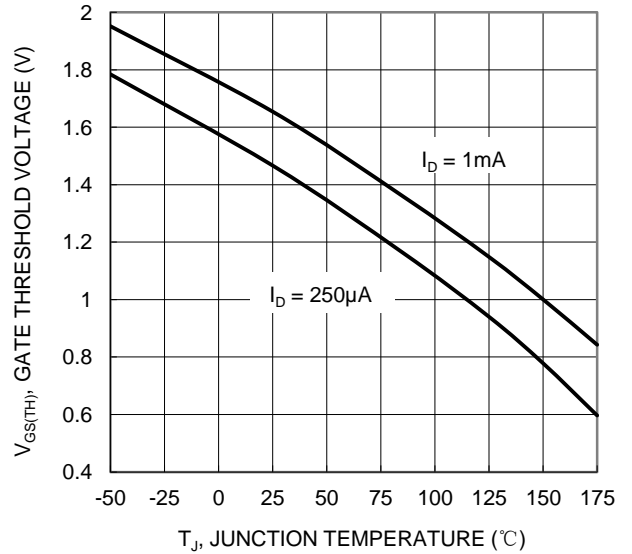


Figure 8. Gate Threshold Variation vs. Temperature

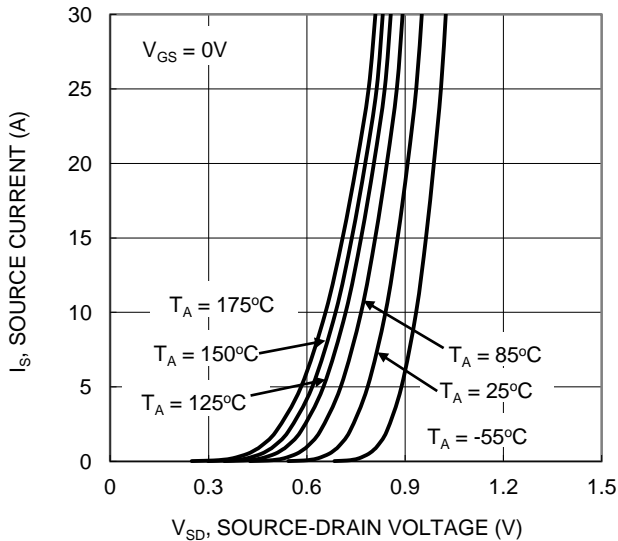


Figure 9. Diode Forward Voltage vs. Current

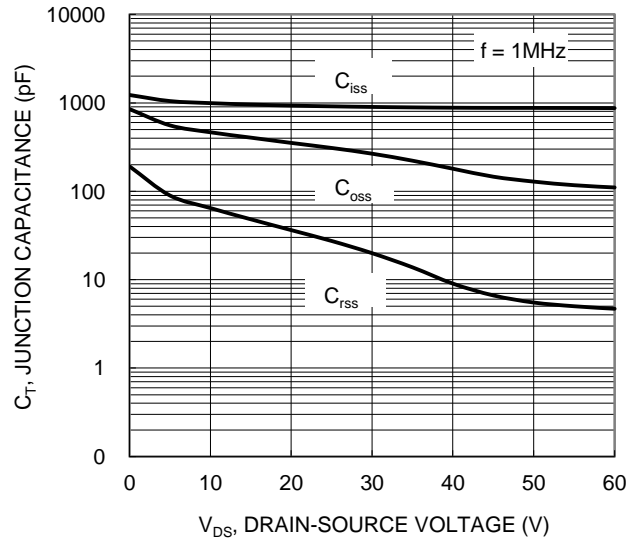


Figure 10. Typical Junction Capacitance

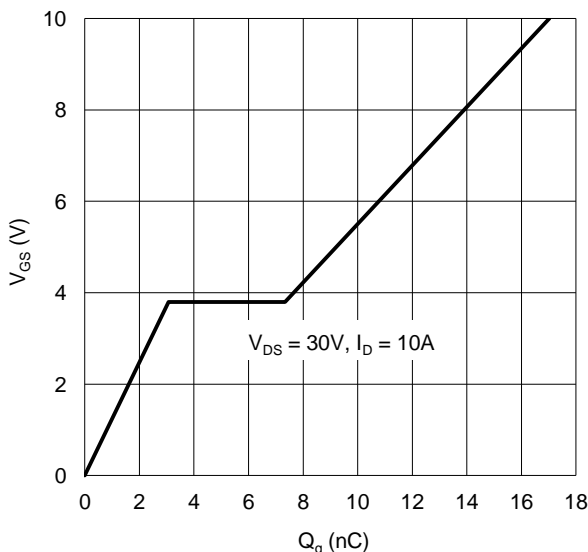


Figure 11. Gate Charge

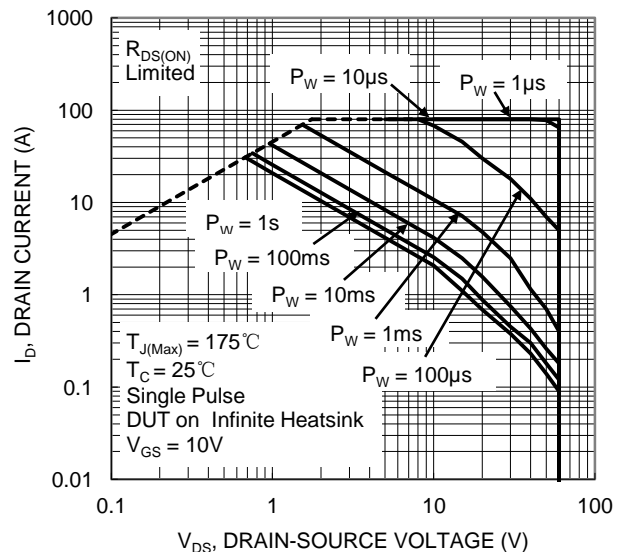


Figure 12. SOA, Safe Operation Area

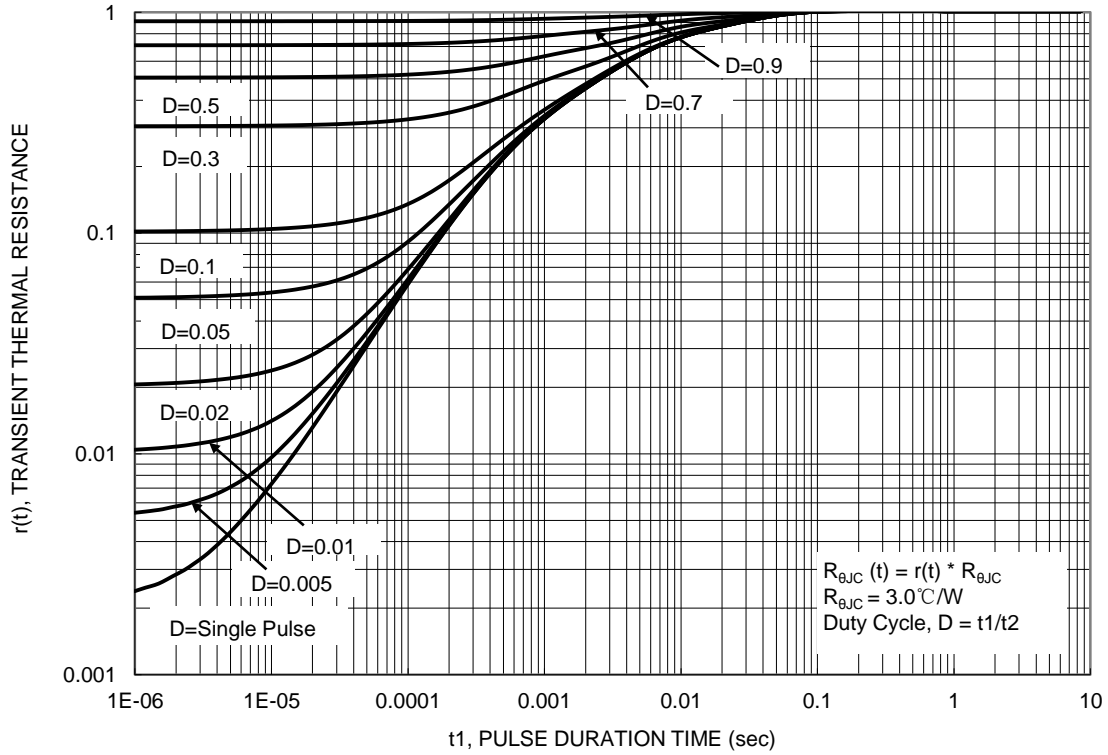
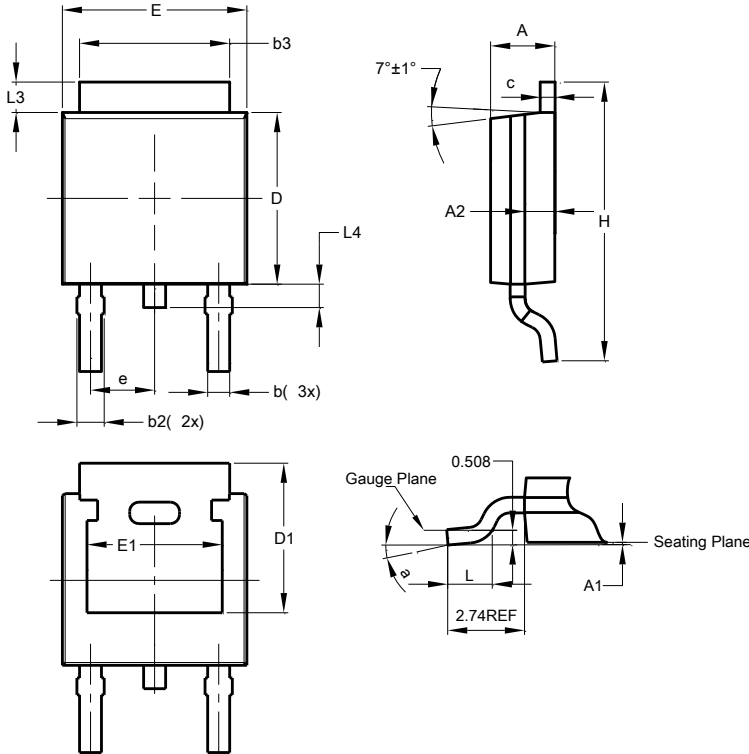


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**TO252 (DPAK)**

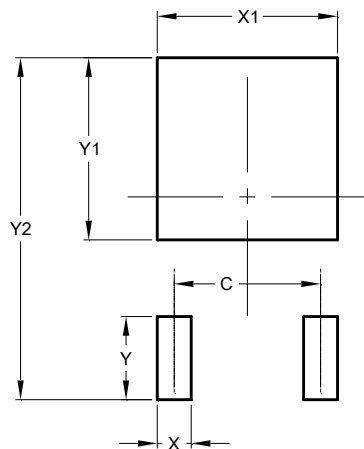


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**TO252 (DPAK)**



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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