



DMT68M8LPS

60V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI5060-8

Product Summary

BV _{DSS}	Rds(on)	Ι _D T _C = +25°C	
60V	7.9mΩ @ V _{GS} = 10V	69.2A	
	10.8mΩ @ V _{GS} = 4.5V	59.2A	

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

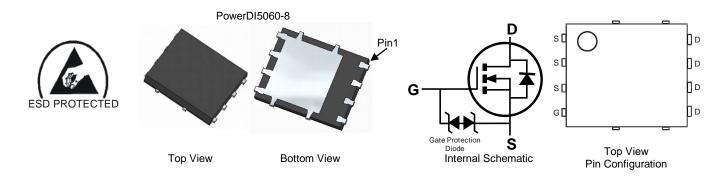
- Synchronous Rectifier
- DC-DC Converters
- Power Management

Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)}—Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish—Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

Case Packaging	
erDI5060-8 2500/Tape & Reel	
Ne	

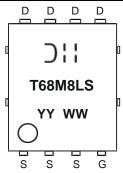
No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, see https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:



] | | = Manufacturer's Marking
 T68M8LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 18 = 2018)
 WW = Week Code (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5) V_{GS} = 10V	T _A = +25°C T _A = +70°C	ID	14.1 11.2	А
Continuous Drain Current (Note 6) V _{GS} = 10V	T _C = +25°C T _C = +70°C	I _D	69.2 55.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	270	А
Maximum Continuous Body Diode Forward Current (Note 6)	ls	69	А	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	270	А	
Avalanche Current, L = 0.1mH	I _{AS}	28.1	А	
Avalanche Energy, L = 0.1mH		EAS	39.5	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	2.4	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{ƏJA}	53	°C/W
Total Power Dissipation (Note 6)	$T_{C} = +25^{\circ}C$	PD	56.8	W
Thermal Resistance, Junction to Case (Note 6)		R _{ejc}	2.2	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			r	1	1		
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance			5.9	7.9	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source Off-Resistance	R _{DS(ON)}		7.8	10.8	11152	$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		2078	_		$\label{eq:VDS} \begin{array}{l} V_{\mathrm{DS}} = 30V, \ V_{\mathrm{GS}} = 0V, \\ f = 1MHz \end{array}$	
Output Capacitance	Coss	_	605	—	pF		
Reverse Transfer Capacitance	Crss	_	44	—			
Gate Resistance	R _G	_	1.71	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	14.4	_		V _{DS} = 30V, I _D = 20A	
Total Gate Charge (V _{GS} = 10V)	Qg	—	30	—	nC		
Gate-Source Charge	Q _{gs}	_	4.1	_	nc		
Gate-Drain Charge	Q _{gd}	_	6.7	—			
Turn-On Delay Time	t _{D(ON)}	_	5.2	_		$V_{DS} = 30V, V_{GS} = 10V,$ $I_{D} = 20A, R_{G} = 3.3\Omega$	
Turn-On Rise Time	t _R	_	9.6	—			
Turn-Off Delay Time	t _{D(OFF)}		20.5	—	ns		
Turn-Off Fall Time	t _F	_	8.9	_			
Reverse Recovery Time	t _{RR}		32.5	—	ns		
Reverse Recovery Charge	Q _{RR}		22.8	—	nC	I _F = 20A, di/dt = 100A/μs	

Notes:

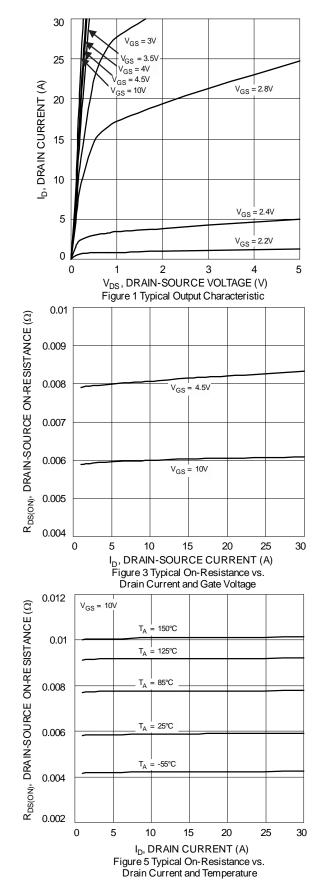
5. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.

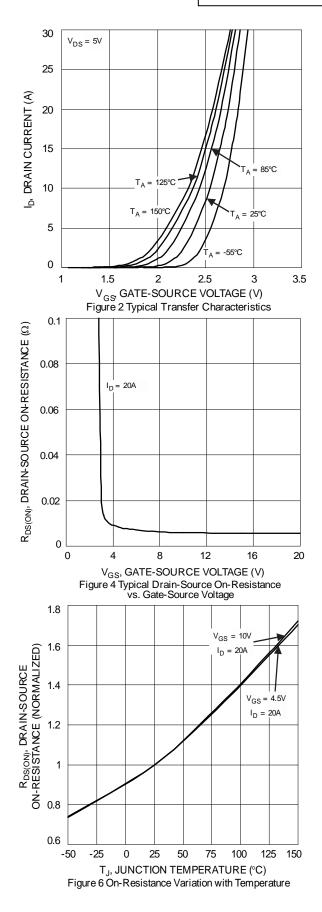
6. Thermal resistance from junction to soldering point (on the exposed drain pad).
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

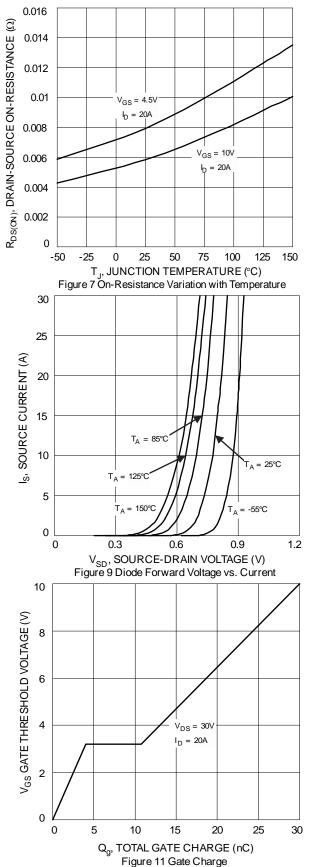


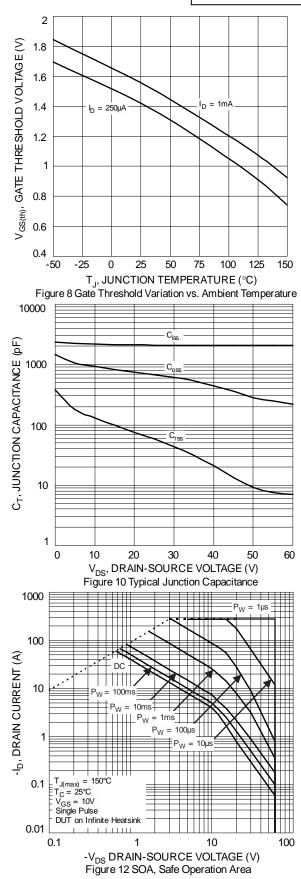
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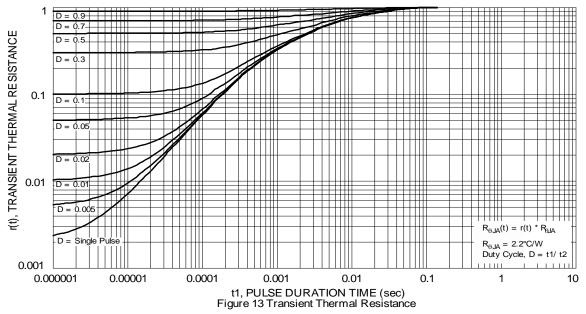








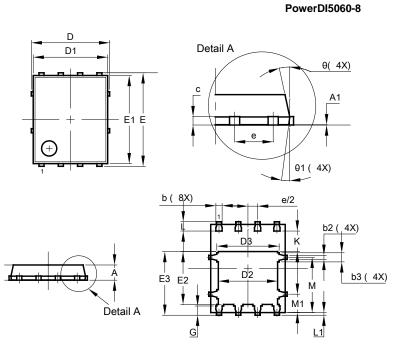
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Package Outline Dimensions

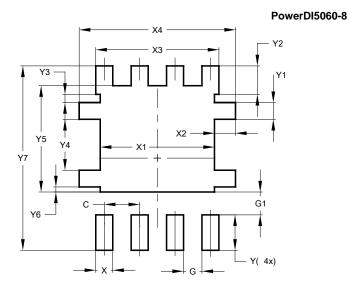
Please see http://www.diodes.com/package-outlines.html for the latest version.



	PowerDI5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05				
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D		5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	—	—			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
Al	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions Value (in mm) 1.270 С G 0.660 G1 0.820 Х 0.610 X1 4.100 X2 0.755 Х3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180 Y7 6.610

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