



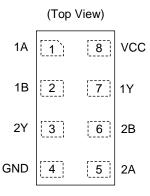
DUAL 2-INPUT OR GATE

Description

The 74LVC2G32 is a dual, two input OR gate. Both gates have push-pull outputs designed for operation over a power supply range of 1.65V to 5.5V. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output, preventing damaging current backflow when the device is powered down. Each gate performs the positive Boolean function:

$$Y = A + B$$
 or $Y = \overline{\overline{A} \bullet \overline{B}}$

Pin Assignments



X2-DFN2010-8 X2-DFN1410-8 X2-DFN1210-8

Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- ± 24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- Schmitt Trigger Action at all inputs makes the circuit tolerant for slower input rise and fall times. The hysteresis is typically 100mV at V_{CC} = 3.0V.
- ESD Protection Exceeds JESD 22
 - 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Applications

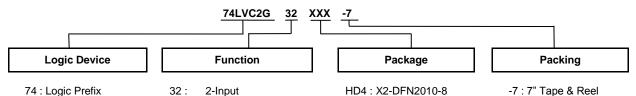
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as:
 - PCs, Networking, Notebooks, Netbooks, PDAs
 - Tablet Computers, E-readers
 - Computer Peripherals, Hard Drives, CD/DVD ROMs
 - TVs, DVDs, DVRs, Set Top Boxes
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players, Cameras, Video Recorders

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information (Note 4)



74 : Logic Prefix LVC : 1.65V to 5.5V Logic Family

2G: Dual Gate

HD4: X2-DFN2010-8 HK3: X2-DFN1410-8 RA3: X2-DFN1210-8

-7: 7" Tape & Reel

	Package	Package	Package	7" Tape and Re	el (Note 6)
Device	Code	(Note 5)	Size	Quantity	Part Number Suffix
74LVC2G32HD4-7	HD4	X2-DFN2010-8	1.95mm x 1.0mm x 0.4mm 0.5 mm lead pitch	5,000/Tape & Reel	-7
74LVC2G32HK3-7	HK3	X2-DFN1410-8	1.35mm x 1.0mm x 0.35mm 0.4 mm lead pitch	5,000/Tape & Reel	-7
74LVC2G32RA3-7	RA3	X2-DFN1210-8	1.2mm x 1.0mm x 0.35mm 0.3 mm lead pitch	5,000/Tape & Reel	-7

Notes: 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
5. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

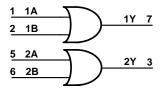
6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

OR Gate

Pin Descriptions

Pin Name	Pin No.	Description	
1A	1	Data Input	
1B	2	Data Input	
2Y	3	Data Output	
GND	4	Ground	
2A	5	Data Input	
2B	6	Data Input	
1Y	7	Data Output	
V _{CC}	8	Supply Voltage	

Logic Diagram



Function Table

Inp	Output	
Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н



Absolute Maximum Ratings (Notes 7 & 8)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage	-0.5 to +6.5	V
VI	Input Voltage	-0.5 to +6.5	V
Vo	Output Voltage - Active Mode	-0.5 to V _{CC} +0.5	V
Vo	Output Voltage Power Down Mode	-0.5 to +6.5	V
I _{IK}	Input Clamp Current V _I <0	-50	mA
lok	Output Clamp Current (Vo < 0 OR Vo > Vcc)	±50	mA
lo	Continuous Output Current (Vo = 0 to V _{CC})	±50	mA
Icc	Continuous Current Through V _{CC}	100	mA
I _{GND}	Continuous Current Through GND	-100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

Recommended Operating Conditions (Note 9)

Symbol	Parameter		Min	Max	Unit	
.,	On and the second	Operating	1.65	5.5	.,	
V _{CC}	Operating Voltage	Data Retention Only	1.5	_	V	
VI	Input Voltage		0	5.5	V	
	Output Voltage Active Mode		0	V _{CC}	V	
Vo	Output Voltage Power-Down Mode		0	5.5]	
		V _{CC} = 1.65V	_	-4		
		V _{CC} = 2.3V	_	-8		
	High-Level Output Current	V _{CC} = 2.7V	_	-12	mA	
I _{OH}	Tright-Level Output Current	V _{CC} = 3.0V	_	-16		
			_	-24		
		V _{CC} = 4.5V	_	-32		
		V _{CC} = 1.65V	_	4		
		V _{CC} = 2.3V	_	8]	
I.e.	Low Lovel Output Current	V _{CC} = 2.7V	_	12	mA	
loL	Low-Level Output Current	V 2.0V	_	16	IIIA	
		$V_{CC} = 3.0V$	_	24		
		V _{CC} = 4.5V	_	32		
Δt/ΔV	Innut Transition Disc or Fall Data	V _{CC} = 1.65V to 2.7V	_	20	20/1	
ΔυΔν	Input Transition Rise or Fall Rate	V _{CC} = 2.7V to 5.5V	_	10	ns/V	
T _A	Operating Fr	ee-Air Temperature	-40	+125	°C	

Note: 9. Unused inputs should be held at V_{CC} or Ground.

^{7.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device

operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics (All typical values are at T_A = +25°C)

	_		.,	-40	°C to +8	5°C	-40°C to	+125°C	
Symbol	Parameter	Test Conditions	V _{cc}	Min	Тур.	Max	Min	Max	Unit
			$V_{CC} = 1.65V \text{ to } 1.95V$	0.65 x V _{CC}	_	_	0.65 x V _{CC}	_	
.,	High-Level		V _{CC} = 2.3V to 2.7V	1.7	-	_	1.7	_	Ī.,
V_{IH}	Input Voltage	_	V _{CC} = 2.7V to 3.6V	2.0	_	_	2.0	_	V
			V _{CC} = 4.5V to 5.5V	0.7 x V _{CC}	_	_	0.7 x V _{CC}	_	
			$V_{CC} = 1.65V \text{ to } 1.95V$	_	_	0.35 x V _{CC}	_	0.35 x V _{CC}	
.,	Low-Level		V _{CC} = 2.3V to 2.7V	_	_	0.7	_	0.7	1
V_{IL}	Input Voltage	_	V _{CC} = 2.7V to 3.6V	_	_	0.8	_	0.8	V
			V _{CC} = 4.5V to 5.5V	_	-	0.3 x V _{CC}	_	0.3 x V _{CC}	
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} – 0.1	Vcc	_	V _{CC} – 0.1	_	
		I _{OH} = -4mA	1.65V	1.2	1.53	_	0.95	_	
	High-Level	$I_{OH} = -8mA$	2.3V	1.9	2.13	_	1.7	_	
Vон	Output	I _{OH} = -12mA	2.7	2.2	2.5	_	1.9	_	V
	Voltage	I _{OH} = -16mA	21.6	2.4	2.7	_	2.2	_	
		I _{OH} = -24mA	3V	2.3	2.6	_	2.0	_	
		I _{OH} = -32mA	4.5V	3.8	4.1	_	3.4	_	
	I _{OL} = 100μA	1.65V to 5.5V	_	0	0.1	_	0.1		
		I _{OL} = 4mA	1.65V	_	0.08	0.45	_	0.7	
	Low-Level	$I_{OL} = 8mA$	2.3V	_	0.14	0.3	_	0.45	
V_{OL}	Output	$I_{OL} = 12mA$	2.7V	_	0.19	0.4	_	0.6	V
	Voltage	I _{OL} = 16mA	2) /	_	0.25	0.4	_	0.6	
		$I_{OL} = 24mA$	3V	_	0.37	0.55	_	0.8	
		I _{OL} = 32mA	4.5V	_	0.43	0.55	_	0.8	
lı	Input Current	V _I = 5.5V or GND	0V to 5.5V	_	± 0.1	±5	_	± 20	μΑ
I _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 5.5V$	ov	_	± 0.1	±10	_	±20	μA
I _{CC}	Supply Current	$V_I = 5.5V$ or GND $I_O=0A$	1.65V to 5.5V	_	0.1	10	_	40	μA
ΔI _{CC}	Additional Supply Current	One input at $V_{CC} - 0.6V$ Other inputs at V_{CC} or GND	2.3V to 5.5V	_	5	500	_	5,000	μА
Cı	Input Capacitance	V _I = V _{CC} or GND	3.3V	_	2.5	_	_	_	рF



Operating Characteristics

	Parameter	Test Conditions	V _{CC} = 1.8V Typ.	V _{CC} = 2.5V Typ.	V _{CC} = 3.3V Typ.	V _{CC} = 5V Typ.	Unit
$C_{\sf pd}$	Power Dissipation Capacitance	f = 10MHz	17	17	17	19	pF

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур.	Max	Unit
	Thermal Resistance Junction-	X2-DFN2010-8		_	313	_	
θ_{JA}		X2-DFN1410-8	(Note 10)	_	321	_	°C/W
	to-Ambient	X2-DFN1210-8		_	395	_	1
		X2-DFN2010-8		_	145	_	
θ_{JC}	θ _{JC} Thermal Resistance Junction-to-Case	X2-DFN1410-8	(Note 10)	_	166	_	°C/W
to		X2-DFN1210-8		_	236	_	1

Note: 10. Test condition for each package type: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

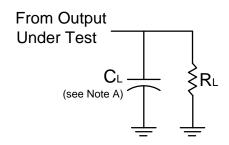
Switching Characteristics

Typical Values at $T_A = +25$ °C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V. See Figure 1.

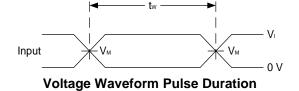
Donomoton	From	То	V	T _A	= -40°C to +85	5°C	T _A = -40°C	to +125°C	l lmit
Parameter	Input	Output	V _{CC}	Min	Тур	Max	Min	Max	Unit
			$1.8V \pm 0.15V$	1.3	3.9	8.8	1.3	11.0	
			$2.5V \pm 0.2V$	0.8	2.4	4.7	0.8	5.9	
t _{pd}	t _{pd} A or B	Y	2.7V	0.8	2.7	4.8	0.8	6.0	ns
			$3.3V \pm 0.3V$	0.9	2.2	4.2	0.9	5.3	
			5.0V ± 0.5V	0.7	1.7	3.2	0.7	4.0	

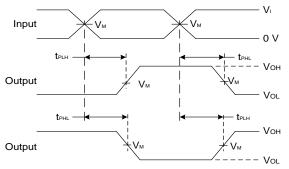


Parameter Measurement Information



V _{cc}	Inputs		. V _M	C _L	R _L
V CC	Vı	t _r /t _f	• • • • • • • • • • • • • • • • • • •	O _L	ΚL
1.8V ± 0.15V	V _{cc}	≤2ns	V _{CC} /2	30pF	1kΩ
2.5V ± 0.2V	Vcc	≤2ns	V _{CC} /2	30pF	500Ω
2.7V	2.7V	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	2.7V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	V _{cc}	≤2.5ns	V _{CC} /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes:

A. Includes test lead and test apparatus capacitance.B. All pulses are supplied at pulse repetition rate ≤ 10MHz.

C. Inputs are measured separately one transition per measurement.

D. t_{PLH} and t_{PHL} are the same as $t_{\text{pd}}.$



Marking Information

(Top View)

<u>XX</u>

XX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week; z represents
52 and 53 week

X : Internal Code

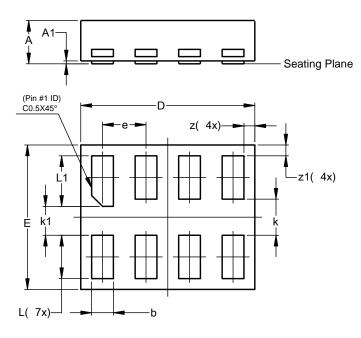
Part Number	Package	Identification Code
74LVC2G32HD4-7	X2-DFN2010-8	9H
74LVC2G32HK3-7	X2-DFN1410-8	9J
74LVC2G32RA3-7	X2-DFN1210-8	9K



X2-DFN1210-8 Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1210-8

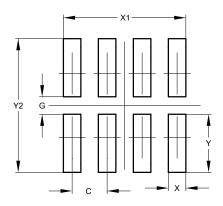


	X2-DFN1210-8								
Dim	Min	Max	Тур						
Α	-	0.35	0.30						
A1	0	0.03	0.02						
b	0.10	0.20	0.15						
D	1.15	1.25	1.20						
Е	0.95	1.05	1.00						
е	-	-	0.30						
k	-	-	0.25						
k1	-	-	0.20						
L	0.25	0.35	0.30						
L1	0.30	0.40	0.35						
Z	0.050	0.100	0.075						
z 1	0.050	0.100	0.075						
All I	Dimens	ions in	mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1210-8



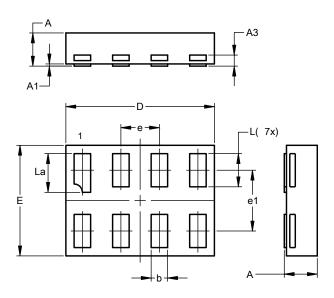
Dimensions	Value (in mm)
С	0.300
G	0.150
Х	0.150
X1	1.050
Y	0.500
Y1	1.150



X2-DFN1410-8 Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1410-8

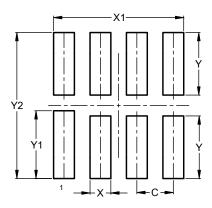


X2-DFN1410-8				
Dim	Min	Max	Тур	
Α	0.30	0.35	0.33	
A1	0.00	0.03	0.02	
A3			0.10	
b	0.12	0.20	0.15	
D	1.30	1.40	1.35	
Е	0.95	1.05	1.00	
е			0.35	
e1			0.55	
L	0.27	0.35	0.30	
L1	0.32	0.40	0.35	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1410-8



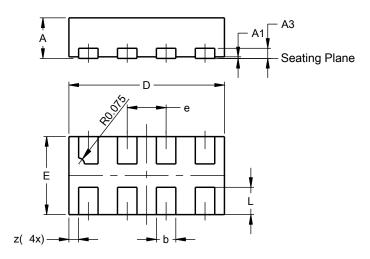
Dimensions	Value	
Dillielisions	(in mm)	
С	0.350	
Х	0.200	
X1	1.250	
Y	0.600	
Y1	0.650	
Y2	1.400	



X2-DFN2010-8 Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN2010-8

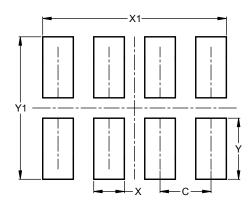


X2-DFN2010-8				
Dim	Min	Max	Тур	
Α		0.40		
A1	0.00	0.05	0.02	
А3			0.13	
b	0.20	0.30	0.25	
D	1.950	2.05	2.00	
Е	0.95	1.05	1.00	
е			0.50	
L	0.30	0.40	0.35	
Z			0.125	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN2010-8



Dimensions	Value (in mm)
С	0.500
X	0.300
X1	1.800
Y	0.600
V1	1 400



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