



DMT10H009SSS

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
100V	9.2mΩ @ V _{GS} = 10V	12A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Features and Benefits

- High Conversion Efficiency
- Low R_{DS(ON)}—Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

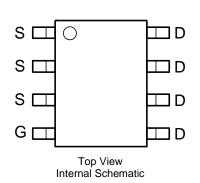
100V N-CHANNEL ENHANCEMENT MODE MOSFET

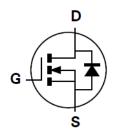
• Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (B)
- Weight: 0.074 grams (Approximate)







Equivalent Circuit

Ordering Information (Note 4)

Case	Packaging
SO-8	2500/Tape & Reel
	SO 9

Notes:

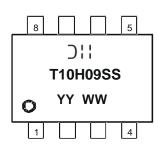
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



) | | = Manufacturer's MarkingT10H09SS = Product Type Marking CodeYYWW = Date Code Marking $YY or <math>\overrightarrow{YY}$ = Year (ex: 18 = 2018) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit V V A
Drain-Source Voltage		V _{DSS}	100	
Gate-Source Voltage		V _{GSS}	±20	
	T _A = +25°C T _A = +70°C	ID	12 10	
Continuous Drain Current (Note 6) $V_{GS} = 10V$	T _C = +25°C T _C = +70°C	ID	42 34	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	100	А
Maximum Continuous Body Diode Forward Current (No	te 6)	Is	1.8	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty	Cycle = 1%)	I _{SM}	100	А
Avalanche Current, L = 0.3mH		I _{AS}	27	А
Avalanche Energy, L = 0.3mH		E _{AS}	109.4	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{ØJA}	89	°C/W
Total Power Dissipation (Note 6)	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{ƏJA}	61	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{eJC}	5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

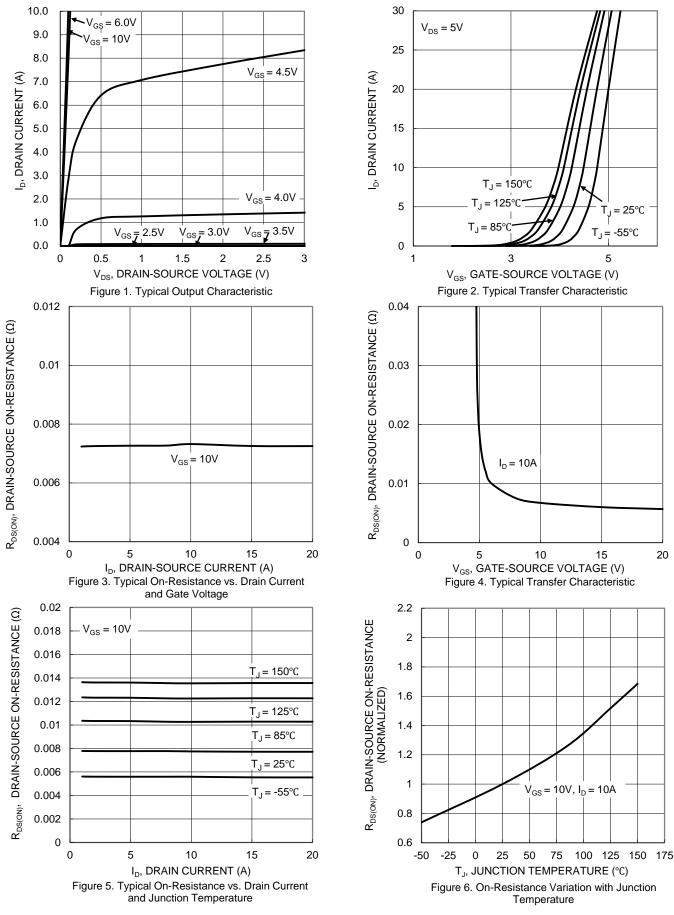
Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Mim	Tum	Мах	Unit	Test Condition
	Symbol	Min	Тур	wax	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			1	r		
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1	μA	$V_{DS} = 80V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	7.5	9.2	mΩ	$V_{GS} = 10V, I_D = 10A$
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 20A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	2085	—		$V_{DS} = 50V, V_{GS} = 0V$ f = 1MHz
Output Capacitance	Coss	_	609	_	pF	
Reverse Transfer Capacitance	C _{rss}	_	13	—		
Gate Resistance	Rg	_	1.7	—	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	29.8	—		V _{DD} = 50V, I _D = 13A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	_	9.5	—	nC	
Gate-Drain Charge	Q _{gd}	_	7.3	—		
Turn-On Delay Time	t _{D(ON)}	_	9.7	—		
Turn-On Rise Time	t _R	_	13.7	—	ns	$\label{eq:VDD} \begin{array}{l} V_{DD}=50V, \ V_{GS}=10V, \\ I_{D}=13A, \ R_{g}=6\Omega \end{array}$
Turn-Off Delay Time	t _{D(OFF)}	_	25.1	—	115	
Turn-Off Fall Time	tF	_	17.4	—		
Reverse Recovery Time	t _{RR}	—	45	—	ns	$I_{-} = 120$ di/dt = 1000//uc
Reverse Recovery Charge	Q _{RR}	_	68	—	nC	I _F = 13A, di/dt = 100A/μs

 Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:

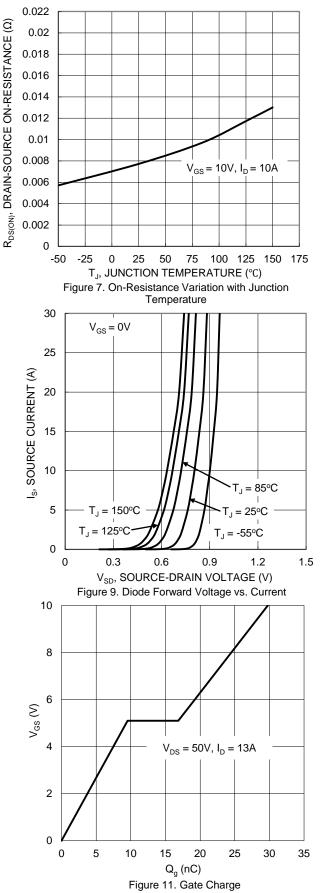


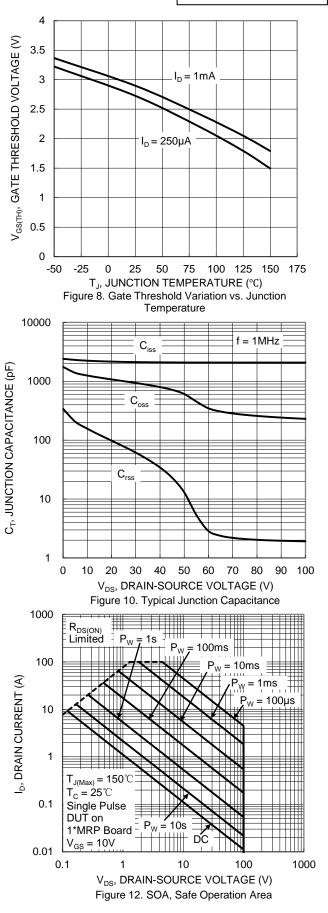
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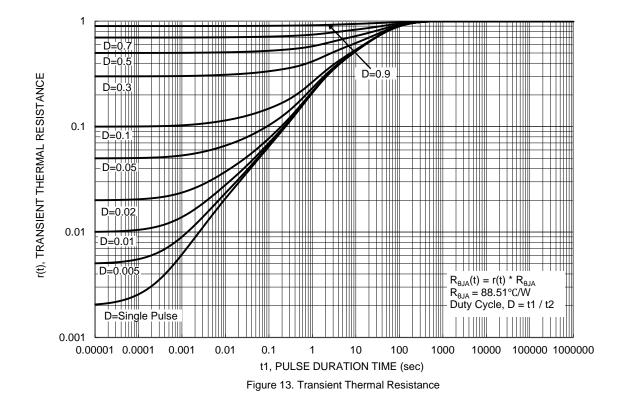


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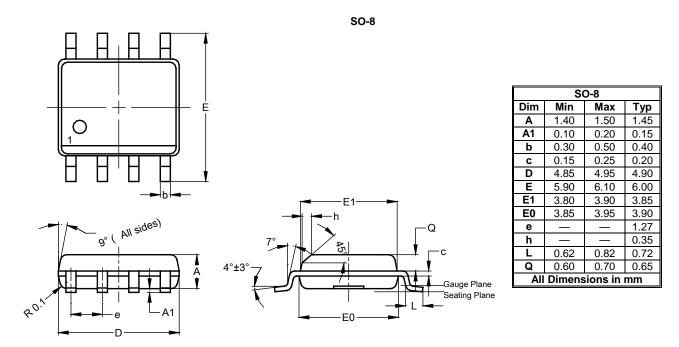






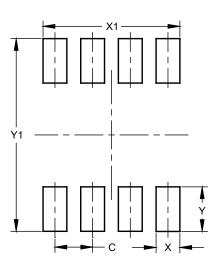
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)			
С	1.27			
Х	0.802			
X1	4.612			
Y	1.505			
Y1	6.50			



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