



#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-20V	$45 \text{m}\Omega$ @ $V_{GS} = -4.5 \text{V}$	-4.2A
-200	$65m\Omega$ @ $V_{GS} = -2.5V$	-3.5A

#### **Description**

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

#### **Applications**

- General Purpose Interfacing Switch
- Power Management Functions

## **Features and Benefits**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

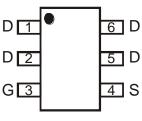
#### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe.
  Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.013 grams (Approximate)

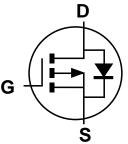
## TSOT26



Top View



Top View Pin Configuration



Equivalent Circuit

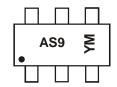
#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2067LVT-7	TSOT26	3,000/Tape & Reel
DMP2067LVT-13	TSOT26	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



AS9 = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	20	020	2021	2022	2	2023	2024	202	25	2026
Code	F	G		Н		J		K	L	N	1	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	-20	V		
Gate-Source Voltage	$V_{GSS}$	±8	V		
Drain Current (Note 5) Continuous	I <sub>D</sub>	-4.2 -3.4	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	-30	Α	
Body-Diode Continuous Current (Note 5)		Is	-1.4	A	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	105	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	78	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

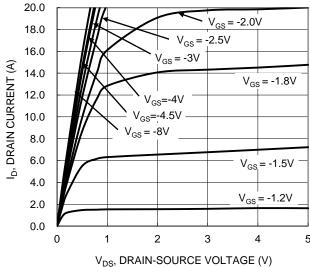
### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS (Note 7)	T		1	1	1		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Body Leakage Current	I <sub>GSS</sub>	_	_	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 8V$	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.4	_	-1.5	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	22 27	45 65	mΩ	$V_{GS} = -4.5V$ , $I_{D} = -4.5A$ $V_{GS} = -2.5V$ , $I_{D} = -3.8A$	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.4	V	$I_S = -2.1A$ , $V_{GS} = 0V$	
DYNAMIC PARAMETERS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	1575	_	pF		
Output Capacitance	Coss	_	124	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	89	_	pF		
Gate Resistance	Rg	_	10	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	15	_			
Total Gate Charge (V <sub>GS</sub> = -8V)	Qg	_	28	_	nC	$V_{DS} = -10V, I_{D} = -4.5A$	
Gate-Source Charge	Q <sub>gs</sub>	_	1.6	_	iiC	$V_{DS} = -10V, I_{D} = -4.5A$	
Gate-Drain Charge	$Q_{gd}$	_	3.4	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.2	_			
Rise Time	t <sub>R</sub>	_	12	_		$V_{DS} = -5V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	103	_	ns	$I_D = -1A, R_g = 6.0\Omega$	
Fall Time	t <sub>F</sub>	_	31	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	13		ns	$I_F = -8.9A$ , $di/dt = -100A/\mu s$	
Body Diode Reverse Recovery Charge	$Q_{RR}$	_	6.7		nC	$I_F = -8.9A$ , $di/dt = -100A/\mu s$	

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 1. Typical Output Characteristic

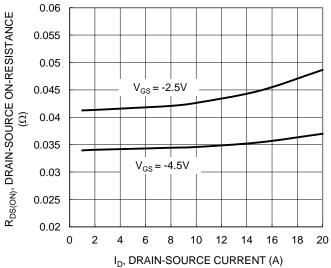


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

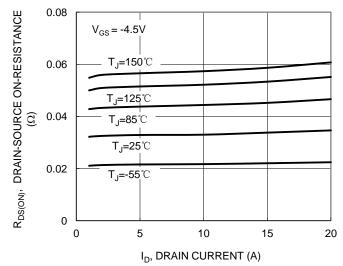


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

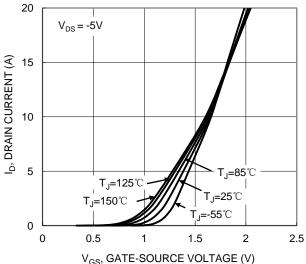


Figure 2. Typical Transfer Characteristic

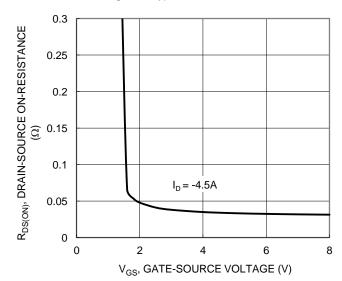


Figure 4. Typical Transfer Characteristic

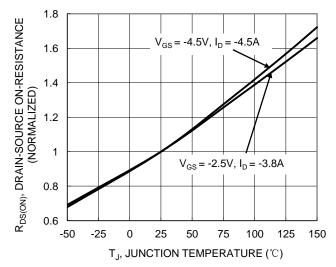


Figure 6. On-Resistance Variation with Junction Temperature



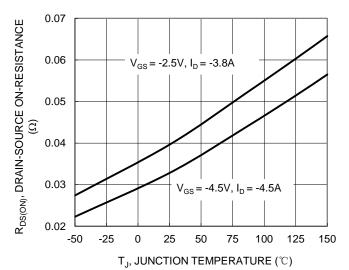
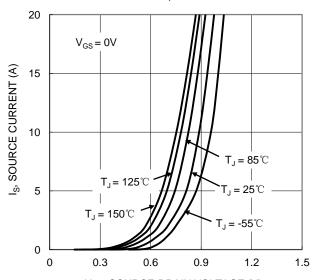


Figure 7. On-Resistance Variation with Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

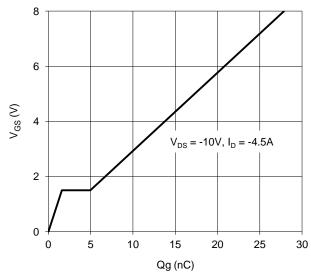
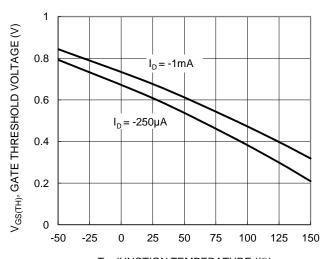
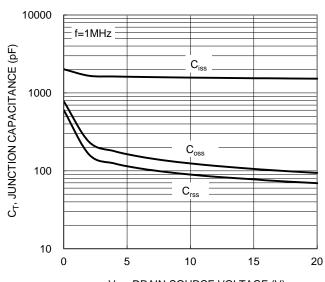


Figure 11. Gate Charge



 $T_J$ , JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction Temperature



V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Junction Capacitance

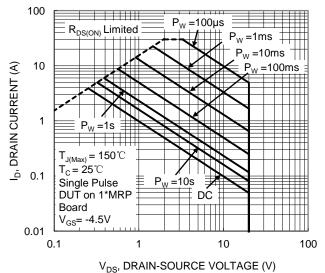


Figure 12. SOA, Safe Operation Area



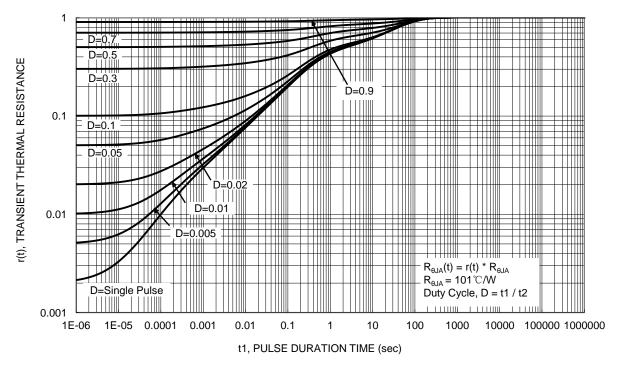
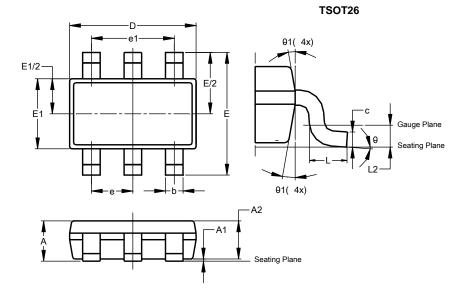


Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

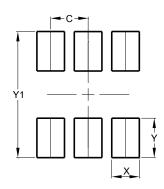


TSOT26							
Dim	Min	Max	Тур				
Α	-	1.00	-				
<b>A</b> 1	0.010	0.100	-				
A2	0.840	0.900	-				
ם	2.800	3.000	2.900				
Е	2	2.800 BSC					
E1	1.500	1.700	1.600				
b	0.300	0.450	_				
С	0.120	0.200	-				
е	0.950 BSC						
e1	1	1.900 BSC					
L	0.30	0.50	-				
L2	0.250 BSC						
θ	0°	8°	4°				
θ1	4°	12°	_				
All Dimensions in mm							

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
V1	3 100



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