,
DMT15H053SSS

## Product Summary

| BV DSS | RDS(ON) Max | $\mathbf{I}_{\mathbf{D}} \operatorname{Max}$ <br> $\mathbf{T}_{\mathbf{A}}=+\mathbf{2 5}{ }^{\circ} \mathbf{C}$ |
| :---: | :---: | :---: |
| 150 V | $53 \mathrm{~m} \Omega @ \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ | 5.2 A |

## Description and Applications

This MOSFET is designed to minimize the on-state resistance ( $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$ ), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters


## Features and Benefits

- High Conversion Efficiency
- Low RDS(ON) - Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- 100\% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)


## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.074 grams (Approximate)


Ordering Information (Note 4)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| DMT15H053SSS-13 | SO-8 | 2,500/Tape \& Reel |

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) \& 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



J|' = Manufacturer's Marking
T15H53SS = Product Type Marking Code
YYWW = Date Code Marking
YY or $\overline{Y Y}=$ Year (ex: $19=2019)$
WW = Week (01 to 53)

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Maximum Ratings $\left(@ T_{A}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Characteristic |  | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Drain-Source Voltage |  | $V_{\text {DSS }}$ | 150 | V |
| Gate-Source Voltage |  | $V_{\text {GSS }}$ | $\pm 20$ | V |
| Continuous Drain Current (Note 6) VGS $=10 \mathrm{~V}$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | ID | 5.2 | A |
|  | $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ |  | 4.1 |  |
|  | $\mathrm{T}_{\mathrm{C}}=+25^{\circ} \mathrm{C}$ | ID | 15 | A |
|  | $\mathrm{T}_{\mathrm{C}}=+70^{\circ} \mathrm{C}$ |  | 12 |  |
| Pulsed Drain Current (10 $\mu$ s Pulse, Duty Cycle = 1\%) |  | IDM | 34 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) |  | Is | 13 | A |
| Pulsed Body Diode Forward Current (10 $\mu \mathrm{s}$ Pulse, Duty Cycle = 1\%) |  | ISM | 34 | A |
| Avalanche Current, L=1mH |  | $\mathrm{I}_{\text {AS }}$ | 11.7 | A |
| Avalanche Energy, $\mathrm{L}=1 \mathrm{mH}$ |  | $\mathrm{E}_{\text {AS }}$ | 68.4 | mJ |

Thermal Characteristics $\left(\mathrm{C}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Total Power Dissipation (Note 5) | $\mathrm{P}_{\mathrm{D}}$ | 1.3 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | $\mathrm{R}_{\theta \mathrm{JA}}$ | 94 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Total Power Dissipation (Note 6) | $\mathrm{P}_{\mathrm{D}}$ | 2 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | $\mathrm{R}_{\theta J \mathrm{~A}}$ | 59 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance, Junction to Case (Note 6) | $\mathrm{R}_{\theta \mathrm{JC}}$ | 7 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J}, \mathrm{TSTG}}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics $\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS (Note 7) |  |  |  |  |  |  |
| Drain-Source Breakdown Voltage | BV ${ }_{\text {DSS }}$ | 150 | - | - | V | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{ID}=10 \mathrm{~mA}$ |
| Zero Gate Voltage Drain Current | IDSS | - | - | 1 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{DS}}=120 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |
| Gate-Source Leakage | Igss | - | - | $\pm 100$ | nA | $\mathrm{V}_{\mathrm{GS}}= \pm 20 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |
| ON CHARACTERISTICS (Note 7) |  |  |  |  |  |  |
| Gate Threshold Voltage | $\mathrm{VGS}_{\text {G (TH) }}$ | 2 | - | 4 | V | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ |
| Static Drain-Source On-Resistance | $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$ | - | 41 | 53 | $\mathrm{m} \Omega$ | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=4.1 \mathrm{~A}$ |
| Diode Forward Voltage | $\mathrm{V}_{\text {SD }}$ | - | 0.8 | 1 | V | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{S}}=4.1 \mathrm{~A}$ |
| DYNAMIC CHARACTERISTICS (Note 8) |  |  |  |  |  |  |
| Input Capacitance | $\mathrm{C}_{\text {iss }}$ | - | 814 | - |  |  |
| Output Capacitance | Coss | - | 84 | - | pF | $\begin{aligned} & V_{D S}=/ 5 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V} \\ & \mathrm{f}=1 \mathrm{MHz} \end{aligned}$ |
| Reverse Transfer Capacitance | $\mathrm{C}_{\text {rss }}$ | - | 3.7 | - |  |  |
| Gate Resistance | $\mathrm{R}_{\mathrm{g}}$ | - | 0.6 | - | $\Omega$ | $\mathrm{V}_{\mathrm{DS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |
| Total Gate Charge | $\mathrm{Q}_{\mathrm{g}}$ | - | 11.5 | - |  |  |
| Gate-Source Charge | $\mathrm{Q}_{\mathrm{gs}}$ | - | 4.6 | - | $n \mathrm{C}$ | $\begin{aligned} & V_{\mathrm{DS}}=1 \mathrm{~V}, I_{\mathrm{D}}=4.1 \mathrm{~A}, \\ & \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V} \end{aligned}$ |
| Gate-Drain Charge | $\mathrm{Q}_{\mathrm{gd}}$ | - | 2.8 | - |  |  |
| Turn-On Delay Time | $\mathrm{t}_{\text {(ON }}$ | - | 8.5 | - |  |  |
| Turn-On Rise Time | $\mathrm{t}_{\mathrm{R}}$ | - | 3.4 | - | ns |  |
| Turn-Off Delay Time | $\mathrm{t}_{\text {(IOFF) }}$ | - | 11.9 | - |  | $\mathrm{ID}=4.1 \mathrm{~A}, \mathrm{R}_{\mathrm{g}}=6 \Omega$ |
| Turn-Off Fall Time | $\mathrm{t}_{\mathrm{F}}$ | - | 6.2 | - |  |  |
| Reverse Recovery Time | $\mathrm{t}_{\mathrm{RR}}$ | - | 47 | - | ns | $\mathrm{IF}=4.1 \mathrm{~A}$ di/dt $=100 \mathrm{~A} / \mu \mathrm{s}$ |
| Reverse Recovery Charge | QRR | - | 87 | - | nC |  |

Notes: 5. Device mounted on FR-4 substrate PC board, $20 z$ copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2 oz copper, with 1inch square copper plate
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

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Figure 1. Typical Output Characteristic


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature


Figure 2. Typical Transfer Characteristic


Figure 4. Typical Transfer Characteristic


Figure 6. On-Resistance Variation with Junction Temperature

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Figure 7. On-Resistance Variation with Junction Temperature


Figure 9. Diode Forward Voltage vs. Current


Figure 11. Gate Charge


Figure 8. Gate Threshold Variation vs. Junction Temperature


Figure 10. Typical Junction Capacitance


Figure 12. SOA, Safe Operation Area

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Figure 13. Transient Thermal Resistance

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## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

## SO-8



| SO-8 |  |  |  |
| :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |
| A | 1.40 | 1.50 | 1.45 |
| A1 | 0.10 | 0.20 | 0.15 |
| b | 0.30 | 0.50 | 0.40 |
| C | 0.15 | 0.25 | 0.20 |
| D | 4.85 | 4.95 | 4.90 |
| E | 5.90 | 6.10 | 6.00 |
| E1 | 3.80 | 3.90 | 3.85 |
| E0 | 3.85 | 3.95 | 3.90 |
| e | -- | -- | 1.27 |
| h | -- | -- | 0.35 |
| L | 0.62 | 0.82 | 0.72 |
| Q | 0.60 | 0.70 |  |
| All Dimensions in $\mathbf{~ m m}$ |  |  |  |
|  |  |  |  |

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.


| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{C}$ | 1.27 |
| $\mathbf{X}$ | 0.802 |
| $\mathbf{X 1}$ | 4.612 |
| $\mathbf{Y}$ | 1.505 |
| $\mathbf{Y 1}$ | 6.50 |

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