



#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

100% Unclamped Inductive Switching (UIS) Test in Production -

capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A

https://www.diodes.com/products/automotive/automotive-

This part is qualified to JEDEC standards (as references in

0.6mm Profile - Ideal for Low Profile Applications

Ensures More Reliable and Robust End Application Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP

## **Product Summary**

BVDSS	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
100V	52mΩ @ V <sub>GS</sub> = 10V	5A
1007	$75m\Omega$ @ V <sub>GS</sub> = 4.5V	4.1A

## **Description**

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

# Mechanical Data

products/.

**Features and Benefits** 

PCB Footprint of 4mm<sup>2</sup> Low On-Resistance

listing can be found at

AEC-Q) for High Reliability.

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0

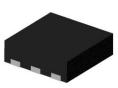
https://www.diodes.com/quality/product-definitions/

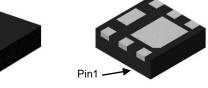
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

# Applications

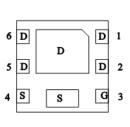
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays Memories, Transistors, etc.

U-DFN2020-6 (Type F)

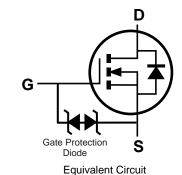




Top View Bottom View







#### **Ordering Information** (Note 4)

Part Number	Case	Quantity Per Reel
DMT10H052LFDF-7	U-DFN2020-6 (Type F)	3,000
DMT10H052LFDF-13	U-DFN2020-6 (Type F)	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



## **Marking Information**

#### U-DFN2020-6 (Type F)



57 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)

W = Week (ex: a = week 27; z represents week 52 and 53)

X = Internal Code (ex: U = Monday)

#### Date Code Kev

Date Code Hoy												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0
Week 1-26				27-52			53					
Code		Α	-Z		a-z			Z				
Internal Code	Sun	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	\	٧	Х		Υ		Z

## **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	VDSS	100	V
Gate-Source Voltage	Vgss	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	ΔI	5 4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	30	Α
Maximum Body Diode Continuous Current	Is	2.3	Α
Pulsed Body Diode Continuous Current (10µs Pulse, Duty Cy	Ism	30	Α
Avalanche Current, L = 0.1mH (Note 7)	I <sub>AS</sub>	15.6	Α
Avalanche Energy, L = 0.1mH (Note 7)	Eas	12.2	mJ

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	D-	0.8	W
Total Power Dissipation (Note 5)	$T_A = +70$ °C	P <sub>D</sub>	0.5	] vv
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	151	°C/W	
Total Bayer Dissination (Note 6)	$T_A = +25^{\circ}C$	D-	1.9	W
Total Power Dissipation (Note 6)	$T_A = +70$ °C	PD	1.2	] vv
Thermal Resistance, Junction to Ambient (Note 6)	RθJA	64	°C/W	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	11	C/VV	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J$  = +25°C.



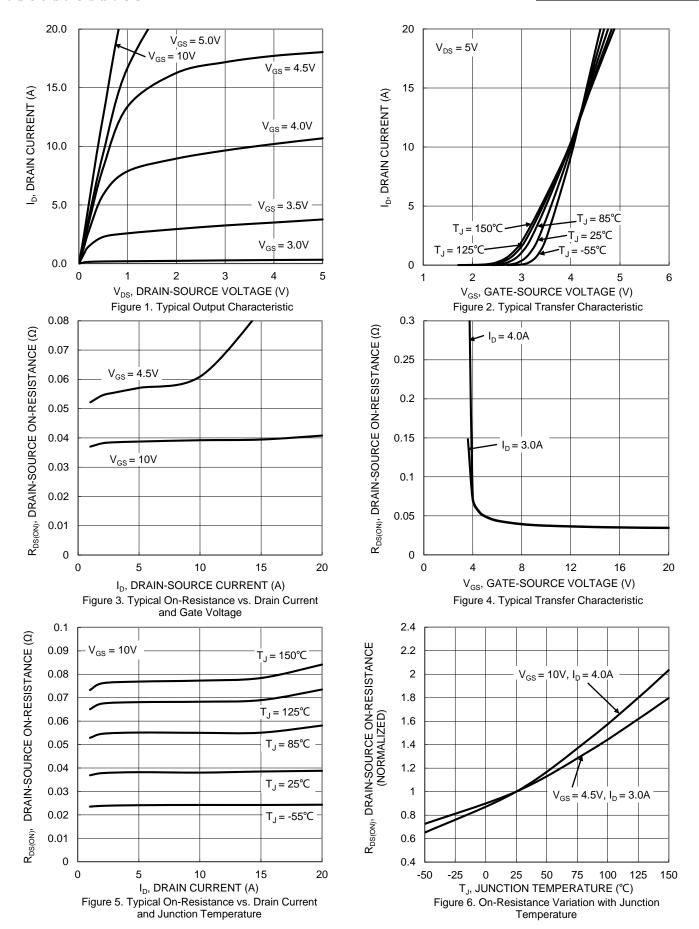
## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100		_	V	$V_{GS} = 0V$ , $I_D = 1mA$
Zero Gate Voltage Drain Current	IDSS		1	1	μΑ	V <sub>DS</sub> = 80V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	Vgs(TH)	1.5	1	3.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	D-s(s)		38	52	mΩ	$V_{GS} = 10V$ , $I_D = 4A$
Static Dialif-Source Off-Resistance	Rds(on)		54	75	11122	$V_{GS} = 4.5V, I_{D} = 3A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss		258	_	рF	), 50\/\), 0\/
Output Capacitance	Coss	l	114		рF	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1MHz
Reverse Transfer Capacitance	Crss	-	5.5		pF	1 – 1101112
Gate Resistance	Rg	_	6.3	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (VGS = 4.5V)	Qg	-	2.9		nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	5.4	_	nC	\/ 50\/ I- 4A
Gate-Source Charge	Q <sub>gs</sub>	_	0.8	_	nC	$V_{DS} = 50V, I_{D} = 4A$
Gate-Drain Charge	Qgd	_	1.6	_	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.1	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	3.8	_	ns	$V_{DS} = 50V$ , $R_L = 11\Omega$
Turn-Off Delay Time	tD(OFF)	_	11.4	_	ns	$V_{GS} = 10V, R_{GEN} = 3\Omega$
Turn-Off Fall Time	tF	_	4.4	_	ns	]
Reverse Recovery Time	trr	_	22.6	_	ns	1 4A 11/11 000A/c-
Reverse Recovery Charge	Q <sub>RR</sub>		43.6	_	nC	IF = 4A, di/dt = 300A/μs

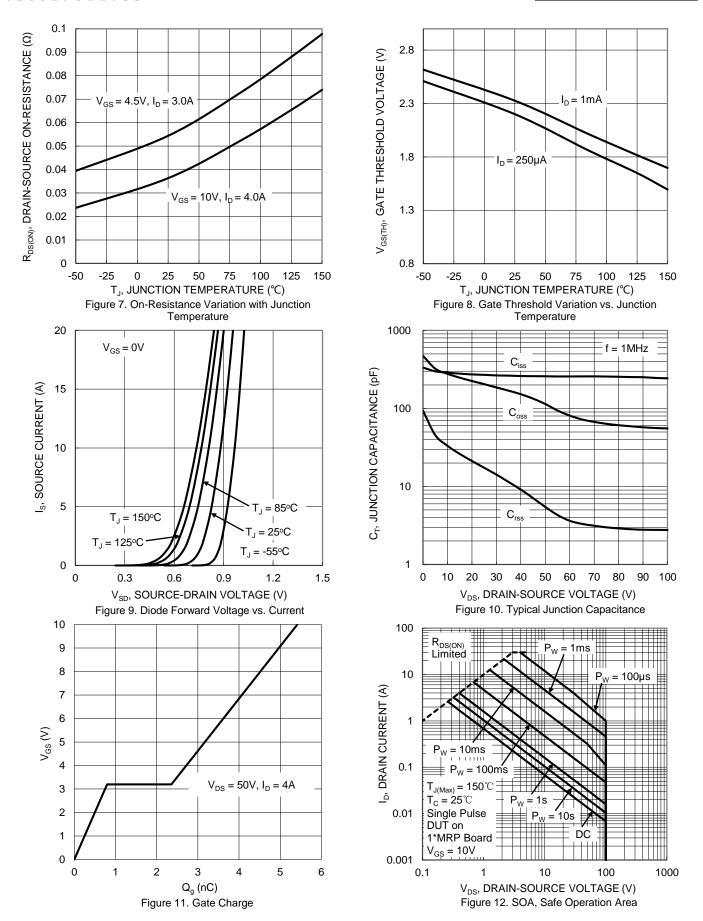
Notes:

<sup>8.</sup> Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.











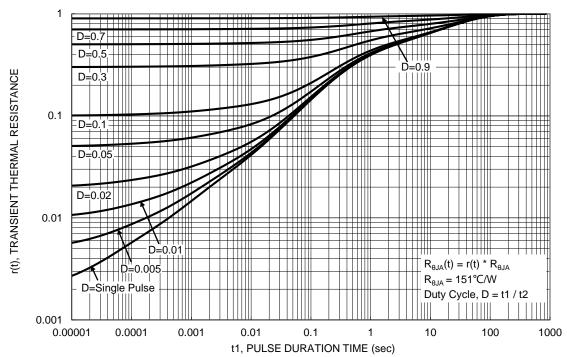


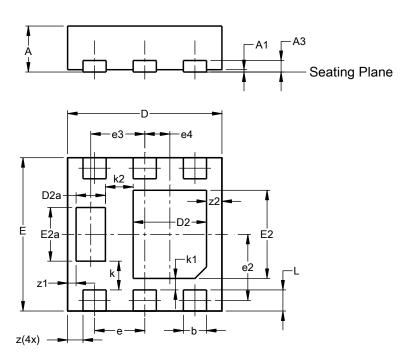
Figure 13. Transient Thermal Resistance



## **Package Outline Dimension**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)

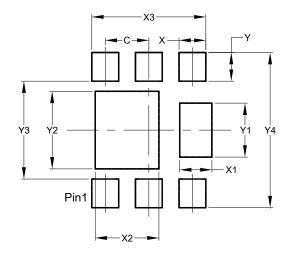


U-DFN2020-6							
(Type F)							
Dim	Min	Max	Тур				
Α	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
А3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
Е	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е		0.65 BSC					
e2	C	).863 BS	SC				
е3		0.70 BS	С				
e4	C	.325 BS	SC				
k		0.37 BS	С				
k1	0.15 BSC						
k2	0.36 BSC						
L	0.225 0.325 0.275						
Z	0.20 BSC						
<b>z</b> 1	0.110 BSC						
z2	0.20 BSC						
All Dimensions in mm							

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)



Dimensions	Value		
Dillielisiolis	(in mm)		
С	0.650		
Х	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Y	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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