

#### SINGLE 2 INPUT POSITIVE NOR GATE

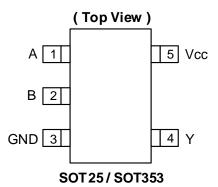
### **Description**

The 74LVC1G02Q is an automotive compliant single 2-input positive NOR gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using loff. The loff circuitry disables the output preventing damaging current backflow when the device is powered down.

The gate performs the positive Boolean function:

$$Y = \overline{A + B}$$
 or  $Y = \overline{A} \cdot \overline{B}$ 

### **Pin Assignments**



#### **Features**

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per AEC-Q100
  - Exceeds 2000V Human Body Model (AEC-Q100-002)
  - Exceeds 1000V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G02Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

### **Applications**

- Voltage Level Shifting
- General Purpose Logic
- Power-Down Signal Isolation
- Wide Array of Products such as:
  - Automotive Applications within Grade 1 Temperature Range
  - Industrial Computing/Controls/Automation
  - High-Reliability Networking/Communications
  - Industrial/Agricultural Equipment

Notes:

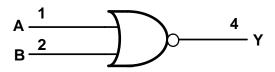
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



# **Pin Descriptions**

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

# **Logic Diagram**



## **Function Table**

Inp	Output	
Α	В	Y
Н	X	L
Х	Н	L
L	L	Н

# Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
Vı	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to Vcc +0.5	V
lıĸ	Input Clamp Current V <sub>I</sub> < 0	-50	mA
lok	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
Icc, Ignd	Continuous current through Vcc or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



# Recommended Operating Conditions (Note 6)

Symbol		Parameter	Min	Max	Unit
\/	Operating Voltage	Operating	1.65	5.5	V
Vcc	Operating voltage	Data Retention Only	1.5	_	V
		Vcc = 1.65V to 1.95V	0.65 × Vcc	_	
V	High-Level Input Voltage	Vcc = 2.3V to 2.7V	1.7	_	V
ViH	High-Level input voltage	$V_{CC} = 3V$ to 3.6V	2	_	V
		Vcc = 4.5V to 5.5V	0.7 × Vcc	_	
		Vcc = 1.65V to 1.95V	_	0.35 × Vcc	
	Laur Laurel Imput Valtage	Vcc = 2.3V to 2.7V	_	0.7	V
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	_	0.8	V
		V <sub>CC</sub> = 4.5V to 5.5V	_	0.3 × V <sub>CC</sub>	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage	Output Voltage		Vcc	V
		V <sub>CC</sub> = 1.65V	_	-4	
		Vcc = 2.3V	_	-8	
la	High Lavel Output Ouges	Vcc = 2.7V	_	-12	mA
Іон	High-Level Output Current	1/ 21/	_	-16	IIIA
		Vcc = 3V	_	-24	
		Vcc = 4.5V	_	-32	
		Vcc = 1.65V	_	4	
		Vcc = 2.3V	_	8	
lou	Low-Level Output Current	V <sub>CC</sub> = 2.7V	_	12	mA
IOL	Low-Level Output Guiterit	Vcc = 3V	_	16	ША
		VCC = 3V	_	24	
		Vcc = 4.5V	_	32	
	Land Tarasitian Disease Fall	$V_{CC} = 1.8V \pm 0.15V$ , $2.5V \pm 0.2V$	_	20	
Δt/ΔV	Input Transition Rise or Fall Rate	$VCC = 3.3V \pm 0.3V$	_	10	ns/V
		$Vcc = 5V \pm 0.5V$		5	
TA	Operating Free-Air Temperature	_	-40	+125	°C

Note:

6. Unused inputs should be held at  $V_{\mbox{\footnotesize CC}}$  or Ground.



## **Electrical Characteristics** (All typical values are at $V_{CC} = 3.3V$ , $T_A = +25$ °C.)

Cumbal	Darameter	Toot Co	Test Conditions		-40°	C to +125	°C	Unit		
Symbol	Parameter	lest Conditions		Vcc	Min	Тур	Max	Onit		
			Іон = -100μΑ	1.65V to 5.5V	Vcc - 0.1	_	_			
			IOH = -4mA	1.65V	0.95	_	_			
Voн	High Lovel Output Voltage	VI = VIH OF VIL	Iон = -8mA	2.3V	1.7	_	_	V		
VOH	High Level Output Voltage	VI = VIH OI VIL	Iон = -12mA	2.7V	1.9	_	_	V		
			Iон = -24mA	3V	2.0	_	_			
			$I_{OH} = -32mA$	4.5V	3.4	_	_			
					I <sub>OL</sub> = 100μA	1.65V to 5.5V	1	_	0.10	
		VI = VIH or VIL	IoL = 4mA	1.65V	_	_	0.70	V		
.,			IoL = 8mA	2.3V	_	_	0.45			
Vol	Low Level Output Voltage		$I_{OL} = 12mA$	2.7V	1	_	0.60			
			I <sub>OL</sub> = 24mA	3V	_	_	0.80			
			IoL = 32mA	4.5V	-	_	0.80			
lı	Input Current	V <sub>I</sub> = 5.5V or GN	ID	0 to 5.5V	_	±0.1	±1	μΑ		
loff	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5V$		0V	_	_	±2	μΑ		
Icc	Supply Current	V <sub>I</sub> = 5.5V or GND Io = 0		5.5V	_	0.1	4	μΑ		
ΔΙσο	Additional Supply Current	One input at V <sub>CC</sub> – 0.6V Other inputs at V <sub>CC</sub> or GND		3V to 5.5V		_	500	μΑ		
Cı	Input Capacitance	$V_I = GND$ to $V_C$	C	3.3V	_	5.0	_	pF		

# **Package Characteristics**

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 7	_	184	1	°C/W
ӨЈА	θ <sub>JA</sub> Junction-to-Ambient	SOT353		_	385	1	
0	Thermal Resistance	SOT25	Note 7		62	1	900
θιс	Junction-to-Case	SOT353	Note 7	_	164	_	°C/W

Note: 7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

# **Switching Characteristics**

Figure 1 Typical Values at  $T_A = +25^{\circ}C$  and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	Vcc	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$			Unit
Input	Input	Output	VCC	Min	Тур	Max	Ollic
		Y	1.8V ± 0.15V	1.0	3.2	10.5	
			2.5V ± 0.2V	0.5	2.2	7.0	
tpD	A or B		2.7V	0.5	2.5	7.0	ns
			$3.3V \pm 0.3V$	0.5	2.1	6.0	
			$5.0V \pm 0.5V$	0.5	1.7	5.5	

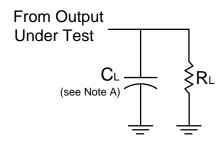
# **Operating Characteristics**

 $T_A = +25^{\circ}C$ 

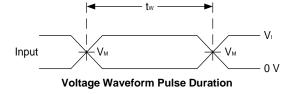
Parameter		Test Conditions	Vcc = 1.8V Typ	Vcc = 2.5V Typ	Vcc = 3.3V Typ	Vcc = 5V Typ	Unit
CPD	Power Dissipation Capacitance	f = 10MHz	14	14	14	14	pF

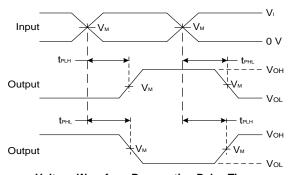


### **Measurement Information**



Vcc	In	puts	V <sub>M</sub>	Cı	R∟	
VCC	Vı	t <sub>R</sub> /t <sub>F</sub>	V IVI	)L	IXL	
1.8V ± 0.15V	Vcc	≤2ns	V <sub>CC</sub> /2	30pF	1kΩ	
2.5V ± 0.2V	Vcc	≤2ns	Vcc/2	30pF	500Ω	
2.7V	Vcc	≤2.5ns	1.5V	50pF	500Ω	
$3.3V \pm 0.3V$	3.0V	≤2.5ns	1.5V	50pF	500Ω	
5.0V ± 0.5V	Vcc	≤2.5ns	Vcc/2	50pF	500Ω	





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

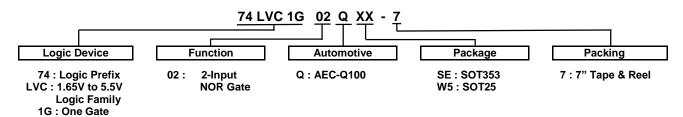
Notes:

A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.

D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .



## Ordering Information (Note 8)



Part Number	Package	Package	ge Package 7" Tape and Reel		and Reel
Fait Nullibei	Code	(Notes 9 & 10)	Size	Quantity	Part Number Suffix
74LVC1G02QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7
74LVC1G02QW5-7	W5	SOT25	3.0mm × 2.8mm × 1.2mm 0.95mm lead pitch	3000/Tape & Reel	-7

Notes:

- 8. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
  9. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 10. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

# **Marking Information**

(Top View)

XXX YWX 2

SOT 25 / SOT 353

XXX: Identification Code

: Year 0~9

: Week: A~Z 1~26 week a~z 27~52 week

z represents week 52 and 53

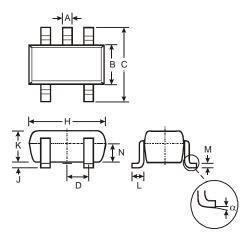
X : A~ Z: Internal Code

Part Number	Package	Identification Code
74LVC1G02QW5-7	SOT25	UTQ
74LVC1G02QSE-7	SOT353	UTQ



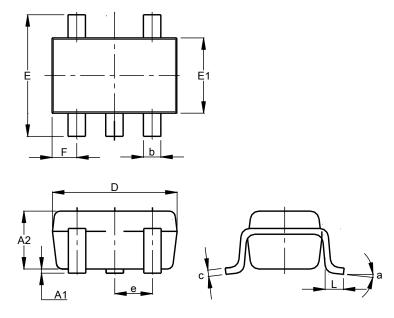
# **Package Outline Dimensions**

#### (1) Package Type: SOT25



	SOT	25				
Dim	Dim Min Max Typ					
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	-	-	0.95			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
M	0.10	0.20	0.15			
N	0.70	0.80	0.75			
α	0°	8°	-			
All D	imensi	ons in	mm			

#### (2) Package Type: SOT353



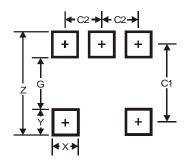
SOT353				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				



# **Suggested Pad Layout**

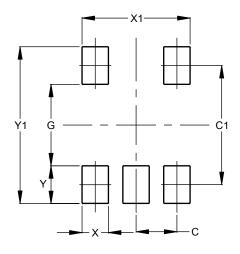
Please see http://www.diodes.com/package-outlines.html for the latest version.

#### (1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Υ	0.80	
C1	2.40	
C2	0.95	

#### (2) Package Type: SOT353



Dimensions	Value (in mm)			
С	0.650			
C1	1.900			
G	1.300			
Х	0.420			
X1	1.720			
Y	0.600			
Y1	2 500			

#### **Mechanical Data**

#### SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.0158 grams (Approximate)

#### **SOT353**

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)



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