



### DDTC (R2-ONLY SERIES) E

Case Material: Molded Plastic, "Green" Molding Compound.

Terminals: Matte Tin Finish. Solderable per MIL-STD-202,

#### NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

Weight: 0.002 grams (Approximate)

**Mechanical Data** 

Case: SOT523

Method 208@3

#### Features

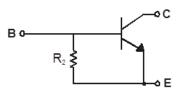
- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R2 Only
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Part Number	R2 (NOM)	Marking
DDTC114GE	10kΩ	N26
DDTC124GE	22kΩ	N27
DDTC144GE	47kΩ	N28
DDTC115GE	100kΩ	N29

SOT523







Schematic Diagram

#### Ordering Information (Note 4)

Part Number	Compliance	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel	
DDTC114GE-7-F	AEC-Q101	7	8	3,000	
DDTC124GE-7-F	AEC-Q101	7	8	3,000	
DDTC144GE-7-F	AEC-Q101	7	8	3,000	
DDTC115GE-7-F	AEC-Q101	7	8	3,000	
lotes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS). 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



Nxx = Product Type Marking Code (See Table in Features) YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: F = 2018)

M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Key												
Year	2018	2019	20	20	2021	2022	2023	2024	20	25	2026	2027
Code	F	G	ŀ	1		J	K	L	Ν	A	Ν	0
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# DDTC (R2-ONLY SERIES) E

# Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Collector Current	I <sub>C</sub> (Max)	100	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	PD	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ ext{ heta}JA}$	833	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

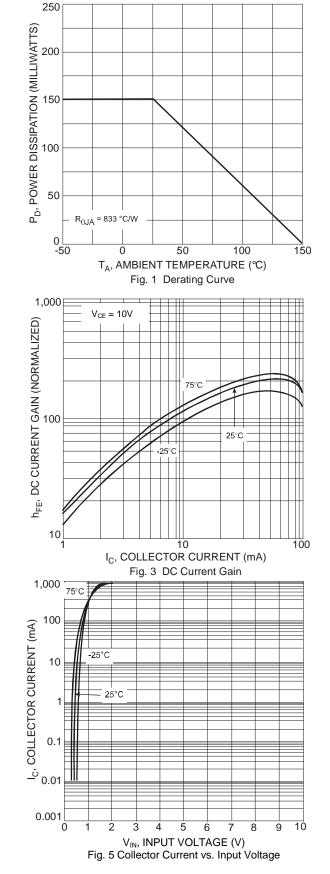
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

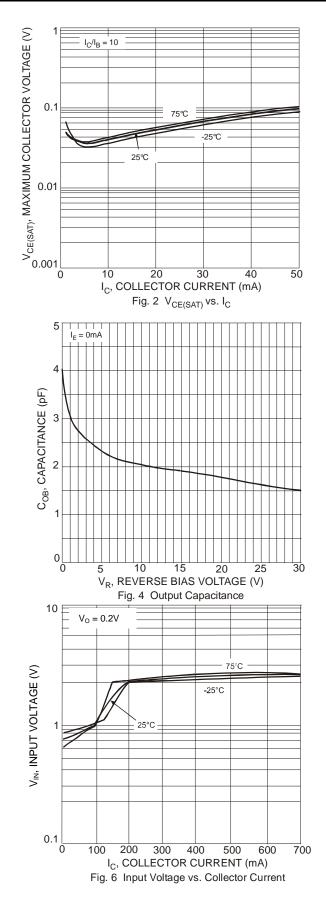
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV <sub>CBO</sub>	50	_	_	V	Ι <sub>C</sub> = 50μΑ
Collector-Emitter Breakdown Vo	ltage	BV <sub>CEO</sub>	50	_	_	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage		BV <sub>EBO</sub>	5			V	I <sub>E</sub> = 720μA, DDTC114GE I <sub>E</sub> = 330μA, DDTC124GE I <sub>E</sub> = 160μA, DDTC144GE I <sub>E</sub> = 72μA, DDTC115GE
Collector Cutoff Current		I <sub>CBO</sub>	_	_	0.5	μA	$V_{CB} = 50V$
Emitter Cutoff Current	DDTC114GE DDTC124GE DDTC144GE DDTC115GE	I <sub>EBO</sub>	300 140 65 30	_	580 260 130 58	μΑ	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Vol	tage	V <sub>CE(SAT)</sub>	_	_	0.3	V	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0.5 {\rm mA}$
DC Current Transfer Ratio	DDTC114GE DDTC124GE DDTC144GE DDTC115GE	h <sub>FE</sub>	30 56 68 82			_	I <sub>C</sub> = 5mA, V <sub>CE</sub> = 5V
Bleeder Resistor (R <sub>2</sub> ) Tolerance		$\Delta R_2$	-30		+30	%	
Gain-Bandwidth Product (Note 6)		f <sub>T</sub>	_	250		MHz	$V_{CE} = 10V, I_E = -5mA, f = 100MHz$

 Mounted on FR-4 PC Board with minimum recommended pad layout.
Transistor only. Notes:



### Typical Curves – DDTC114GE



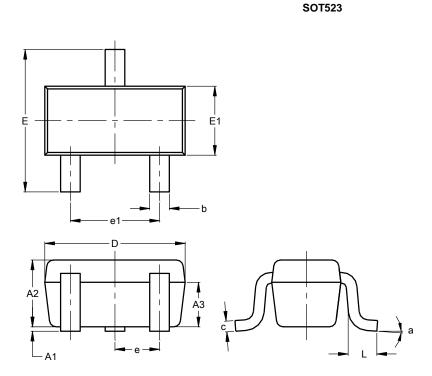


DDTC (R2-ONLY SERIES) E Document number: DS30316 Rev. 9 - 2



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

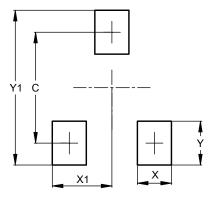


SOT523							
Dim	Min Max Typ						
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
c	0.10	0.20	0.12				
D	1.50	1.70	1.60				
ш	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
e		0.50 BS	С				
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
Α	All Dimensions in mm						

### Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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