



74AHC1G125Q

SINGLE BUFFER GATE WITH 3-STATE OUTPUT

### Description

The 74AHC1G125Q is an automotive compliant single non-inverting buffer/bus driver with a 3-state output. The output enters a high impedance state when a HIGH-level is applied to the output enable  $(\overline{\text{OE}})$  pin. The device is designed for operation with a power supply range of 2.0V to 5.5V.

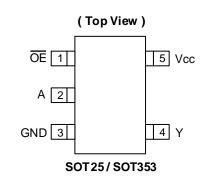
### **Pin Assignments**

Applications

General Purpose Logic

Range

Wide Array of Products, such as:



Automotive Applications within Grade 1 Temperature

Industrial Computing/Controls/Automation High Reliability Networking/Communications

Industrial/Agricultural Equipment

### **Features**

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- ±8mA Output Drive at 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs Not Limited by V<sub>CC</sub>
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G125Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

#### Notes:

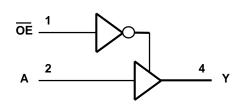
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



### **Pin Descriptions**

Pin Name	Description
ŌĒ	Output Enable
А	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

### Logic Diagram



### **Function Table**

Inp	Output	
OE	Α	Y
L	Н	Н
L	L	L
Н	Х	Z

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V <sub>CC</sub> + 0.5	V
Ік	Input Clamp Current VI < 0	-20	mA
loк	Output Clamp Current ( $V_O < 0$ or $V_O > V_{CC}$ )	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
Icc	Continuous Current Through Vcc	75	mA
Ignd	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
TSTG	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

## 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should

Stresses beyond the absolute maximum can result in minimum the result of reserve reserves reserves to a second the maximum current of conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

6. This will need to be derated at higher operating temperatures to prevent exceeding maximum T<sub>J</sub>. Refer to package thermal characteristics section.

Notes:



### Recommended Operating Conditions (Note 7)

Symbol	Р	arameter	Min	Max	Unit	
Vcc	Operating Voltage	—	2	5.5	V	
		V <sub>CC</sub> = 2V	1.5	_		
VIH	High-Level Input Voltage	$V_{CC} = 3V$	2.1	_	V	
		Vcc = 5.5V	3.85	_		
		$V_{CC} = 2V$	—	0.5		
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 3V	_	0.9	V	
		$V_{CC} = 5.5 V$	_	1.65		
Vı	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		Vcc = 2V	—	-50	μA	
Iон	High-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	-4		
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA	
		$V_{CC} = 2V$	_	50	μA	
lol	Low-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	4		
		$V_{CC} = 5V \pm 0.5V$	_	8	mA	
		$V_{CC} = 3.3V \pm 0.3V$	_	100		
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 5V \pm 0.5V$	—	20	ns/V	
TA	Operating Free-Air Temperature	_	-40	+125	°C	

Note: 7. Unused inputs should be held at  $V_{CC}$  or Ground.

### **Electrical Characteristics** (All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = +25°C.)

	-	<b>T</b> ( <b>0</b> )			+25°C		-40°C te	o +85°C	-40°C to	+125°C				
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit			
		VI = VIH or VIL	2V	1.9	2	_	1.9	—	1.9	—				
		vī = vīн or vī∟ I <sub>OH</sub> = -50µА	3V	2.9	3	_	2.9	—	2.9	—				
	High Level		4.5V	4.4	4.5	_	4.4	—	4.4	—				
Vон	Output Voltage	VI = VIH or VIL IOH = -4mA	3V	2.58			2.48	—	2.40	—	V			
					Vı = Viн or Vil Іон = -8mA	4.5V	3.94			3.8	—	3.70	—	
		VI = VIH or VIL I <sub>OL</sub> = 50µA	2V	-		0.1	—	0.1		0.1				
			3V		_	0.1	—	0.1	_	0.1				
			4.5V	_	_	0.1	—	0.1	_	0.1				
V <sub>OL</sub>	Voltage		VI = VIH or VIL IOL = 4mA	3V	_	_	0.36	_	0.44	_	0.55	V		
							VI = VIH or VIL IOL = 8mA	4.5V			0.36	_	0.44	-
loz		$V_1 = 5.5V \text{ or GND}$ $V_0 = 0 \text{ to } 5.5V$	5.5V			0.25	_	2.5		10	μA			
lı	Input Current	$V_1 = 5.5V$ or GND	0 to 5.5V			±0.1		±1		±2	μA			
Icc	Supply Current	VI = 5.5V or GND Io = 0	5.5V	_	_	1	_	10	_	40	μΑ			
Cı	Input Capacitance	$V_I = V_{CC}$ or GND	5.5V	_	2.0	10	_	10	_	10	pF			



### **Package Characteristics**

Symbol	Parameter	Package	Test Conditions	Min	Тур	Мах	Unit
0	Thermal Resistance	SOT25	Note 0		184	_	°C/W
θја	Junction-to-Ambient	SOT353	Note 8		385	_	
0	Thermal Resistance	SOT25	Note O		62	—	
θις	Junction-to-Case	SOT353	Note 8		164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

### **Switching Characteristics**

$Vcc = 3.3V \pm 0$	/cc = 3.3V ± 0.3V (See Figure 1)										
Parameter ,	From	То	Test		+25°C		-40°C t	o +85°C	-40°C to	o +125℃	Unit
Farameter	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	Unit
<b>4</b>		V	C∟ = 15pF	1.0	4.7	8.0	1.0	9.5	1.0	11.5	ns
t <sub>PD</sub>	A	T	C <sub>L</sub> = 50pF	1.0	6.6	11.5	1.0	13.0	1.0	14.5	ns
<b>4</b>	5	V	C∟ = 15pF	1.0	5.0	8.0	1.0	9.5	1.0	11.5	ns
t <sub>EN</sub>	OE	T	C <sub>L</sub> = 50pF	1.0	6.9	11.5	1.0	13.0	1.0	14.5	ns
tava	5	V	$C_L = 15 pF$	1.0	6.0	9.7	1.0	11.5	1.0	12.5	ns
tDIS	OE	ř	$C_L = 50 pF$	1.0	8.3	13.2	1.0	15.0	1.0	16.5	ns

#### Vcc = 5V ± 0.5V (See Figure 1)

Persmeter From		From To		+25°C		-40°C to +85°C		-40°C to	o +125°C	Unit	
Parameter	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	Unit
4		V	C <sub>L</sub> = 15pF	1.0	3.4	5.5	1.0	6.5	1.0	7.0	ns
tPD	A	ř	$C_L = 50 pF$	1.0	4.8	7.5	1.0	8.5	1.0	9.5	ns
4		V	$C_L = 15 pF$	1.0	3.6	5.1	1.0	6.0	1.0	6.5	ns
ten	OE	r	$C_L = 50 pF$	1.0	4.9	7.5	1.0	8.5	1.0	9.5	ns
4		V	$C_L = 15 pF$	1.0	4.1	6.8	1.0	8.0	1.0	8.5	ns
tDIS	OE	r	$C_L = 50 pF$	1.0	5.7	8.8	1.0	10.0	1.0	11.0	ns

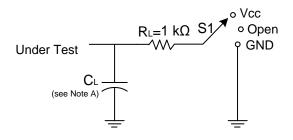
### **Operating Characteristics**

T<sub>A</sub> = +25°C

Parameter		Test Conditions	Vcc = 5V Typ	Unit
Cpd	Power Dissipation Capacitance	f = 1MHz No Load	10	pF

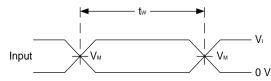


### **Measurement Information**

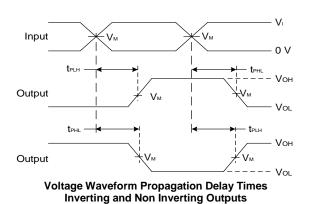


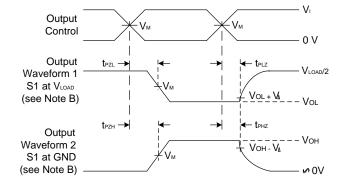
Test	S1
t <sub>PLH</sub> /t <sub>PHL</sub>	Open
tplz/tpzl	VLOAD
tphz/tpzh	GND

N	In	puts	Mar	0	N/A	
Vcc	VI	t <sub>R</sub> /t <sub>F</sub>	VM	C∟	$\mathbf{V}\Delta$	
3.3V±0.3V	Vcc	≤3ns	V <sub>CC</sub> /2	15pF	0.3V	
5V±0.5V	Vcc	≤3ns	V <sub>CC</sub> /2	15pF	0.3V	
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	50pF	0.3V	
5V±0.5V	Vcc	≤3ns	V <sub>CC</sub> /2	50pF	0.3V	



**Voltage Waveform Pulse Duration** 





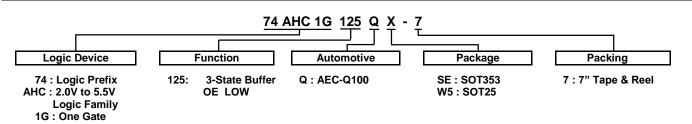
#### Voltage Waveform Enable and Disable Times Low and High Level Enabling

#### Figure 1. Load Circuit and Voltage Waveforms

- Notes: A. Includes test lead and test apparatus capacitance.
  - B. All pulses are supplied at pulse repetition rate  $\leq$  1MHz.
  - C. Inputs are measured separately one transition per measurement.
  - D.  $t_{\text{PLZ}}$  and  $t_{\text{PHZ}}$  are the same as  $t_{\text{DIS}}.$
  - E.  $t_{\mathsf{PZL}}$  and  $t_{\mathsf{PZH}}$  are the same as  $t_{\mathsf{EN}}.$
  - F.  $t_{\mathsf{PLH}}$  and  $t_{\mathsf{PHL}}$  are the same as  $t_{\mathsf{PD}}.$



### Ordering Information (Note 9)

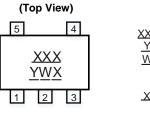


Part Number	Package	Package	Package Size	7" Tape and Reel		
Fait Number	Code	(Notes 10 & 11)	Fackage Size	Quantity	Part Number Suffix	
74AHC1G125QSE-7	SE	SOT353	2.15mm × $2.1$ mm × $1.1$ mm 0.65mm lead pitch	3000/Tape & Reel	-7	
74AHC1G125QW5-7	W5	SOT25	3.0mm × 2.8mm × 1.2mm 0.95mm lead pitch	3000/Tape & Reel	-7	

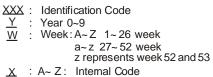
Notes:

9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

### **Marking Information**



SOT 25 / SOT 353



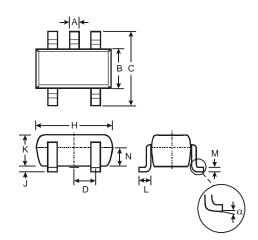
Part Number	Package	Identification Code
74AHC1G125QW5-7	SOT25	YYQ
74AHC1G125QSE-7	SOT353	YYQ



### **Package Outline Dimensions**

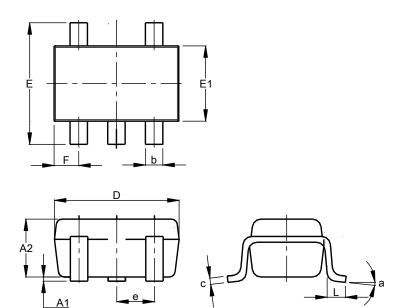
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



SOT25				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	-	0.95	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
К	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
Ν	0.70	0.80	0.75	
α	0°	8°	-	
All Dimensions in mm				

#### (2) Package Type: SOT353



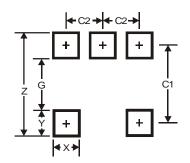
	SOT353				
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
E	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



### **Suggested Pad Layout**

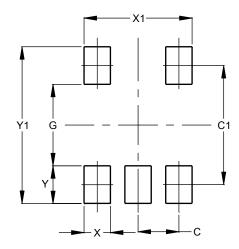
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

#### (2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Ý	0.600
Y1	2.500

### **Mechanical Data**

#### SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0158 grams (Approximate)

#### SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0064 grams (Approximate)



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