



60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
60V	$10m\Omega$ @ $V_{GS} = 10V$	89.5A
60 V	13.5mΩ @ V_{GS} = 4.5V	77A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

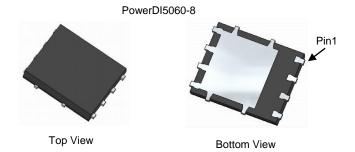
- High Frequency Switching
- Sync. Rectification
- DCDC Converters

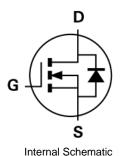
Features

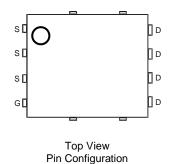
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_G Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.097 grams (Approximate)







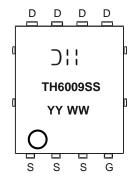
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH6009SPS-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);; = Manufacturer's Marking
TH6009SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 18 = 2018)
WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25$ °C $T_A = +100$ °C	I _D	12.9 9.1	А
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	89.5 63.3	А
Maximum Continuous Body Diode Forward Current (Note 6)		I _S	80	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	350	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I _{SM}	350	Α
Avalanche Current, L = 0.1mH		I _{AS}	20.3	Α
Avalanche Energy, L = 0.1mH		E _{AS}	20.6	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25$ °C	P _D	2.8	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	53	°C/W
Total Power Dissipation (Note 6)	$T_{C} = +25^{\circ}C$	P_D	136	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	1.1	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

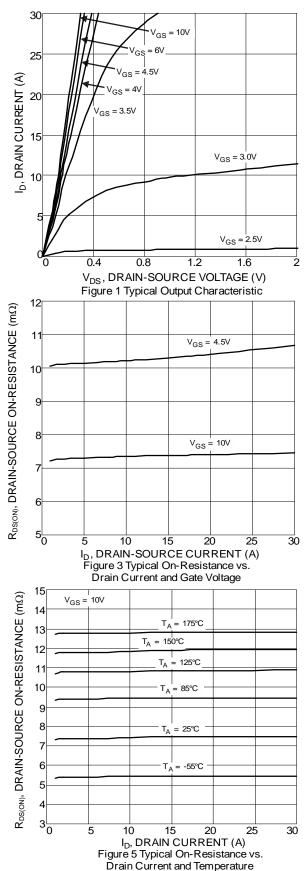
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	1.2	_	2.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	7.6	10	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	10.7	13.5	11122	$V_{GS} = 4.5V, I_D = 15A$	
Diode Forward Voltage	V _{SD}	_	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)		•	•	•	•	•	
Input Capacitance	C _{iss}		1572			$V_{DS} = 30V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss	_	490	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	50	_			
Gate Resistance	Rg	_	1.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_{G}	_	29.3	_			
Total Gate Charge (V _{GS} = 4.5V)	Q _G	_	13.9	_	nC	N/ 00V/ 1 40.5A	
Gate-Source Charge	Q _{GS}	_	4.3	_	IIC	$V_{DS} = 30V, I_{D} = 13.5A$	
Gate-Drain Charge	Q_{GD}	_	5.9	_			
Turn-On Delay Time	t _{D(ON)}	_	5.4	_		$V_{DD} = 30V, V_{GS} = 10V,$ $R_G = 6\Omega, I_D = 13.5A$	
Turn-On Rise Time	t _R	_	9.8	_			
Turn-Off Delay Time	t _{D(OFF)}	_	28.1	_	ns		
Turn-Off Fall Time	t _F	_	16.3	_			
Body Diode Reverse Recovery Time	t _{RR}	_	17.2	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	25.1	_	nC	$I_F = 13.5A$, di/dt = 400A/ μ s	

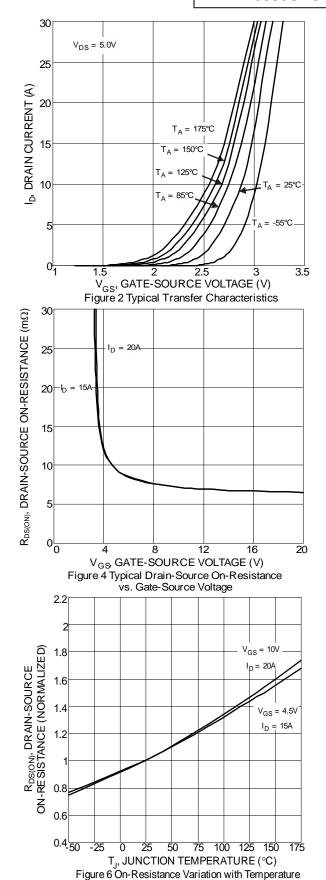
Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



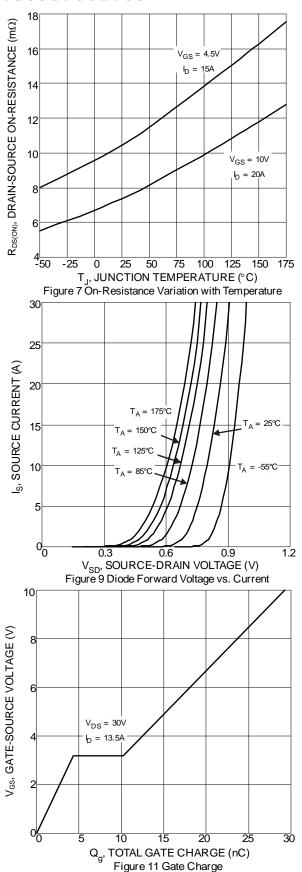


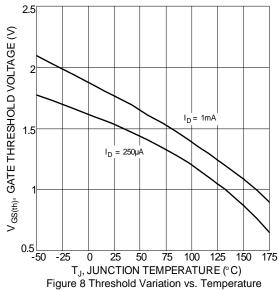


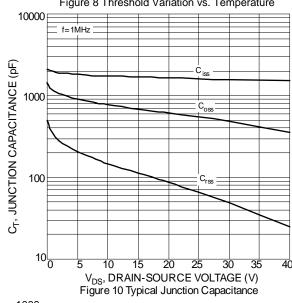


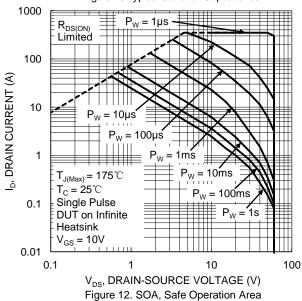














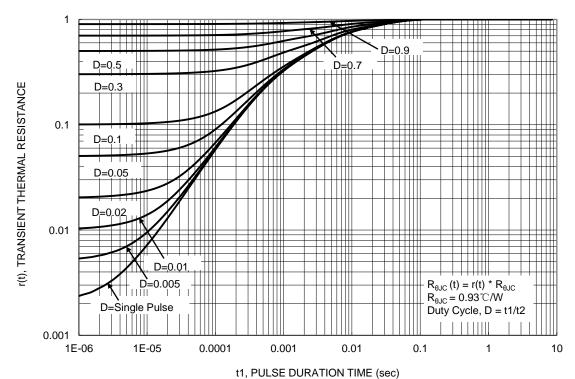


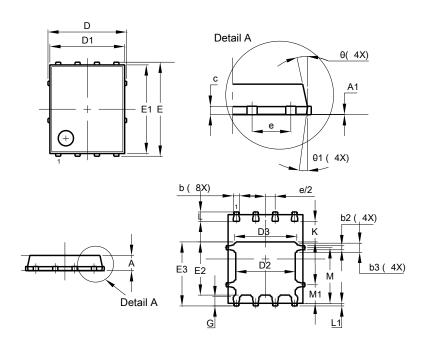
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

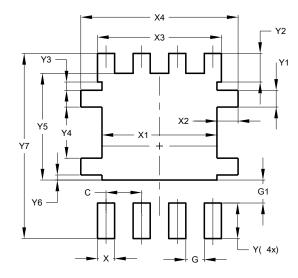


PowerDI5060-8					
Dim	Min	Тур			
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	,	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(6.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	_	_		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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